

## 英飞凌汽车级80V 175°C N沟道

### 增强型OptiMOS™ 5 功率晶体管



#### 产品概述

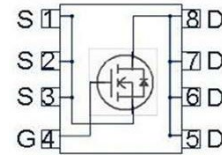
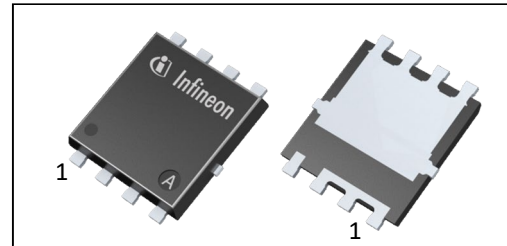
|                  |      |    |
|------------------|------|----|
| $V_{DS}$         | 80   | V  |
| $R_{DS(on),max}$ | 10.2 | mΩ |
| $I_D$            | 50   | A  |

#### 特点

- 适用于汽车应用的功率MOSFET
- N 沟道 - 增强型 - 标准电平
- MSL1 回流焊峰值温度高达 260°C
- 工作温度 175°C
- 绿色产品：符合 RoHS 标准
- 100% 雪崩测试

| Type            | Package                       | Marking |
|-----------------|-------------------------------|---------|
| IAUC50N08S5N102 | <a href="#">PG-TDSON-8-33</a> | 5N08102 |

#### PG-TDSON-8-33



除非另有规定，否则均为 $T_j=25^\circ\text{C}$ 的**最大额定值**。

| Parameter                                    | Symbol         | Conditions                                                                         | Value        | Unit |
|----------------------------------------------|----------------|------------------------------------------------------------------------------------|--------------|------|
| Drain current                                | $I_D$          | $V_{GS}=10\text{ V}$ , Chip limitation <sup>1,2)</sup>                             | 50           | A    |
|                                              |                | $V_{GS}=10\text{ V}$ , DC current                                                  | 50           |      |
|                                              |                | $T_a=85^\circ\text{C}$ , $V_{GS}=10\text{ V}$ , $R_{thJA}$ on 2s2p <sup>2,3)</sup> | 12           |      |
| Pulsed drain current <sup>2)</sup>           | $I_{D,pulse}$  | $T_c=25^\circ\text{C}$                                                             | 200          |      |
| Avalanche energy, single pulse <sup>2)</sup> | $E_{AS}$       | $I_D=29\text{ A}$                                                                  | 45           | mJ   |
| Avalanche current, single pulse              | $I_{AS}$       | -                                                                                  | 29           | A    |
| Gate source voltage                          | $V_{GS}$       | -                                                                                  | $\pm 20$     | V    |
| Power dissipation                            | $P_{tot}$      | $T_c=25^\circ\text{C}$                                                             | 60           | W    |
| Operating and storage temperature            | $T_j, T_{stg}$ | -                                                                                  | -55 ... +175 | °C   |

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| Parameter | Symbol | Conditions | Values |      |      | Unit |
|-----------|--------|------------|--------|------|------|------|
|           |        |            | min.   | typ. | max. |      |

### 热特性<sup>2)</sup>

|                                                      |            |   |   |      |     |     |
|------------------------------------------------------|------------|---|---|------|-----|-----|
| Thermal resistance, junction - case                  | $R_{thJC}$ | - | - | -    | 2.5 | K/W |
| Thermal resistance, junction - ambient <sup>4)</sup> | $R_{thJA}$ | - | - | 24.9 | -   |     |

除非另有规定，否则均为 $T_j=25^\circ\text{C}$ 的电气特性。

### 静态参数特性

|                                  |               |                                                    |     |      |      |            |
|----------------------------------|---------------|----------------------------------------------------|-----|------|------|------------|
| Drain-source breakdown voltage   | $V_{(BR)DSS}$ | $V_{GS}=0V, I_D=1mA$                               | 80  | -    | -    | V          |
| Gate threshold voltage           | $V_{GS(th)}$  | $V_{DS}=V_{GS}, I_D=24\mu A$                       | 2.2 | 3.0  | 3.8  |            |
| Zero gate voltage drain current  | $I_{DSS}$     | $V_{DS}=80V, V_{GS}=0V, T_j=25^\circ\text{C}$      | -   | 0.1  | 1    | $\mu A$    |
|                                  |               | $V_{DS}=80V, V_{GS}=0V, T_j=85^\circ\text{C}^{2)}$ | -   | 1    | 20   |            |
| Gate-source leakage current      | $I_{GSS}$     | $V_{GS}=20V, V_{DS}=0V$                            | -   | -    | 100  | nA         |
| Drain-source on-state resistance | $R_{DS(on)}$  | $V_{GS}=6V, I_D=12.5A$                             | -   | 12.3 | 15.8 | m $\Omega$ |
|                                  |               | $V_{GS}=10V, I_D=25A$                              | -   | 8.4  | 10.2 |            |
| Gate resistance <sup>2)</sup>    | $R_G$         | -                                                  | -   | 1.1  | -    | $\Omega$   |

| Parameter | Symbol | Conditions | Values |      |      | Unit |
|-----------|--------|------------|--------|------|------|------|
|           |        |            | min.   | typ. | max. |      |

#### 动态参数特性<sup>2)</sup>

|                              |              |                                                              |   |      |      |    |
|------------------------------|--------------|--------------------------------------------------------------|---|------|------|----|
| Input capacitance            | $C_{iss}$    | $V_{GS}=0V, V_{DS}=40V,$<br>$f=1MHz$                         | - | 1072 | 1394 | pF |
| Output capacitance           | $C_{oss}$    |                                                              | - | 204  | 265  |    |
| Reverse transfer capacitance | $C_{rss}$    |                                                              | - | 13   | 20   |    |
| Turn-on delay time           | $t_{d(on)}$  | $V_{DD}=40V, V_{GS}=10V,$<br>$I_D=25A, R_{G,ext}=3.5 \Omega$ | - | 4    | -    | ns |
| Turn-off delay time          | $t_{d(off)}$ |                                                              | - | 6    | -    |    |
| Rise time                    | $t_r$        |                                                              | - | 2    | -    |    |
| Fall time                    | $t_f$        |                                                              | - | 4    | -    |    |

#### 栅极电荷特性<sup>2)</sup>

|                       |               |                                                      |   |     |    |    |
|-----------------------|---------------|------------------------------------------------------|---|-----|----|----|
| Gate to source charge | $Q_{gs}$      | $V_{DD}=40V, I_D=25A,$<br>$V_{GS}=0 \text{ to } 10V$ | - | 5   | 7  | nC |
| Gate to drain charge  | $Q_{gd}$      |                                                      | - | 4   | 6  |    |
| Gate charge total     | $Q_g$         |                                                      | - | 16  | 21 |    |
| Gate plateau voltage  | $V_{plateau}$ |                                                      | - | 5.0 | -  | V  |

#### 反向二极管

|                                                |               |                                           |   |     |     |    |
|------------------------------------------------|---------------|-------------------------------------------|---|-----|-----|----|
| Diode continuous forward current <sup>2)</sup> | $I_S$         | $T_C=25^\circ C$                          | - | -   | 50  | A  |
| Diode pulse current <sup>2)</sup>              | $I_{S,pulse}$ | $T_C=25^\circ C$                          | - | -   | 200 |    |
| Diode forward voltage                          | $V_{SD}$      | $V_{GS}=0V, I_F=25A,$<br>$T_J=25^\circ C$ | - | 0.9 | 1.2 | V  |
| Reverse recovery time <sup>2)</sup>            | $t_{rr}$      | $V_R=40V, I_F=50A,$                       | - | 32  | -   | ns |
| Reverse recovery charge <sup>2)</sup>          | $Q_{rr}$      | $di_F/dt=100A/\mu s$                      | - | 25  | -   |    |

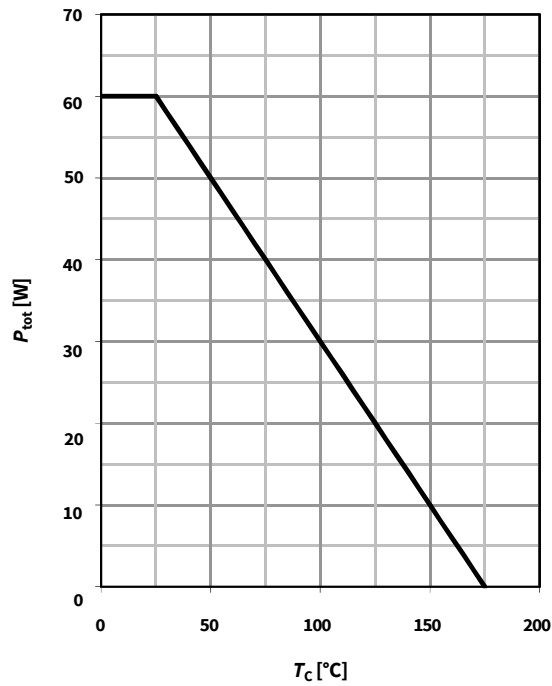
<sup>1)</sup> 实际上，电流能力受到包括客户特定的印刷电路板在内的整体系统设计的限制。

<sup>2)</sup> 这些参数不经过生产测试，但经过设计和/或特性验证。

<sup>3)</sup> 器件放置于根据JEDEC标准 (JESD51-5, -7) 定义的 2s2p FR4 印刷电路板上。印刷电路板垂直放置在静止空气中。

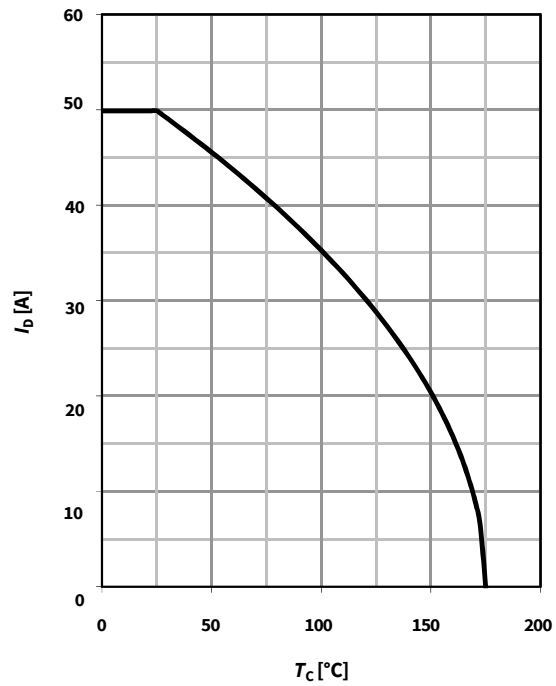
### 1 功率耗散

$$P_{\text{tot}} = f(T_c); V_{\text{GS}} = 10 \text{ V}$$



### 2 漏极电流

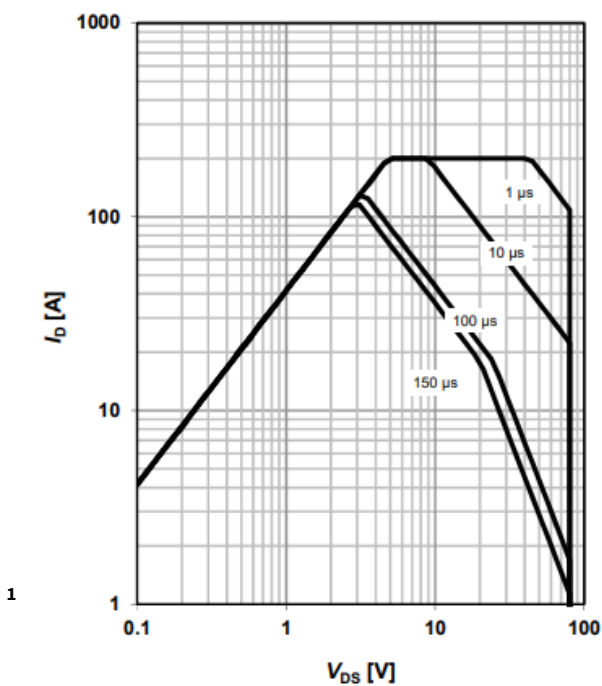
$$I_D = f(T_c); V_{\text{GS}} = 10 \text{ V}$$



### 3 安全工作区

$$I_D = f(V_{\text{DS}}); T_c = 25^\circ\text{C}; D = 0$$

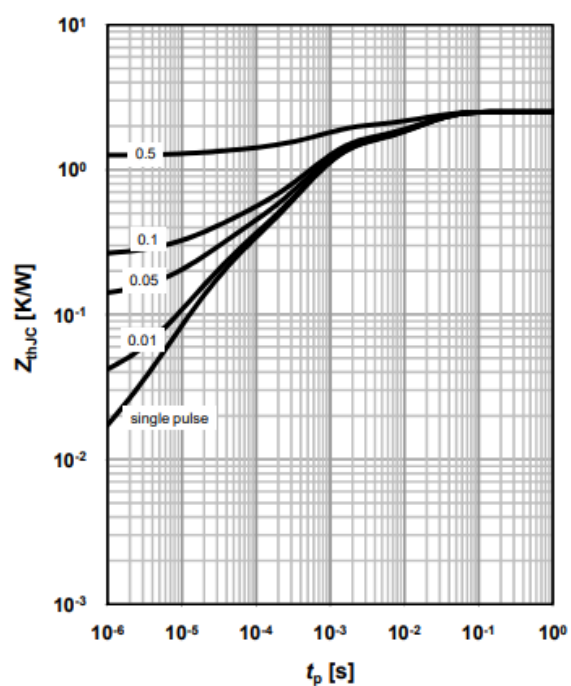
parameter:  $t_p$



### 4 最大瞬态热阻抗

$$Z_{\text{thJC}} = f(t_p)$$

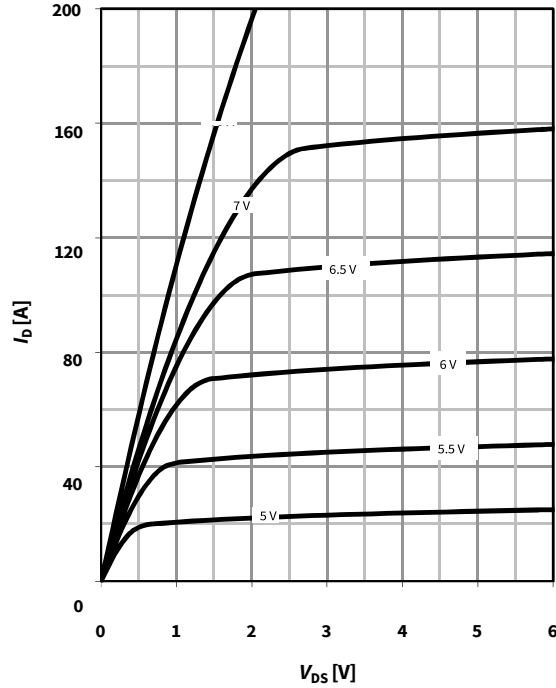
parameter:  $D = t_p/T$



## 5 典型输出特性

$$I_D = f(V_{DS}); T_J = 25^\circ\text{C}$$

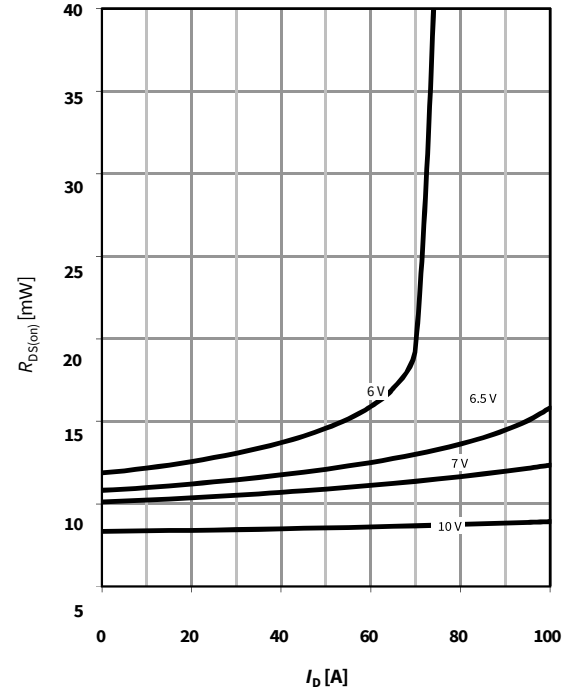
parameter:  $V_{GS}$



## 6 典型漏源导通电阻

$$R_{DS(on)} = f(I_D); T_J = 25^\circ\text{C}$$

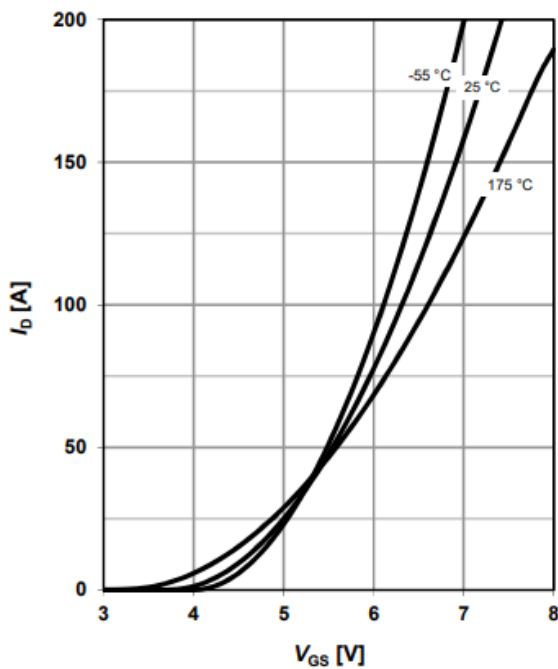
parameter:  $V_{GS}$



## 7 典型转移特性

$$I_D = f(V_{GS}); V_{DS} = 6V$$

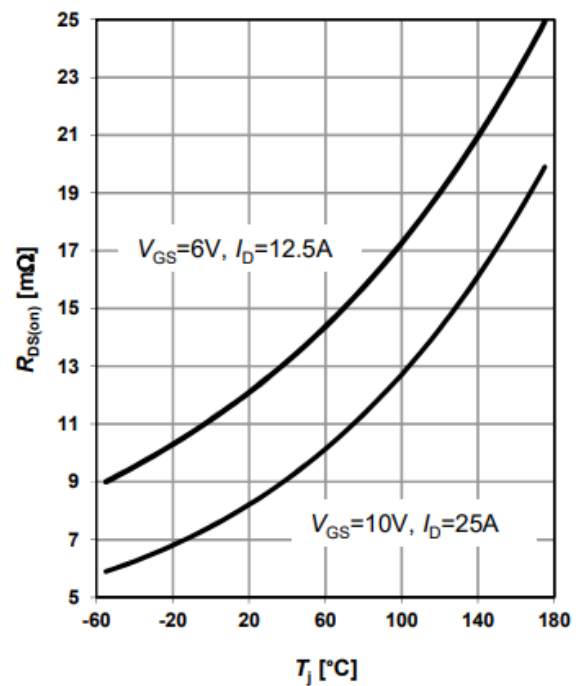
parameter:  $T_J$



## 8 典型漏源导通电阻

$$R_{DS(on)} = f(T_J);$$

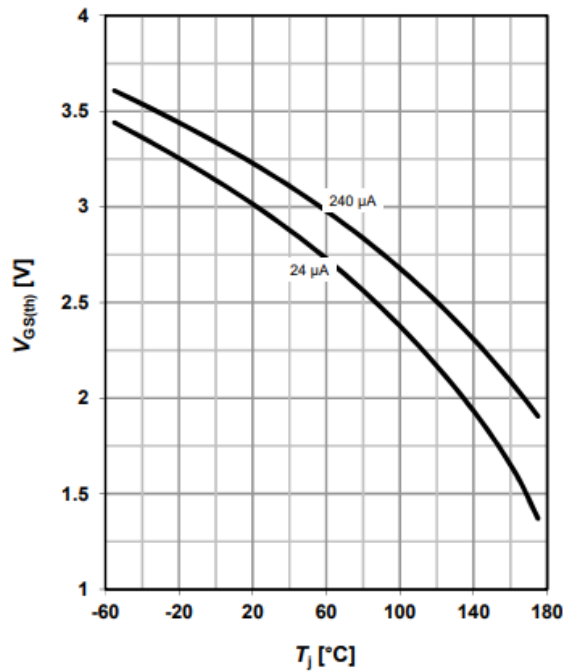
parameter:  $I_D, V_{GS}$



# 9 典型栅极阈值电压

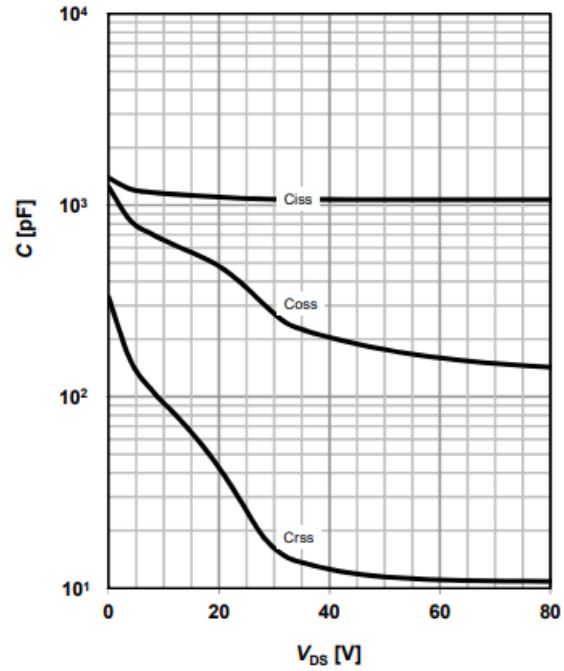
$$V_{GS(th)} = f(T_j); V_{GS} = V_{DS}$$

parameter:  $I_D$



# 10 典型电容值

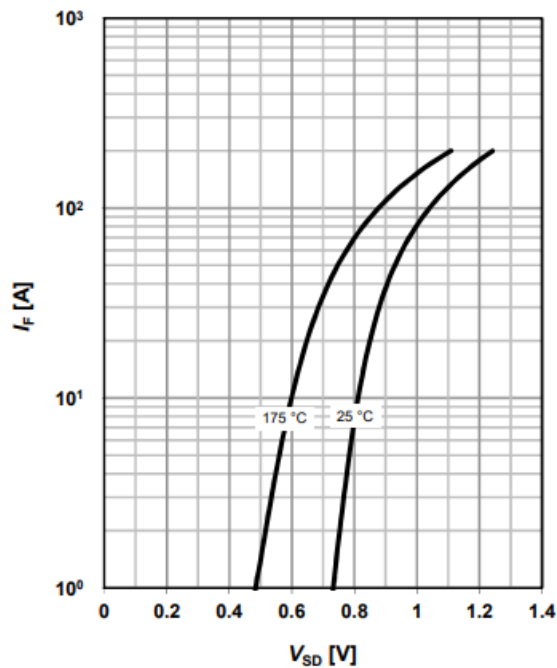
$$C = f(V_{DS}); V_{GS} = 0 V; f = 1 MHz$$



# 11 典型二极管正向导通特性

$$I_F = f(V_{SD})$$

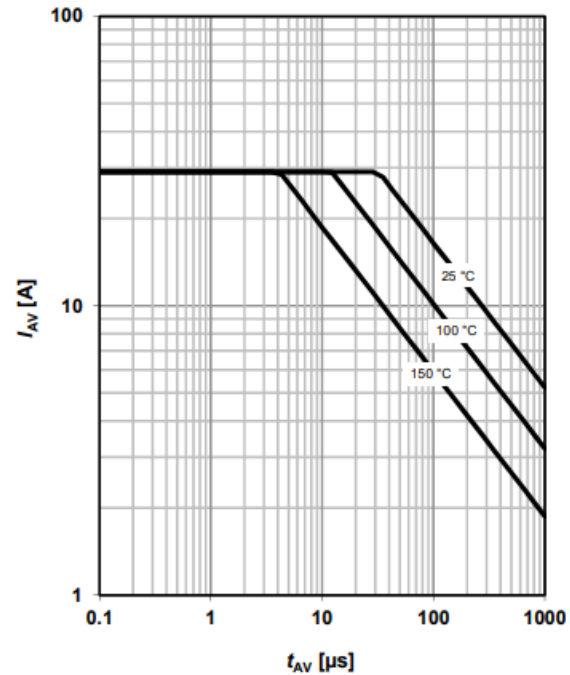
parameter:  $T_j$



# 12 典型雪崩特性

$$I_{AS} = f(t_{AV})$$

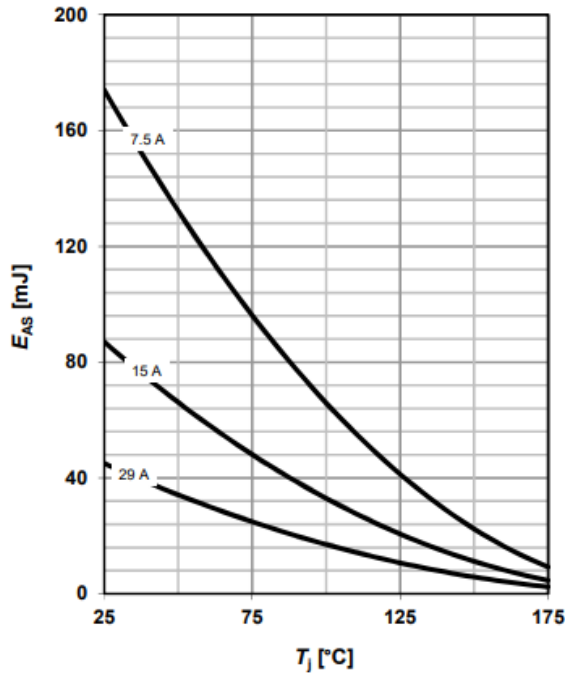
parameter:  $T_{j(start)}$



### 13 典型雪崩能量

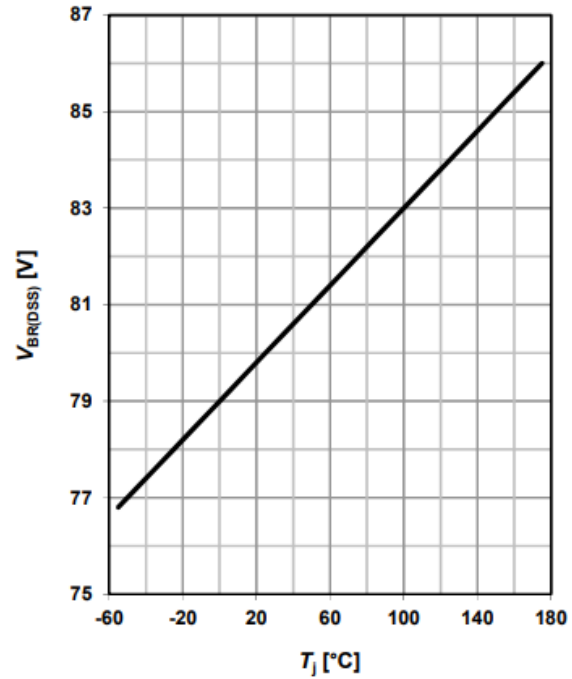
$$E_{AS} = f(T_J)$$

parameter:  $I_D$



### 14 漏源击穿电压

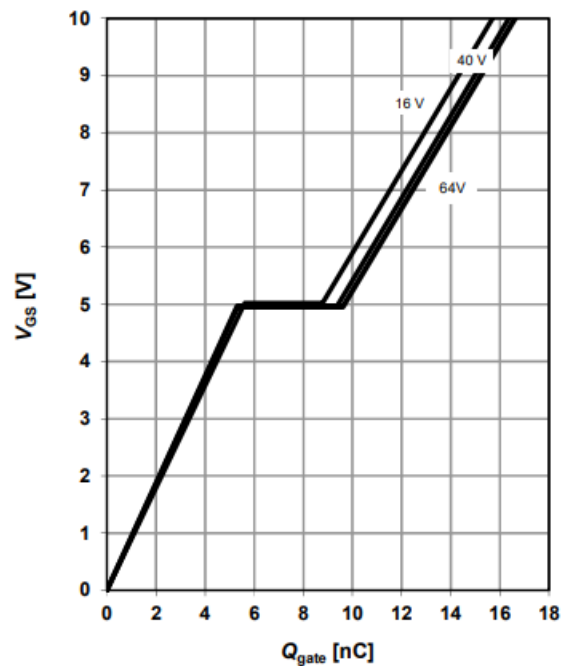
$$V_{BR(DSS)} = f(T_J); I_D = 1 \text{ mA}$$



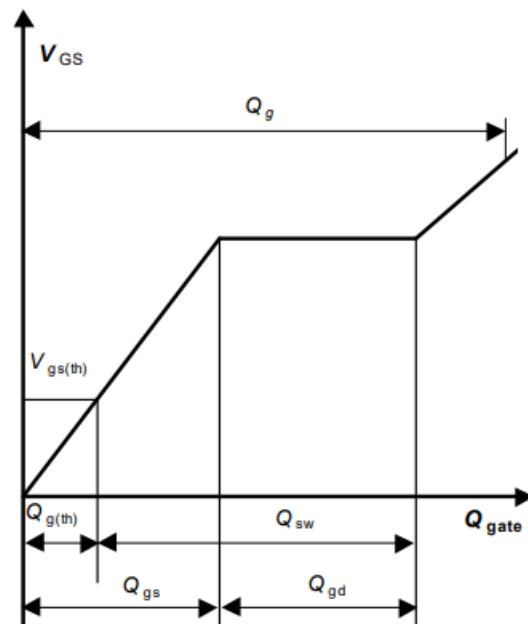
### 15 典型栅极电荷

$$V_{GS} = f(Q_{gate}); I_D = 25 \text{ A pulsed}$$

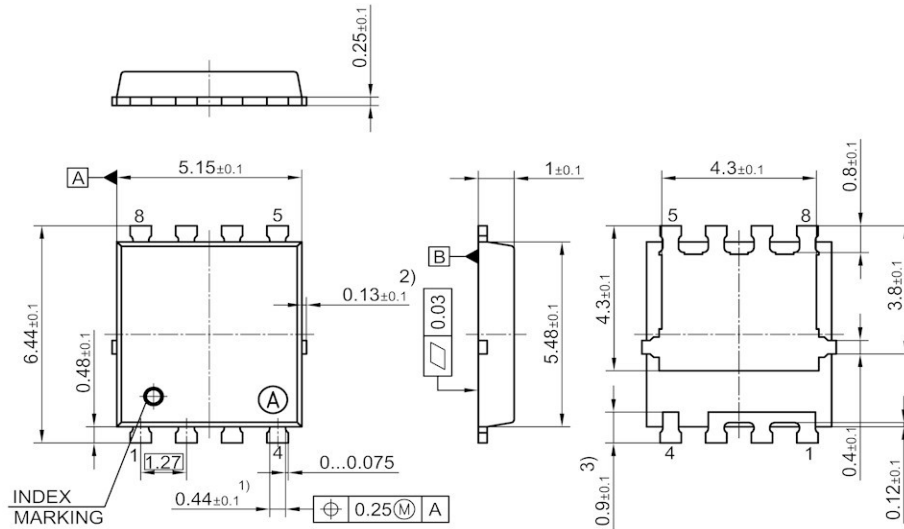
parameter:  $V_{DD}$



### 16 栅极充电波形

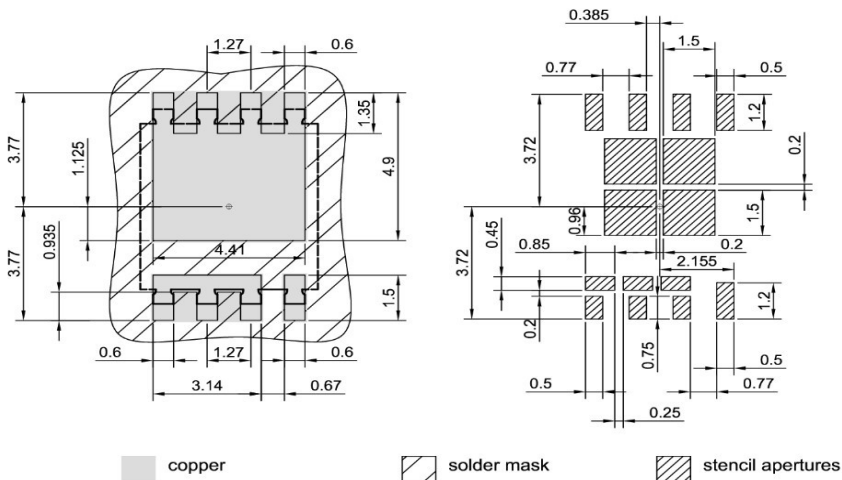


## 封装外形



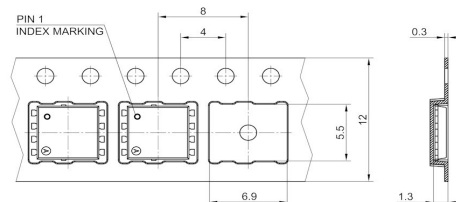
- 1) EXCLUDE MOLD FLASH
  - 2) REMOVAL ON MOLD GATE, INTRUSION 0.1MM AND PROTRUSION 0.1MM
  - 3) LEAD LENGTH UP TO ANTI FLASH LINE
  - 4) ALL METAL SURFACE ARE PLATED, EXCEPT AREA OF CUT
- ALL DIMENSIONS ARE IN UNITS MM  
THE DRAWING IS IN COMPLIANCE WITH ISO 128 & PROJECTION METHOD 1 [ ]

## Footprint



All dimensions are in units mm

## 包装信息



ALL DIMENSIONS ARE IN UNITS MM  
THE DRAWING IS IN COMPLIANCE WITH ISO 128 & PROJECTION METHOD 1 [ ]

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修订记录

| Version      | Date       | Changes                     |
|--------------|------------|-----------------------------|
| Revision 1.0 | 2021-05-14 | Final Data Sheet            |
| Revision 1.1 | 2021-06-18 | Datasheet file name updated |



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