RELEASE NOTES

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Product Name : **DAvE XE16xxN_Series**
Release Version : **2.0**
Type of Release : **Production**

Important Notice:
The delivery is **not** intended to be used for production code

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Name of the Supplier : **Infineon Technologies**
Date of Release : **08.10.2010**

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List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	XE16xxN_Series_v2_0.dip	2.0	*
Software Release Notes	DAvE_XE16xxN_Series_Release_Notes_v2_0.pdf	2.0	*
Installation Procedure	DAvE_XE16xxN_Series_Dip_Installation_Procedure_v2_0.pdf	2.0	*

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- Tasking Viper v2.4r2
- Tasking Classic v8.8r1
- Keil uVision v4.00a

Installation Instructions:

Installation instructions for installing XE16xxN_Series_v2.0 dip is provided in "DAvE_XE16xxN_Series_Dip_Installation_Procedure_v2.0.pdf"

Changes from the previous Release Version v1.0:

List of new Features:

- In MultiCAN Message objects name can be commented in MO Name text box.
- Enhancements:

ID	Description	Status
AI00055880	In MultiCAN module – DLC, Data Fields, IDE and Identifiers are configured if the MO is configured as Gateway destination object.	Done
AI00055881	In MultiCAN module - a check box is provided to enable FIFO BOT pointer as Gateway FIFO base object.	Done

List of fixed Bugs:

ID	Description	Status
AI00056519	In USIC module Channel control register is configured inside the initialization function before the pin setup.	Fixed

List of Limitations (incl. Workarounds):

- Interrupt disable register (SCU_INTDIS) is configured in MAIN.C as per Errata sheet reference SCU_X.D007.

Changes from the previous Release Version v0.3:

List of new Features:

- Support for Tasking Viper V2.4r2.
- Enhancements:

ID	Description	Status
AI00055275	MultiCAN module clock is supported LXBUS frequency and default baud rate is configured for 500kbaud.	Done
AI00055276	MultiCAN default sampling point changed to 80% from existing 60%	Done
AI00055487	System Timer (STM) module support is provided.	Done
AI00055491	ADC trigger with gating input is provided.	Done

List of fixed Bugs:

ID	Description	Status
AI00055272	Dummy reading of `MCAN_KSCCFG` & `CAN_FDRL` provided to avoid pipeline effects in MultiCAN module.	Fixed
AI00055274	In MultiCAN gateway destination can be configured to FIFO base object	Fixed

List of Limitations (incl. Workarounds):

- Interrupt disable register (SCU_INTDIS) is configured in MAIN.C as per Errata sheet reference SCU_X.D007.

Changes from the previous Release Version v0.2:

List of new Features:

- None

List of fixed Bugs:

ID	Description	Status
AI00053498	Wrong implementation of ADC0_vAliasChNum() function	Fixed

List of Limitations (incl. Workarounds):

- No known limitations

Changes from the previous Release Version v0.1:

List of new Features:

ID	Description	Status
AI00053605	SCU Driver in DAvE updated as per SCU Driver LLD v1.0 code	Done
AI00052844	ADC Function “ADC_uwResultValid” supported in DAvE	Done

List of fixed Bugs:

ID	Description	Status
AI00053604	EBC Module configured pins are not disabled for other modules	Fixed
AI00053601	CCU61_COUT2 Pin configuration incorrect	Fixed
AI00053600	DAvE generated definition #define PSW_HLDEN/PL1 provides warning in compiler	Fixed

List of Limitations (incl. Workarounds):

- No known limitations

Notes on Release v0.1

List of Features:

- Following devices are supported
    a. XE162FN
    b. XE164FN
- Following Compilers are supported
    a. Tasking Viper v2.4r1
    b. Tasking Classic v8.7r3
    c. Keil v6.17
- Following System Units & Peripheral Units can be configured through DAvE
    a. Main (Project Settings)
    b. Ports
    c. Interrupt
    d. SCU
    e. WDT
    f. USIC0/1/2
    g. GPT1/2
    h. RTC
    i. ADC0/1
    j. CC2
    k. CCU60/1
    l. MultiCAN

List of Limitations (incl. Workarounds):

Limitations:

- No known limitations