RELEASE NOTES

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Product Name : **DAvE XE16xxH_Series**
Release Version : **2.1**
Type of Release : **Production- Maintenance**

Important Notice:
The delivery is **not** intended to be used for production code

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Name of the Supplier : **Infineon Technologies**
Date of Release : **12.12.2012**

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List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	XE16xxH_Series_v2_1.dip	2.1	*
Software Release Notes	DAvE_XE16xxH_Series_Release_Notes_v2_1.pdf	2.1	*
Installation Procedure	DAvE_XE16xxH_Series_Dip_Installation_Procedure_v2_1.pdf	2.1	*

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- Tasking Viper v2.4r2
- Tasking Classic v8.8r1
- Keil uVision v4.00a

Installation Instructions:

Installation instructions for installing XE16xxH_Series_v2_1 dip is provided in "DAvE_XE16xxH_Series_Dip_Installation_Procedure_v2_1.pdf"

Changes from the previous Release Version v2.0:

List of new Features:

- Nil

List of fixed UTP:

ID	Description	Status
AI00101964	The Documentation drop box shows error message when clicked on user manual links , this links has to be removed	Fixed
AI00101965	CCU6x Dead Time control register T12DTC mask value in DAvE generated code should be 0x00FF	Fixed
AI00052844	Bug in the commenting of ADC input class selection in DAvE generated code	Fixed
AI00104835	Module CAPCOM1 is added for XE167FH device.	Fixed

List of Limitations (incl. Workarounds):

- Interrupt disable register (SCU_INTDIS) is configured in MAIN.C as per Errata sheet reference SCU_X.D007.

Changes from the previous Release Version v1.0:

List of new Features:

- In MultiCAN Message objects name can be commented in MO Name text box.
- Enhancements:

ID	Description	Status
AI00055880	In MultiCAN module – DLC, Data Fields, IDE and Identifiers are configured if the MO is configured as Gateway destination object.	Done
AI00055881	In MultiCAN module - a check box is provided to enable FIFO BOT pointer as Gateway FIFO base object.	Done

List of fixed Bugs:

ID	Description	Status
AI00056519	In USIC module Channel control register is configured inside the initialization function before the pin setup.	Fixed

List of Limitations (incl. Workarounds):

- Interrupt disable register (SCU_INTDIS) is configured in MAIN.C as per Errata sheet reference SCU_X.D007.

Changes from the previous Release Version v0.1:

List of new Features:

- Support for Tasking Viper V2.4r2.
- Enhancements:

ID	Description	Status
AI00055275	MultiCAN module clock is supported LXBUS frequency and default baud rate is configured for 500kbaud.	Done
AI00055276	MultiCAN default sampling point changed to 80% from existing 60%	Done
AI00055487	System Timer (STM) module support is provided.	Done
AI00055491	ADC trigger with gating input is provided.	Done

List of fixed Bugs:

ID	Description	Status
AI00055272	Dummy reading of `MCAN_KSCCFG` & `CAN_FDRL` provided to avoid pipeline effects in MultiCAN module.	Fixed
AI00055274	In MultiCAN gateway destination can be configured to FIFO base object	Fixed

List of Limitations (incl. Workarounds):

- Interrupt disable register (SCU_INTDIS) is configured in MAIN.C as per Errata sheet reference SCU_X.D007.

Notes on Release v0.1

List of Features:

- Following devices are supported
    a. XE167FH
    b. XE169FH
- Following Compilers are supported
    a. Tasking Viper v2.4r1
    b. Tasking Classic v8.8r1
    c. Keil v7.00
- Following System Units & Peripheral Units can be configured through DAvE
    a. Main (Project Settings)
    b. Ports
    c. Interrupt
    d. SCU
    e. WDT
    f. USIC0/1/2/3/4 (USIC - 4 Only for XE169FH)
    g. GPT1/2
    h. RTC
    i. ADC0/1
    j. CC1 (Only for XE169FH)
    k. CC2
    l. CCU60/61/62/63
    m. MultiCAN
    n. EBC

List of Limitations (incl. Workarounds):

Limitations:

- No known limitations