RELEASE NOTES

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Product Name : **DAvE XE16xx_Series**
Release Version : **2.2**
Type of Release : **Maintenance**

Important Notice:
The delivery is **not** intended to be used for production code

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Name of the Supplier : **Infineon Technologies**
Date of Release : **08.03.2010**

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List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	XE16xx_Series_v2_2.dip	2.2	*
Software Release Notes	DAvE_XE16xx_Series_Release_Notes_v2_2.pdf	2.2	*
Installation Procedure	DAvE_XE16xx_Series_Dip_Installation_Procedure_v2_2.pdf	2.2	*

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

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System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- Tasking Classic C166 v8.7r1
- Keil C166 v6.10
- Tasking Viper C166 2.1r4

Installation Instructions:

Installation instructions for installing XE16xx_Series_v2_2 dip is provided in "DAvE_ XE16xx_Series_Dip_Installation_Procedure_v2_2.pdf"

Changes from the previous Release Version v2.1:

List of new Features:

- Nil

List of fixed UTP:

ID	Description	Status
AI00053071	SCU Driver to be reverted as in v2.0 dip	Fixed
AI00052844	ADC0_uwResultValid() Macro to be supported	Fixed
AI00054143	USIC IIC Interrupt code TC Flag is not cleared	Fixed
AI00053604	EBC Configured pins are not blocked for other modules usage	Fixed

List of Limitations (incl. Workarounds):

- No Limitations

Changes from the previous Release Version v2.0:

List of new Features:

ID	Description	Status
AI00048801	support of latest SCU Driver	Enhancement done
AI00048786	Support of External Bus Unit (EBU) Module	Enhancement done

List of fixed UTP:

ID	Description	Status
AI00050459	IIC baud rate calculation incorrect	Fixed
AI00050450	The baud rate limitation for USIC SPI mode need to be changed. A value up to 20MBaud should be supported.	Fixed
AI00049068	ESR pins cannot be used for USIC I/O.	Fixed

List of Limitations (incl. Workarounds):

- No Limitations

Changes from the previous Release Version v1.0:

List of new Features:

- 80 MHz option supported for the derivatives; XE167F-96F80 and XE164F-96F80.
- CCU6x interrupt node selection is provided in matrix format with drag-n-drop method.
- USIC SSC frame length is made as user configurable
- USIC SSC: Two slave select output lines are provided

List of fixed Bugs:

- CCU6 T12 Dead time configuration for (CCU6_T12DTC.DTM) 8 bits is fixed
- CCU6 Timer Run bit set as last instruction of the Init function is corrected
- CCU6 compare mode selection: three compare modes are been changed as one compare mode.
- CCU6 CCPOSx Pins selection default item content changed and enabled tooltip is added
- CCU6 HALL mode made available without CCPOS inputs also.
- CCU6 Channel names are changed to CC6y, y = 0,1,2 & 3
- USIC SSC slave select polarity is set as low active by default
- USIC SSC word length and select output incorrect code generation is corrected
- USIC IIC pins configuration is updated with open drain characteristics by default.

List of Limitations (incl. Workarounds):

- XE167F and XE164F dips support “80 MHz” controller configuration :
    a. This configuration is not been tested
    b. All modules may not work as expected with this configuration

Changes from the previous Release Version v0.2:

List of new Features:

- Project Settings (MAIN):
    a. Supported Tasking Viper compiler
    b. Flexible System Frequency configuration
    c. Clock Output configuration
- SCU:
    a. ERU configuration for ADC Triggering & Gating
    b. WDT configuration
- USIC ASC:
    a. Half duplex, Loopback & IrDA mode configurations
    b. All ASC related interrupts configurations
    c. FIFO configuration
- USIC SSC:
    a. Slave & Loopback mode configurations
    b. Clock Polarity & Phase control feature configurations for Master mode
    c. Word length configuration
    d. All SSC related interrupts configurations
    e. FIFO configuration

- USIC IIC:
  - a. Slave mode configuration
  - B. 7/10 bit Address mode configuration for Slave
  - c. All IIC related interrupts configuration
  - d. FIFO configuration
- Interrupt:
  - a. Local Bank & Fast Interrupts configuration
- CCU6x:
  - a. CCU62, CCU63 modules configuration
  - b. Global Synchronization feature configuration
- CC2:
  - a. Global Synchronization feature configuration
- ADCx:
  - a. Module Clock configuration
  - b. Input Class configuration
  - c. Boundary 2 & 3 configuration
  - d. Result FIFO configuration
  - e. All available Channels are supported
  - f. Reference Voltage configuration for channels
  - g. Request source gate configuration
- Multi CAN:
  - a. Node configuration
    - i. All available nodes are supported
    - Ii. Analyzer Mode configuration
    - iii. All Node related interrupts configuration
    - iv. Error Warning Level configuration
    - v. Baud rate configuration
    - vi. Frame Counter configuration
  - b. MO configuration
    - i. All available MO's are supported
    - Ii. Remote Monitoring, Priority Class, Single Data Transfer Mode, Single Transmission Trial options configuration
    - iii. FIFO/Gateway configuration

List of fixed Bugs:

- PEC Segment pointer setting for USIC registers configuration
- Default register configuration in DPT file
- Avoiding in USIC Baudrate configuration :- PDIV Value reaching 0

List of Limitations (incl. Workarounds):

- XE1687 dip supports "XE1687-96F80" controller configuration :
  - a. This configuration is not been tested
  - b. All modules may not work as expected with this configuration

Changes from the previous Release Version v0.1:

List of new Features:

- Provided for CAN Node 0 the following additional two pins:
    a. P2.6 as input
    b. P2.5 as output

List of fixed Bugs:

- Dialog Page title in Interrupt Bubble "ERU" is been changed into "External Interrupt via ERU"
- The EEPROM Configuration title in the U1C1 channel of SSC is changed to "Easy Kit EEPROM Configuration". Bit field settings are shown in checkbox and also described in generated c file
- Generated Main.h doesn't include "c166.h" for Tasking compiler
- Function code of "SCS_ResetCCU60Registers();" is been copied at the end of function "SCS_SwitchSystemClock(1);"
- P2.4 used for CAPCOM2 output(CC2_17) configuration code is corrected as P2.4 = 0x00B0

List of Limitations (incl. Workarounds):

- No Limitations

Notes on Release v0.1

List of Features:

- List of supported XE16xx series derivatives:
    a. XE167F
    b. XE164F
- Following Compilers are supported
    a. Tasking C166 v8.6r3
    b. Keil C166 v6.09
- Following System Units & Peripheral Units can be configured through DAvE
    a. Main { Project Settings}
    b. Ports
    c. Interrupt
    d. USIC0 { ASC/SSC/IIC}
    e. USIC1 { ASC/SSC/IIC}
    f. USIC2 { ASC/SSC/IIC}
    g. GPT1/2
    h. RTC
    i. ADC0/1
    j. CC2
    k. CCU60/61
    l. MCAN

List of Limitations (incl. Workarounds):

- No known limitations