RELEASE NOTES

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Product Name : **DAvE XE16xFU_Series**
Release Version : **1.2**
Type of Release : **Release Candidate (RC)**

Important Notice:
The delivery is **not** intended to be used for production code

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Name of the Supplier : **Infineon Technologies**
Date of Release : **23.02.2011**

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List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	XE16xFU_Series_v1_2.dip	1.2	
Software Release Notes	DAvE_XE16xFU_Series_Release_Notes_v1_2.pdf	1.2	
Installation Procedure	DAvE_XE16xFU_Series_Dip_Installation_Procedure_v1_2.pdf	1.2	

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- Tasking Viper v2.4r2
- Tasking Classic v8.8r1
- Keil v4.00a

Installation Instructions:

Installation instructions for installing XE16xFU_Series_v1.2.dip is provided in "DAvE_XE16xFU_Series_Dip_Installation_Procedure_v1_2.pdf"

Changes from the previous Release Version v1.0:

List of fixed Bugs:

ID	Description	Status
AI00058307	The current implentation of the USIC FIFO fill function can lead to a constant filling of the FIFO. The value which is hand over to the function to load the FIFO is a 16 Bit value compared inside the for loop with a Byte value. Even the Tasking compiler generates WARNINGS. The paramter which is hand over should be a ubyte value as well	Fixed
AI00058084	Acording to the latest Version of the Datasheet, for the P11 LE/ULE XE166 derivates the maximum system frequency is as following: XE162L and XE161L = 80MHz XE161U and XE160U = 66MHz This should be changed to let the customer use the maximum performace of the devices.	Fixed
I00058371	DAvE DIP XE160FU Clock Settings for 38pin Devices: in the current Dave DIP for the XE160FU the default clock "external 8Mhz" is used. This can not work because the XE160FU -> all 38Pins P11 ULE devices do not have XTAL Pins. The option need to be removed as option an per default the internal clock should be used.	Fixed
AI00059186	XTAL pin not available for ULE 38 pin devices. The clock source for the RTC is the HP_OSC for the crystal. so no clock to RTC. so disable the RTC.	Fixed

List of Limitations (incl. Workarounds):

Limitations:

- No known limitations

Changes from the previous Release Version v0.1:

List of new Features:

- None

List of fixed Bugs:

ID	Description	Status
AI00058025	In SCU-ESR Page Pins that are not available in LE/ULE dips are disabled with a tool tip.	Fixed

List of Limitations (incl. Workarounds):

Limitations:

- No known limitations

Notes on Release v0.1

List of Features:

- Following devices are supported
  a. XE161FU
  b. XE160FU
- Following Compilers are supported
  a. Tasking Viper v2.4r2
  b. Tasking Classic v8.8r1
  c. Keil v4.00a
- Following System Units & Peripheral Units can be configured through DAvE
  a. Main (Project Settings)
  b. Ports
  c. Interrupt
  d. SCU
  e. WDT
  f. USICx
  g. GPTx
  h. RTC
  i. ADCx
  j. CC2
  k. CCU6x

List of Limitations (incl. Workarounds):

Limitations:

- No known limitations