RELEASE NOTES

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Product Name : **DAvE XC878CLM**
Release Version : **2.2**
Type of Release : **Maintenance**

Important Notice:
The delivery is **not** intended to be used for production code

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Name of the Supplier : **Infineon Technologies**
Date of Release : **11.08.2010**

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List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	XC878CLM_v2_2.zip	2.2	*
Software Release Notes	DAvE_XC878_RELEASE_NOTES_v2_2.pdf	2.2	*
Installation Procedure	DAvE_XC878_Dip_Installation_Procedure_v2_2.pdf	2.2	*

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- KEIL v8.18
- SDCC v2.9 r6.0.0
- IAR v7.60

Installation Instructions:

Installation instructions for installing XC878 dip is provided in "DAvE_XC878_Dip_Installation_Procedure_v2_2.pdf"

Changes from the previous Release Version v2.1:

List of new Features:

ID	Description	Status
AI00054448	Following Device supports are added: XC878-13FF, XC878-16FF, XC878LM-13FF, XC878LM-16FF.	Done
AI00055275	Default baud rate for MultiCAN Nodes are changed to 125kbaud instead of 100kbaud.	Done
AI00055276	Default Sample point in MultiCAN node is set to 83.33%.	Done

List of fixed Bugs:

None

List of Limitations (incl. Workarounds):

None

Changes from the previous Release Version v2.0:

List of new Features:

- Nil

List of fixed Bugs:

ID	Description	Status
AI00049340	Based on the Dave generated NMI ISR code, SYSCON0 is not being pushed and pop and RESET_RMAP () is not called upon entry of the routine.	Fixed

List of Limitations (incl. Workarounds):

ID	Description	Status
AI00049080	H/W bug workaround tool to be executed for all test cases to check any usage of MOV SFR, SFR instruction in DAvE generated code.	Tested & no usage of MOV SFR,SFR instruction found

Changes from the previous Release Version v1.3:

List of new Features:

- Nil

List of fixed Bugs:

ID	Description	Status
AI00046672	External Memory Interface: External Memory Interface and Ports Module dependencies removed as per Hardware specifications	Fixed
AI00046671	T2CCU: In the channels page of T2CCU, supported compare mode configurations without need of pins being configured.	Fixed
AI00046473	CCU6: Duty Cycle is calculated properly according to the changes in period register configuration of T12/T13 timer	Fixed

List of Limitations (incl. Workarounds):

- No Limitations

Changes from the previous Release Version v1.2:

List of new Features:

- Supported External Bus Interface feature for XC878CM-16FF device

List of fixed Bugs:

ID	Description	Status
AI00046436	Timer2 Overflow time display incorrect	Fixed

List of Limitations (incl. Workarounds):

- No Limitations

Changes from the previous Release Version v1.1:

List of new Features:

- IAR Compiler supported
- In Main module fcpumax = 26.67MHz & fsys = 160MHz is configurable with appropriate P, N, K configuration e.g. P = 2, K = 1 N = 80/40 (onchip /8 Mhz off chip oscillator)
- In ADC module supported fADC/2 (CTC = 0) configuration with warning "Internal clock for the analog part fADCI might be out of range. Pls refer datasheet for further information"

List of fixed Bugs:

- List of Changes in T2CCU module:
  - Added a macro "WriteTXOV(level)"
  - Removed function "CCU_vEnShadTransTimeroverflow"
  - Added macro ShadowTransferCompareChannels(ChNr)
- Port module Pull devices characteristics can be changed only if GPIO/alternate module port pin function is used
- Port 3 PUDSEL/PUDEN settings are been fixed
- Renamed SFR16 register DPTR as DPLH in Main.h

List of Limitations (incl. Workarounds):

- No Limitations

Changes from the previous Release Version v1.0:

List of new Features:

- XC878CM-16FF device is made as default option in project settings

List of fixed Bugs:

- In port configuration tab, the "Pull Device" is made selectable once certain alternate function of port pins is chosen
- Removed setting of Mex3 register in SDCC startup when XC878CM-13FF device is chosen
- Keil startup updated to provide option of XC878_CHIP_16FF or XC878_CHIP_13FF setting, so that Mex3 register is set according to device type chosen in DAvE Project Setting

List of Limitations (incl. Workarounds):

- No Limitations

Notes on Release v1.0

List of Features:

- Following Compilers are supported
    a. Keil v812c
    b. SDCC_XC800_2.5.4B4_R1.7
- Following User Manual is integrated:
    a. XC878_um_v1_0.pdf
- Following System Units & Peripheral Units can be configured through DAvE
    a. Main (Project Settings)
    b. Ports
    c. Interrupt
    d. UART/UART1
    e. T0/T1
    f. T2
    g. T2CCU
    h. Watchdog Timer(WDT)
    i. Power Saving Modes
    j. Analog/Digital Converter(ADC)
    k. Capture/Compare Unit 6(CAPCOM 6)
    l. Synchronous Serial Interface (SSC)
    m. Multi CAN(MCAN)

List of Limitations (incl. Workarounds):

No Known Limitations