RELEASE NOTES

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Product Name : **DAvE XC864**
Release Version : **1.2**
Type of Release : **Maintenance**

Important Notice:
The delivery is **not** intended to be used for production code

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Name of the Supplier : **Infineon Technologies**
Date of Release : **31.07.2009**

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List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	XC864_v1.2.dip	1.2	*
Software Release Notes	DAvE_XC864_RELEASE_NOTES_V1.2.pdf	1.2	*
Installation Procedure	DAvE_XC864_Dip_Installation_Procedure_V1.2.pdf	1.2	*

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- Keil v815
- SDCC_XC800_2.5.4B4_R1.8
- IAR 7.50A

Installation Instructions:

Installation instructions for installing XC864 dip is provided in "DAvE_XC864_Dip_Installation_Procedure.doc"

Changes from the previous Release Version v1.1:

List of new Features:

ID	Description	Status
AI00045866	Implementation of sfr16 data type to access two 8 bit SFRs as a single 16-bit SFR for ADC, CAN, CCU6, MDU, T2 (where applicable to the device).	Enhancement done

List of fixed Bugs:

- Nil

List of Limitations (incl. Workarounds):

ID	Description	Status
AI00049080	H/W bug workaround tool to be executed for all test cases to check any usage of MOV SFR, SFR instruction in DAvE generated code	Tested & no usage of MOV SFR,SFR instruction found

Changes from the previous Release Version v1.0:

List of new Features:

- IAR compiler supported

List of fixed Bugs:

UTP Status of Bugs:

ID	Description	Status
AI00046804	In CCU6 module: change in T12 period value do not change the compare register value in control page of channels dialog	Fixed

List of Limitations (incl. Workarounds):

- 2nd limitation of v0.1 is open in this release

UTP Status of Enhancements:

ID	Description	Status
AI00045866	Implementation of sfr16 data type to access two 8 bit SFRs as a single 16-bit SFR for ADC, CAN, CCU6, MDU, T2 (where applicable to the device).	Open

Changes from the previous Release Version v0.2:

List of new Features:

- None

List of fixed Bugs:

- None

List of Limitations (incl. Workarounds):

- No addition Limitations in this release.
- 1st limitation in v0.1 is been fixed by Keil. 2nd limitation is open in this release

Changes from the previous Release Version v0.1:

List of new Features:

- None

List of fixed Bugs:

- Following three lines are removed in nac_nad_init.s file for sdcc.
  .area startup_code (code)
  .globl __sdcc_external_startup
  __sdcc_external_startup:

List of Limitations (incl. Workarounds):

- No addition Limitations in this release. v0.1 limitations are open in this version

Notes on Release v0.1

List of Features:

- Following Compilers are supported
  a. Keil v815
  b. SDCC_XC800_2.5.4B4_R1.8
- Following User Manual is integrated:
  a. XC864_um_v1.0.pdf
- Following System Units & Peripheral Units can be configured through DAvE
  a. Main (Project Settings)
  b. NAC/NAD Configuration Feature in Main (Project Settings)
  c. Ports
  d. Interrupt
  e. UART
  f. T0/T1
  g. T2
  h. Watchdog Timer(WDT)
  i. Power Saving Modes
  j. Analog/Digital Converter(ADC)
  k. Capture/Compare Unit 6(CAPCOM 6)
  l. Synchronous Serial Interface (SSC)

List of Limitations (incl. Workarounds):

- Limitation in Keill Compiler:
  DAvE generates “NAC_NAD_INIT.A51” which contains configuration of NAC/NAD. But when DAvE Project file is opened in Keil IDE, Keil IDE does not include “nac_nad_init.a51” file in the project. User needs to include this file manually in the project.
- Errata Sheet:
  “XC846-Flash_AB_Errata_Sheet_v1_1.pdf” errata sheet is been considered for this DAvE dip implementation