RELEASE NOTES

==========

Product Name : **DAvE XC83xM_Series**
Release Version : **2.1**
Type of Release : **Maintenance**

Important Notice:
The delivery is **not** intended to be used for production code

==========

Name of the Supplier : **Infineon Technologies**
Date of Release : **23.02.2011**

==========

List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	XC83xM_Series_v2_1.dip	2.1	*
Software Release Notes	DAvE_XC83xM_Series_Release_Notes_v2_1.pdf	2.1	*
Installation Procedure	DAvE_XC83xM_Series_Dip_Installation_Procedure_v2_1.pdf	2.1	*

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- Keil v8.18
- Sdcc v2.9 r6.0.0
- IAR v7.60

Installation Instructions:

Installation instructions for installing XC83xM_Series_v2_1 dip is provided in "DAvE_ XC83xM_Series_Dip_Installation_Procedure_v2_1.pdf"

Changes from the previous Release Version v2.0:

List of new Features:

- Enhancements:

ID	Description	Status
AI00058404	To make LDLINE of COLA to be configurable when only touch-sense is enabled	Done
AI00057378	To add calibration selection for Touch Sense into Dave	Done
AI00056969	To add Touch Wheel library file into Dave	Done

List of fixed Bugs:

ID	Description	Status
AI00056537	slew rate setting for pins	Done

List of Limitations (incl. Workarounds):

- No known new limitations

Changes from the previous Release Version v1.0:

List of new Features:

- Enhancements:

ID	Description	Status
AI00055662	TS Counter Saturation and Automatic reset will be enabled when user enables ROM Library of Touch Sense mode.	Done
AI00056077	RTC module external clock Frequency for Mode 3 is supported up to 1MHz.	Done

List of fixed Bugs:

ID	Description	Status
AI00055663	Falling edge trigger is fixed for all types of edges, except Bypass the Edge detection in Level/Edge trigger of External Interrupt.	Fixed
AI00056072	SDCON Register is configured for NMI VDDC & VDDP pre-warning interrupts.	Fixed
AI00056080	RTC module RTCCR registers are updated for possible values of timer match	Fixed

List of Limitations (incl. Workarounds):

- Improper Dialogue page displayed in Windows 7 Operating system (ID - AI00056103) – **Recommended resolution (DPI settings) to use 125%**

Changes from the previous Release Version v0.2:

List of new Features:

- Enhancements:

ID	Description	Status
AI00050031	DAvE need to support IAR tool chain	Done
AI00055506	Additional User Code begin/end sections are required in CCU6 ISR	Done

List of fixed Bugs:

ID	Description	Status

AI00055617	Definition of typedef in Math library header file is changed from
	`typedef struct pi_param{`
	`:`
	`}PI_param;`
	to
	`typedef struct pi_param{`
	`:`
	`}idata PI_param;`
		Done

List of Limitations (incl. Workarounds):

- No known limitations

Changes from the previous Release Version v0.1:

List of new Features:

- Enhancements:

ID	Description	Status
AI00055121	The variable TOUCH_SENSE_RESULT [19] changed as below to allow user to access those variables individually in an easy manner. Eg for KEIL: unsigned char xdata TOTAL_TSCTRH _at_ 0xF0FE unsigned char xdata TOTAL_TSCTRL _at_ 0xF0FD unsigned char xdata TSCTR_Counter _at_ 0xF0FC	Done
AI00055162	Power Management Control page Peripheral Status controls has been disabled except CDC & MDU.	Done

AI00055150	Port initialization optimization:
	Port initialization sequence changed as per SFR paging.
	Eg:
	`SFR_PAGE(_pp0, noSST); // switch to page 0`
	`P0_DATAOUT = 0xFE; // load data output register for port P0`
	
	`P1_DATAOUT = 0x3D; // load data output register for port P1`
	
	`SFR_PAGE(_pp3, noSST); // switch to page 3`
	`P0_OD = 0xFE; // load open-drain register for port P0`
	
	`P1_OD = 0x3D; // load open-drain register for port P1`
	
	`SFR_PAGE(_pp0, noSST); // switch to page 0`
		Done

AI00055175	In Math function - Additional functions like Sine/Cosine, Inv_Vector_Rotation have been added.	Done

List of fixed Bugs:

ID	Description	Status
AI00054837	Removed one fADC clock option (fADC /5)	Fixed
AI00055112	Wrong handling of dependency in UART for half duplex mode	Fixed
AI00055149	Port initialization sequence has been changed in order to avoid unnecessary port toggling.	Fixed

List of Limitations (incl. Workarounds):

- No known limitations

Notes on Release v0.1

List of Features:

- Following devices are supported
  - XC835M
    - XC835MT-2F
  - XC836M
    - XC836-2F
    - XC836M-2F
    - XC836M-1F
    - XC836T-2F
    - XC836MT-2F
- Following Compilers are supported
  - Keil v8.18
  - Sdcc v2.9 r6.0.0
- Following System Units & Peripheral Units can be configured through DAvE
  - MAIN (Project Settings)
  - MATH Function
  - EEPROM
  - PORT
  - INTERRUPT
  - UART
  - T0/T1
  - LED/TS
  - T2
  - RTC
  - ADC
  - CCU6
  - IIC
  - SSC
  - WDT
  - Power Management

List of UTP issues Fixed:

ID	Description	Status
AI00054067	Add support to call BootROM routine BR_UART_BSL	Done
AI00054187	CR: support for ATV variant XC836	Done
AI00054399	UART ISR function prototype is wrongly generated	Done
AI00054450	Add another device names into XC83x DIP	Done
AI00054599	Wrong formula used in calculation of period for valid pad touch for LEDTS module	Done
AI00054619	Wake-up Source via EXINT0 from power-down in "Power Saving Modes"	Done

List of Limitations (incl. Workarounds):

Limitations:

- No known limitations