RELEASE NOTES

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Product Name : **DAvE XC27xxX_Series**
Release Version : **2.1**
Type of Release : **Maintenance**

Important Notice:
The delivery is **not** intended to be used for production code

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Name of the Supplier : **Infineon Technologies**
Date of Release : **08.03.2010**

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List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	XC27xxX_Series_v2_1.dip	2.1	*
Software Release Notes	DAvE_XC27xxX_Series_Release_Notes_v2_1.pdf	2.1	*
Installation Procedure	DAvE_XC27xxX_Series_Dip_Installation_Procedure_v2_1.pdf	2.1	*

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- Tasking Viper v2.2r5
- Tasking Viper v2.3r1
- Tasking Classic v8.7r3
- Keil v6.17

Installation Instructions:

Installation instructions for installing XC27xxX_Series_v2.1 dip is provided in "DAvE_ XC27xxX_Series_Dip_Installation_Procedure_v2_1.pdf"

Changes from the previous Release Version v2.0:

List of New Features:

ID	Description	Status
AI00053605	SCU Driver in DAvE to be updated as per in SCU Driver v1.0	Fixed

List of Fixed Bugs:

ID	Description	Status
AI00052844	ADC0_uwResultValid() Macro to be supported	Fixed
AI00054143	USIC IIC Interrupt code TC Flag is not cleared	Fixed
AI00053604	EBC Configured pins are not blocked for other modules usage	Fixed

List of Limitations (incl. Workarounds):

ID	Description	Status
AI00049744	EBC Module: In 8-bit Demultiplexed configuration, data written in external RAM in odd addresses are incorrect	Open

Changes from the previous Release Version v1.0:

List of New Features:

- Nil

List of Fixed Bugs:

- Nil

List of Limitations (incl. Workarounds):

ID	Description	Status
AI00049744	EBC Module: In 8-bit Demultiplexed configuration, data written in external RAM in odd addresses are incorrect	Open

Changes from the previous Release Version v0.3:

List of New Features:

ID	Description	Status
AI00049822	Number of USIC Modules supported, changed from 2 to 3 as per change in datasheet	Done

List of Fixed Bugs:

ID	Description	Status
AI00050019	USIC Baud rate calculated (fractional divider option) for some higher frequencies has variations with respect to required	Fixed

List of Limitations (incl. Workarounds):

ID	Description	Status
AI00049744	EBC Module: In 8-bit Demultiplexed configuration, data written in external RAM in odd addresses are incorrect	Open

Changes from the previous Release Version v0.2:

List of New Features:

- Supported Tasking Classic v8.7r3 and Keil v6.17 compilers
- Supported EBC Module
- Supported Normal divider configuration for baudrate generation in ASC/SSC (USIC)

List of Fixed Bugs:

ID	Description	Status
AI00049395	In CCU6 Initialization configuration, shadow transfer register configuration is done after port pins configuration. This leads to spikes during switching the port function	Fixed
AI00049099	In Port Output Characteristics page, edge mode selection option for	Fixed

	medium/weak driver incorrect	
AI00049098	wrong EEPROM Configuration comment in UxCx.c when using the SSC mode	Fixed
AI00049097	USIC SSC PDIV value calculated is wrong when the name of the function ,e.g. U1C0_SSC_vi0IC is changed	Fixed
AI00048890	Port 8.4, 8.5 & 8.6 configuration in CCU60 module vInit () incorrect	Fixed
AI00048854	In GPT Module, Enable alternate output T3IN label incorrect	Fixed
AI00047889	Frame End Mode Bit configuration Support in SSC	Fixed
AI00046139	MultiCAN FIFO Function (read/write) don't work properly when two FIFO buffers are configured in one node	Fixed

List of Limitations (incl. Workarounds):

ID	Description	Status
AI00049744	EBC Module: In 8-bit Demultiplexed configuration, data written in external RAM in odd addresses are incorrect	Open

Changes from the previous Release Version v0.1:

List of new Features:

- Supported XC2765X-104F Device (100 Pin)
- Supported Tasking Viper v2.3r1
- Fast interrupts supported for Viper Compiler.
- PLL configuration feature is supported in Main (Project Setting)
- Enhancements:

ID	Description	Status
AI00048169	Naming of Message Objects in List is been changed from "MO0" to "MO 000". Similarly for other MOs. This generates sorted list for easy configuration	Done
AI00047837	Maximum ADC frequency changed to 20 MHz as per latest datasheet	Done

List of fixed Bugs:

ID	Description	Status
AI00048171	In MultiCAN Initialization configuration, RXEN bit configuration of slave objects in Receive FIFO incorrect.	Fixed
AI00048028	Clearing of Receive Indication flag (RIF) incorrect in UxCx_SSC_uwGetData(0 function. AIF to be cleared.	Fixed
AI00046979	1. In the control page of the CCU2 , the feature of the Port Lock control is not needed 2. In the channel control page of CCU2 the naming of the output register control is incorrect	Fixed
AI00046726	U1CI SSC Module, Clock polarity & clock phase settings generated incorrect	Fixed

List of Limitations (incl. Workarounds):

- List of Open **Bugs/Issues Status:**

ID	Description	Status
AI00047889	Frame End Mode Bit configuration Support in SSC	Open
AI00046139	MultiCAN FIFO Function (read/write) don't work properly when two FIFO buffers are configured in one node	Open

Notes on Release v0.1

List of Features:

- Following devices are supported
  a. XC2785X-104F
- Following Compilers are supported
  a. Tasking Viper v2.2r5
- Following System Units & Peripheral Units can be configured through DAvE
  a. Main (Project Settings)
  b. Ports
  c. Interrupt
  d. SCU
  e. WDT
  f. USIC0 & 1 (ASC/SSC/IIC)
  g. GPT1/2
  h. RTC
  i. ADC0/1
  j. CC2
  k. CCU60...63
  l. MCAN

List of Limitations (incl. Workarounds):

Limitations:

- Interrupt
  Fast interrupts not supported for Viper Compiler.
- Main:
  PLL configuration feature is not supported

Bugs/Issues Status:

ID	Description	Status
AI00046140	USIC IIC Write Data function don't support shifted slave address	Fixed
AI00046137	IO Port Initialization sequence incorrect	Fixed
AI00046139	MultiCAN FIFO Function (read/write) don't work properly	Open
AI00046596	USIC CH1 as SSC with clock out on P3.5 incorrect configuration	Open