RELEASE NOTES

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Product Name : **DAvE XC27xxX_Part5_Series**
Release Version : **1.2**
Type of Release : **Release Candidate (RC)**

Important Notice:
The delivery is **not** intended to be used for production code

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Name of the Supplier : **Infineon Technologies**
Date of Release : **23.02.2011**

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List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	XC27xxX_Part5_Series_v1_2.dip	1.2	
Software Release Notes	DAvE_XC27xxX_Part5_Series_Release_Notes_v1_2.pdf	1.2	
Installation Procedure	DAvE_XC27xxX_Part5_Series_Dip_Installation_Procedure_v1_2.pdf	1.2	

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- Tasking Viper v2.4r2
- Tasking Classic v8.8r1
- Keil v4.00a

Installation Instructions:

Installation instructions for installing XC27xxX_Part5_Series_v1.2.dip is provided in "DAvE_XC27xxX_Part5_Series_Dip_Installation_Procedure_v1_2.pdf"

Changes from the previous Release Version v1.1:

List of fixed Bugs:

ID	Description	Status
AI00058307	The current implentation of the USIC FIFO fill function can lead to a constant filling of the FIFO. The value which is hand over to the function to load the FIFO is a 16 Bit value compared inside the for loop with a Byte value. Even the Tasking compiler generates WARNINGS. The paramter which is hand over should be a ubyte value as well	Fixed
AI00058371	DAvE DIP XE160FU Clock Settings for 38pin Devices: in the current Dave DIP for the XE160FU the default clock "external 8Mhz" is used. This can not work because the XE160FU -> all 38Pins P11 ULE devices do not have XTAL Pins. The option need to be removed as option an per default the internal clock should be used.	Fixed
AI00059186	XTAL pin not available for ULE 38 pin devices. The clock source for the RTC is the HP_OSC for the crystal. so no clock to RTC. so disable the RTC.	Fixed
AI00059095	_TP_BUFF_MGMT macro is not generated for LIN 2.0	Fixed
AI00059098	C Style Comments not accepted while Loading LDF files	Fixed
AI00059100	If Subscriber node having frames which contain more signals, then Corresponding signal information is not generated in LIN_ldf.c & LIN_ldf.h.	Fixed

List of Limitations (incl. Workarounds):

Limitations:

- LIN support is only available for Tasking Viper.
- LIN baud rate has to be in the range of 5k to 20k.
- The LDF reader is designed to read LDF files based upon Lin specification versions 2.0 and 2.1 only. In addition, it supports reading of LDF file without comments.

Note / Application Hints:

1. Supports latest LIN driver release V1.4 Beta. Please refer to the driver release notes/user manual for driver details.
2. While using the static signals for master requests and slave responses care should be taken to declare the required variables as “extern”
   Ex:
```
extern uint8 Lin_DiagTxFifo[_DGNSTC_TBUFF_SZ][LIN_DIAG_FRM_LEN];
extern uint8 Lin_DiagRxFifo[_DGNSTC_RBUFF_SZ][LIN_DIAG_FRM_LEN];
extern uint8 Lin_DiagRxFifoRdIdx;
extern uint8 Lin_DiagTxFifoWrIdx;
```
3. While using DAvE option “Internal Buffer Management” for transport protocol, the diagnostic buffers are not defined to be used external to the driver.
4. DAvE LDF parser is not taking any action on the section “Node Composition definition” in ldf file, since this is not required by the driver.

Changes from the previous Release Version v1.0:

List of fixed Bugs:

ID	Description	Status
AI00058372	Lin configuration: Unconditional frames associated with Event Triggered frames did not carry own PID within the first data byte	Fixed

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Changes from the previous Release Version v0.1:

List of new Features:

- Enhancements:

ID	Description	Status
AI00058019	Selection of protocol version is provided for each node.	Done
AI00058021	Lin - Static signal API is supported for signal size being maximum 16 bits & out of byte boundary.	Done

List of fixed Bugs:

ID	Description	Status
AI00058025	In SCU-ESR Page Pins that are not available in LE/ULE dips are disabled with a tool tip.	Fixed

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Note / Application Hints:

5. Supports latest LIN driver release V1.4 Beta. Please refer to the driver release notes/user manual for driver details.
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   Ex:

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extern uint8 Lin_DiagRxFifoRdIdx;
extern uint8 Lin_DiagTxFifoWrIdx;
```

7. While using DAvE option “Internal Buffer Management” for transport protocol, the diagnostic buffers are not defined to be used external to the driver.
8. DAvE LDF parser is not taking any action on the section “Node Composition definition” in ldf file, since this is not required by the driver.

Notes on Release v0.1

List of Features:

- Following devices are supported
  a. XC2722X
  b. XC2712X
- Following Compilers are supported
  a. Tasking Viper v2.4r2
  b. Tasking Classic v8.8r1
  c. Keil v4.00a
- Following System Units & Peripheral Units can be configured through DAvE
  a. Main (Project Settings)
  b. Ports
  c. Interrupt
  d. SCU
  e. WDT
  f. USICx
  g. GPTx
  h. RTC
  i. ADCx
  j. CC2
  k. CCU6x
- Lin Configuration for USIC Peripheral is supported

List of Limitations (incl. Workarounds):

Limitations:

- LIN support is only available for Tasking Viper.
- LIN baud rate has to be in the range of 5k to 20k.
- If static signal API option is selected, signal size should be less than 8 bits & should fall in byte boundary.
- The LDF reader is designed to read LDF files based upon Lin specification versions 2.0 and 2.1 only. In addition, it supports reading of LDF file without comments.

Note / Application Hints:

1. Supports latest LIN driver release V1.4 Beta. Please refer to the driver release notes/user manual for driver details.
2. While using the static signals for master requests and slave responses care should be taken to declare the required variables as “extern”
   Ex:

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extern uint8 Lin_DiagTxFifo[_DGNSTC_TBUFF_SZ][LIN_DIAG_FRM_LEN];
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3. While using DAvE option “Internal Buffer Management” for transport protocol, the diagnostic buffers are not defined to be used external to the driver.
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