# RELEASE NOTES

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**Product Name** : **DAvE XC22xxI_Series**
**Release Version** : **1.0**
**Type of Release** : **Release Candidate (RC)**

Important Notice:
The delivery is **not** intended to be used for production code

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**Name of the Supplier** : **Infineon Technologies**
**Date of Release** : **28.08.2012**

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## List of Released Items:

| Released Item | Filename | Version | Updated |
|---|---|---|---|
| DAvE DIP | XC22xxI_Series_v1_0.dip | 1.0 | |
| Software Release Notes | DAvE_XC22xxI_Series_Release_Notes_v1_0.pdf | 1.0 | |
| Installation Procedure | DAvE_XC22xxI_Series_Dip_Installation_Procedure_v1.0.pdf | 1.0 | |

## Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

## System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- Tasking Viper v2.5r1
- Tasking Classic v8.9r1
- Keil v7.05

## Installation Instructions:

Installation instructions for installing XC22xxI_Series_v1_0 dip is provided in "DAvE_XC22xxI_Series_Dip_Installation_Procedure_v1_0.pdf"

## Changes from the previous Release Version v0.1:

### List of new Features:

- Enhancements:
    - None

### List of fixed Bugs:

| ID | Description | Status |
|---|---|---|
| Issue_01 | a. In P11 Future, we have 12-bit ADC but the timing information only show for 10-bit and 8 bit<br>b. The selection for the ADC resolution only available for 10-bit and 8-bit, no selection for 12-bit ADC. | Fixed |
| Issue_02 | When ADC1 Channel 10 is enable, there is no warning on the JTAG pin, which this warning is available when selecting ADC0 CH10. Since both channel is using the same pin, it would be wise to have the warning massage for ADC1 CH10 as well. | Fixed |
| Issue_03 | XC2269i – USIC4 Channel1 should show none for the protocol selection. This is because there are no pins maps to this channel. This channel should be greyed out or disable as it cannot be used. | Fixed |
| Issue_04 | The VCO frequency is not shown correctly at the PLL setting. With the default 8MHz reference frequency, the VCO should be 256MHz | Fixed |
| Issue_05 | The Documentation drop box shows error message when clicked on user manual links – the links to be removed | Fixed |

### List of Limitations (incl. Workarounds):

**Limitations:**

- No known limitations

## Notes on Release v0.1

### List of Features:

- Following devices are supported
    a. XC2268I
    b. XC2269I
    c. XC2289I
- Following Compilers are supported
    a. Tasking Viper v2.5r1
    b. Tasking Classic v8.9r1
    c. Keil v7.05
- Following System Units & Peripheral Units can be configured through DAvE
    a. Main (Project Settings)
    b. Ports

c. Interrupt
d. SCU
e. WDT
f. USIC0/1/2/3/4
g. GPT1/2
h. RTC
i. ADC0/1
j. CC2
k. CCU60/1/2/3
l. MCAN

## List of Limitations (incl. Workarounds):

**Limitations:**

- No known limitations