RELEASE NOTES

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Product Name : **DAvE TC178x_Series**
Release Version : **1.0**
Type of Release : **Release Candidate (RC)**

Important Notice:
The delivery is **not** intended to be used for production code.

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Name of the Supplier : **Infineon Technologies**
Date of Release : **17.08.2011**

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List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	TC178x_Series_v1.0.zip	1.0	
Software Release Notes	DAvE_TC178x_Series _Release_Notes_v1.0.pdf	1.0	
Installation Procedure	DAvE_TC178x_Series_Dip_Installation_Procedure _v1.0.pdf	1.0	

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- Tasking v3.5r1
- Hightec GNU v3.4.7.4

Installation Instructions:

Installation instructions for installing TC178x_Series _v1.0 dip is provided in "DAvE_TC178x_Series _Dip_Installation_Procedure_v1.0.pdf"

Changes from the previous Release Version v0.2:

List of new Features:

- -NA-

List of fixed Bugs:

ID	Description	Status
AI00059623	Configuration of High Priority in DMAPRIO bit field for the register DMA_CHCR0x is corrected.	Fixed
AI00061415	Dave issues warning for the limits of cascading GTCs and LTCs.	Fixed

List of Limitations (incl. Workarounds):

- No Known Limitations.

Changes from the previous Release Version v0.1:

List of new Features:

- Supported MLI and MSC Modules

List of fixed Bugs:

ID	Description	Status
AI00060868	Frequency range for internal ADC clock (f_{ADCI}) is corrected to 1 to 18 Mhz.	Fixed

List of Limitations (incl. Workarounds):

- No Known Limitations.

Notes on Release v0.1

List of Features:

- Following device is supported
    a. TC1782 (BA), TC1784 (BA)
- Following Compilers are supported
    a. Tasking v3.5r1
    b. Hightec GNU v3.4.7.4
- Following System Units & Peripheral Units can be configured through DAvE
    a. Main (Project Settings)
    b. Ports
    c. STM
    d. DMA
    e. ASC0/1
    f. SSC0/1/2
    g. MultiCAN
    h. GPTA
    i. ADC0/1
    j. SCU
    k. FADC
    l. PMI
    m. DMI
    n. Interrupt
    o. SBCU
    p. PCP
    q. EBU

Testing Status:

- All planned test cases are completed.

List of Limitations (incl. Workarounds):

- No Known Limitations.