RELEASE NOTES

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Product Name : **DAvE TC1767**
Release Version : **0.4**
Type of Release : **Beta**

Important Notice:
The delivery is **not** intended to be used for production code.

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Name of the Supplier : **Infineon Technologies**
Date of Release : **21.10.2009**

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List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	TC1767_v0_4.dip	0.4	*
Software Release Notes	DAvE_TC1767_Release_Notes_v0_4.pdf	0.4	*
Installation Procedure	DAvE_TC1767_Dip_Installation_Procedure_v0_4.pdf	0.4	*

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.1r22 and above
- Tasking v3.1r1
- Hightec GNU v3.4.5.2

Installation Instructions:

Installation instructions for installing TC1767 dip is provided in "DAvE_TC1767_Dip_Installation_Procedure_v0_4.pdf"

Changes from the previous Release Version v0.3:

List of new Features:

- Supported MLI, MSC and FADC Modules
- Fixed below enhancement UTP:

ID	Description	Status
AI00051417	Supporting of following modules: PCP, SBCU, PMI, DMI, Interrupt	Enhancement done
AI00051042	Support for sfr header from Tasking and HighTec	Enhancement done

List of fixed Bugs:

ID	Description	Status
AI00052239	TRIG signals are not connected to GPTA1 and LTCA2	Fixed

List of Limitations (incl. Workarounds):

- No Known Limitations.

Changes from the previous Release Version v0.2:

List of new Features:

ID	Description	Status
AI00049532	SCU (Including ERU) Module to be supported	Enhancement done

List of fixed Bugs:

ID	Description	Status
AI00049367	Some reset indicator switch options are not available in main()	Fixed
AI00050144	Incorrect PLL disconnecting message in generated RTF file	Fixed

List of Limitations (incl. Workarounds):

- No Known Limitations.

Changes from the previous Release Version v0.1:

List of new Features:

- Changes in Main (Project Settings)
  - Controller type is disabled, no option to select for the user (TC1767 default device shown)
  - In the General page compiler naming format is changed. Indicates the version of compiler tested
  - Default CPU Clock is been changed to 133.33 MHz. Appropriate P, N & K divider configurations are set.
  - All frequency values are limit to two decimal digits.

List of fixed Bugs:

- Nil

List of Limitations (incl. Workarounds):

- No Known Limitations.
- Limitations mentioned in v0.1 release are not applicable in this release as planned tests are completed (Unit, Integration & system tests)

Notes on Release v0.1

List of Features:

- Following device is supported
    a. TC1767
- Following Compilers are supported
    a. Tasking v3.1r1
    b. Hightec GNU v3.4.5.2
- Following System Units & Peripheral Units can be configured through DAvE
    a. Main (Project Settings)
    b. Ports
    c. STM
    d. DMA
    e. ASC0/1
    f. SSC0/1
    g. MultiCAN
    h. GPTA
    i. ADC0/1

Testing Status:

Unit Tests:

- All planned Unit Tests are completed

Integration Tests:

- ADC – DMA Integration Tests are completed
- ASC0/1 – SSC0/1 – DMA Integration Tests are completed

- STM – ADC Integration Tests are not completed
- GPTA – ADC – DMA Integration Tests are not completed
- GPTA – MCAN Integration Tests are not completed

System Tests:

- System Tests involving all supported modules are not completed

List of Limitations (incl. Workarounds):

- Alpha release of dip indicates early release & all planned tests for release are not been completed. Level of Quality is not reached.

- Integration Tests and System Tests as indicated in testing status section are not completed