RELEASE NOTES

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Product Name : **DAvE TC176x Series**
Release Version : **1.1**
Type of Release : **Maintenance**

Important Notice:
The delivery is **not** intended to be used for production code

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Name of the Supplier : **Infineon Technologies**
Date of Release : **22.12.2009**

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List of Released Items:

Released Item	Filename	Version	Updated
DAvE DIP	TC176x_Series_v1_1.zip	1.1	x
Software Release Notes	DAvE_TC176x_RELEASE_NOTES_v1_1.pdf	1.1	
Installation Procedure	DAvE_TC176x_Dip_Installation_Procedure_v1_1.pdf	1.1	

Customer Contact:

If you need any support for the software or documentation, contact Infineon Technologies AG by electronic mail:

dave@Infineon.com

System Requirements:

This release is intended to be used with

- DAvE Mother System version 2.2r1
- Tasking v2.4r1
- GNU v3.3.7.3

Installation Instructions:

Installation instructions for installing TC176x Series dip is provided in "DAvE_TC176x_Dip_Installation_Procedure_v1_1.pdf"

Changes from the previous Release Version v1.0:

List of New Features:

- Nil

List of Fixed Bugs:

ID	Description	Status
AI00050007	ADC Module Clock Range is not as per data sheet	Fixed
AI00046132	PLL Initialization sequence hangs during software reset	Fixed
AI00050864	In DAvE Code Generation: • DAVE code should only contain printable character of UTF-8 • use dot as decimal separator • no more than 2 numbers after decimal separator	Fixed
AI00039564	GPTA LTC cells run only half the module speed, this limitation is not been indicated to user	Fixed
AI00034966	MultiCAN Gateway UI Configuration options incorrect	Fixed
AI00034337	"B signal group" on MLI0 not supported	Fixed
AI00022191	Register.h definitions incorrect	Fixed

List of Limitations (incl. Workarounds):

- No Known Limitations

Notes on Release v1.0

List of features:

- supports following TC176x Series Derivatives:
    a. TC1762
    b. TC1764
    c. TC1766
- Following Compilers are supported
    a. Tasking v2.4r1
    b. GNU v3.3.7.3
- Following System Units & Peripheral Units can be configured through DAvE
    a. Interrupt
    b. Peripheral Control Processor (PCP2)
    c. Direct Memory Access Controller (DMA)
    d. Port
    e. System Control Unit (SCU)
    f. System Timer (STM)
    g. System Peripheral Bus Control Unit (SBCU)
    h. Asynchronous/Synchronous Serial Interfaces (ASC0 and ASC1)
    i. Synchronous Serial Interfaces (SSC0 and SSC1)
    j. Micro Second Channel Interface (MSC0)
    k. Micro Link Serial Bus Interfaces (MLI0 and MLI1)
    l. MultiCAN (MCAN)
    m. General Purpose Timer Array (GPTA)
    n. Analog-to-Digital Converter (ADC)
    o. Fast Analog-to-Digital Converter (FADC)

List of new features added:

- LTC Prescaler added in CDU page of GPTA module
- LTC Prescaler Enable and Active Input select provided in LTC cells.
- ADC requests SRN (4-7) are routed to DMA peripheral request sources by adding “DMA request sources” check boxes in ADC SRN page.

List of issues fixed.

- Frame name in Output pins page of GPTA module is corrected.
- PLL configuration in VCO normal operation (no bypass) is corrected.
- The unit symbol for microsecond is changed from “mu” to “u”.

List of Limitations (incl. Workarounds):

- No Known Limitations