PRODUCT BRIEF

TRAVEO™ T2G CYT6BJ series

High-performance Body and Driver information microcontroller

Infineon releases its second generation TRAVEO™ microcontroller in embedded flash 40 nm technology. It comes back with an increase in performance, memory sizes, connectivity, and scalability to address the new automotive trends and challenges. This family has more than 30 products to provide the most scalable portfolio of safety microcontroller. In terms of performance, the middle CYT6BJ offers quad core Cortex-M7 running up to 320 MHz, up to 16 Mbytes embedded flash memory, 2 MB embedded RAM, and consuming below 800 mA in Active mode and 50 uA in Deepsleep mode with 64 KB RAM retention. Its mirrored embedded flash banks offers A/B swap capabilities.

Safety is the core know-how of Infineon, and all products provide safety mechanism (including MBIST, ECC Flash/RAM, CRC) to ensure a safety platform supporting ASIL-B ISO 26262, state-of-the-art security with Secure Boot support by a dedicated Arm® Cortex-M0+ core, and security hardware to accelerate cryptographic functions. The CYT6BJ has quad cores providing up to 2700 DMIPS in ASIL-B.

In terms of security, this product has an HSM compliant eVita full, ensuring the implementation of future proofed security measure. On top of this, it offers extensive connectivity with 10 CAN FD, 20 LINs, One QSPI, and new high-speed communicating interfaces, such as 1 Gbit Ethernet and 10Base-T1s 2 channels and eMMC, which are critical to address new domain control and connected gateway ECUs.

The TRAVEO™ CYT6BJ family features a dedicated best-in-class standby mode controller with its own voltage domain to, not only support low power modes, but also to perform certain operations such as analog measurements with three SAR A/D converters, CAN and LIN communications, RTC and basic processing, while the rest of microcontroller is in standby.

Finally to ensure the scalability, the whole family shares its core architecture (Arm® Cortex-M7™ based), allowing a maximum of software re-use, while offering pin to pin compatibility on three main packages (TQFP 176, BGA 272, and BGA 320). With this approach, customers can develop on this CYT6BJ for the application and build a complete portfolio ECU with optimized go to market and development cost.



Key features

- Quad Arm® Cortex-M7 ™ running up to 320 MHz delivering 2700 DMIPS
- Up to 16 MB flash and up to 2 MB SRAM
- Up to one Gbit Ethernet / 10 Base-T1s 2 channels and 10 CAN FD channels
- 1x eMMC/SDIO
- 3x I^2S/TDM
- ISO 26262 ASIL-B support
- eVita Full, ISO21434 compliant
- Cortex-M0+ for HSM security
- Low power consumption
- AUTOSAR 4.2 support
- 125°C temperature support

Key benefits

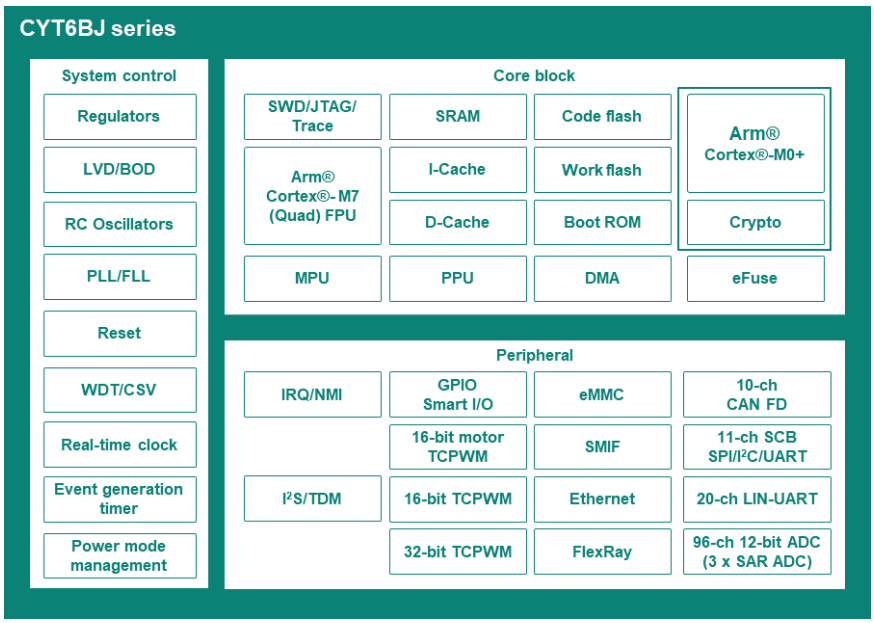
- High performance enabling ASIL-B designs
- Backward compatibility with CYT3B/4B family
- A/B swap software update over the air support
- Best-in-class power consumption

Key applications

- Body Domain Control
- Zone control
- Gateway
- Cockpit domain control

Zone control unit
Telematics gateway
Infotainment

Example block diagram



Ordering information

Type	Package	Arm® Cortex-M7	Code Flash [MB]	RAM [MB]	Ethernet	Ambient temperature
CYT6BJ8DDBQ0AESGS	TQFP 176	Quad x 320 MHz	16	2	1 ch	–40°C to 105°C
CYT6BJ8DDBQ0AEEGS	TQFP 176	Quad x 320 MHz	16	2	1 ch	–40°C to 125°C
CYT6BJBDHBQ0BZSGS	BGA 272	Quad x 320 MHz	16	2	2 ch	–40°C to 105°C
CYT6BJBDHBQ0BZEGS	BGA 272	Quad x 320 MHz	16	2	2 ch	–40°C to 125°C
CYT6BJCDHBQ0BZSGS	BGA 320	Quad x 320 MHz	16	2	2 ch	–40°C to 105°C
CYT6BJCDHBQ0BZEGS	BGA 320	Quad x 320 MHz	16	2	2 ch	–40°C to 125°C

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