PRODUCT BRIEF

256 Mbit radiation tolerant NOR Flash

Infineon's NewSpace memories are optimized for performance in low Earth orbit

Infineon Technologies offers a high-performance, low-pin count, radiation tolerant 256 Mbit NOR Flash memory optimized for low Earth orbit (LEO) applications. This NOR Flash utilizes Floating Gate technology and the industry-standard QSPI interface, with the ability to operate in SPI, DSPI, or QSPI mode. With this memory, data transfer can match or exceed traditional parallel interfaces while drastically reducing pin count.

Infineon NOR Flash memories are ideal for use as configuration image storage within space-grade FPGAs. With >20-year data retention, 100K P/E endurance cycles, and a small-footprint plastic package, this memory is designed to fit the needs of LEO missions and constellations.

Note: This product is U.S. government program restricted



Key features

Product features

- 256 Mbit density
- QSPI interface
- 133 MHz per channel
- 16-pin SOIC plastic package
- Operating temp - 40°C to 125°C

Radiation Performance

- TID: 30Krad (Si)
- SEU: 1e-16 err/bit.day
- SEL: >60 MeV.cm2/mg [LET] @ 85°C
- SEFI: IMMUNE >60 [LET] (Read); <1.1e-4 err/bit.day (Erase)

NewSpace Portfolio Benefits

- Single Lot Date Code (SLDC)
- 100% electrical test
- Certificate of Conformance (CoC)
- Radiation Lot Acceptance (RLAT) for TID

Key Applications

- Space FPGA configuration image storage

New space

www.infineon.com/hirelmemory

PRODUCT BRIEF

Infineon's radiation tolerant NewSpace memory portfolio features solutions designed for Low Earth Orbit (LEO) space applications and constellations. Our rad tol memories provide the right balance between performance and reliability based on size, weight, power, and cost (SWaP-C) considerations.

Infineon NewSpace memories provide:
- Single Lot Date Code (SLDC)
- 100% guaranteed electrical testing
- Certificate of Conformance (CoC)
- Radiation Lot Acceptance (RLAT) for TID

Product Features
- 256 Mbit density
- QSPI interface (< 8 pins)
- Operates in SPI/DSPI/QSPI mode
- 133MHz per channel
- 20-year data retention @ +125°C
- 100K P/E endurance cycles @ 85 °C
- 16-pin SOIC (10.3 x 7.5 mm)
- Lead finish Pb-free (Matte Tin)
- Operating temp - 40°C to 125°C

Parts list

Density	Interface	Part Number	Description	Package	Temp. range	TID
256 Mbit	QSPI	CYEL16B256-133SXE	256 Mb, QSPI Floating Gate rad tol NOR Flash	16-pin SOIC	-40°C to 125°C	30 Krad

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