LITIX™ Pixel Rear Configuration Wizard

User guide

Z8F80782081

1 About this tool

The LITIX™ Pixel Rear Configuration Wizard tool can be used to read, program, and emulate the *one-time programmable memory (OTP)* and to control the device through *high-speed lighting interface (HSLI)*. Use this tool together with the OTP Programmer board and the *uIO-Stick* v2, which has been available since tool release v2.1.0.

On the landing window select the device, then click on one of the available tools from the sidebar: **OTP Management** and **HSLI Control**. For more information on these tools, refer to Navigating the OTP configuration and Navigating to HSLI control.

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2 Interface boards

The following sub-chapters outline four alternative methods for connecting the TLD7002-16ES device to the OTP Programmer board and the uIO Stick v2.

With the uIO Stick v2 you can perform OTP programming and HSLI control, while with the OTP programmer you can only perform OTP memory operations. The OTP programmer has a socket to program bare TLD7002-16ES devices. The method you choose depends on your preference and the availability of your hardware.



Figure 1 OTP Programmer board with evaluation kit board

2.1 uIO-Stick v2

With the uIO Stick v2 you can read, write, emulate and control the *OTP* of the LITIX™ Pixel Rear device. To connect the uIO Stick v2 to any LITIX™ Pixel Rear evaluation board, or to a custom PCB equipped with a LITIX™ Pixel Rear device only 4 pins are needed. In case multiple LITIX™ devices are present, additional GPINx wires have to be connected.

Table 1 uIO Stick v2 to evaluation board connections

Pin usage	uIO Stick v2	TLD7002-16SYS_EVAL
Read, write, emulate, control	Pin 1 (CANH)	HSLI-H
Read, write, emulate, control	Pin 2 (GND)	GND
Read, write, emulate, control	Pin 3 (CANL)	HSLI-L
Write, emulate	Pin 8, 10, 12, 14	GPIN0 (a and b)

An example below is showing the TLD7002-16SYS_EVAL to the corresponding pins from the uIO Stick v2 refer to the following figures.

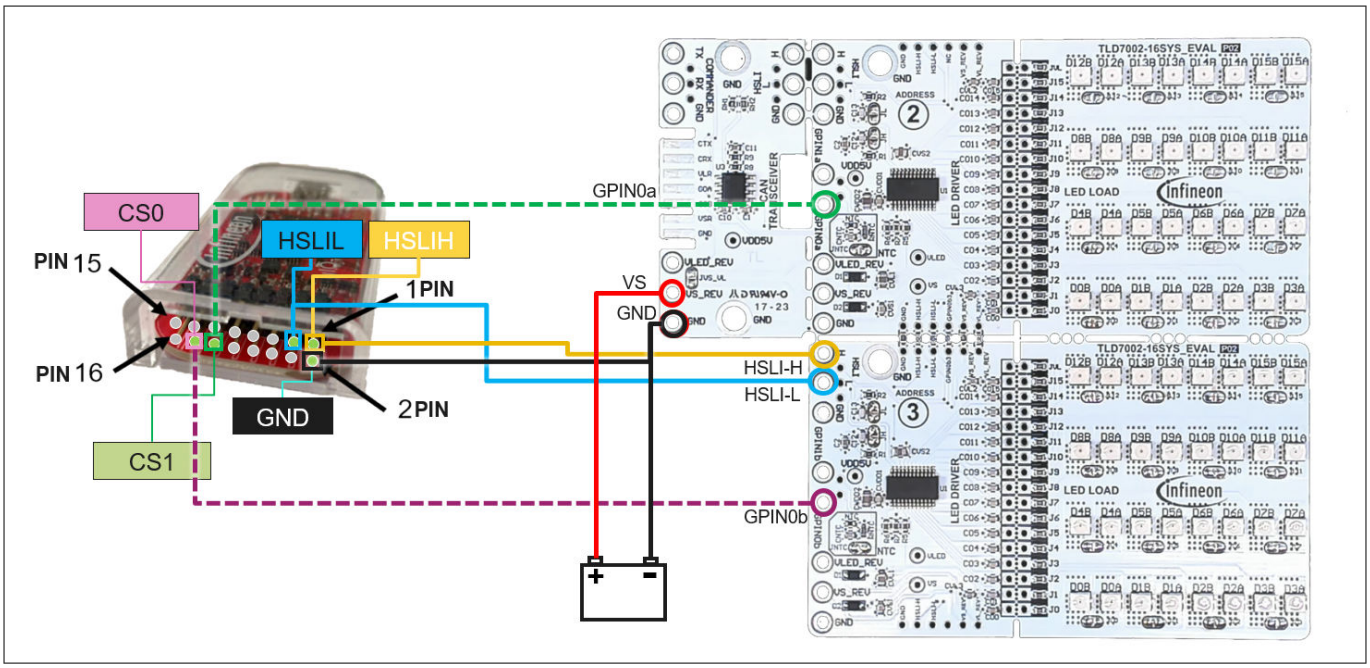


Figure 2 uIO-Stick and TLD7002-16SYS_EVAL selected pin

In case your uIO Stick v2 device is not compatible with the version of the tool, you need to update the uIO Stick v2 Firmware. Go to **Extras** and select **Update uIO**. The update starts immediately.

2.2 OTP Programmer board interface

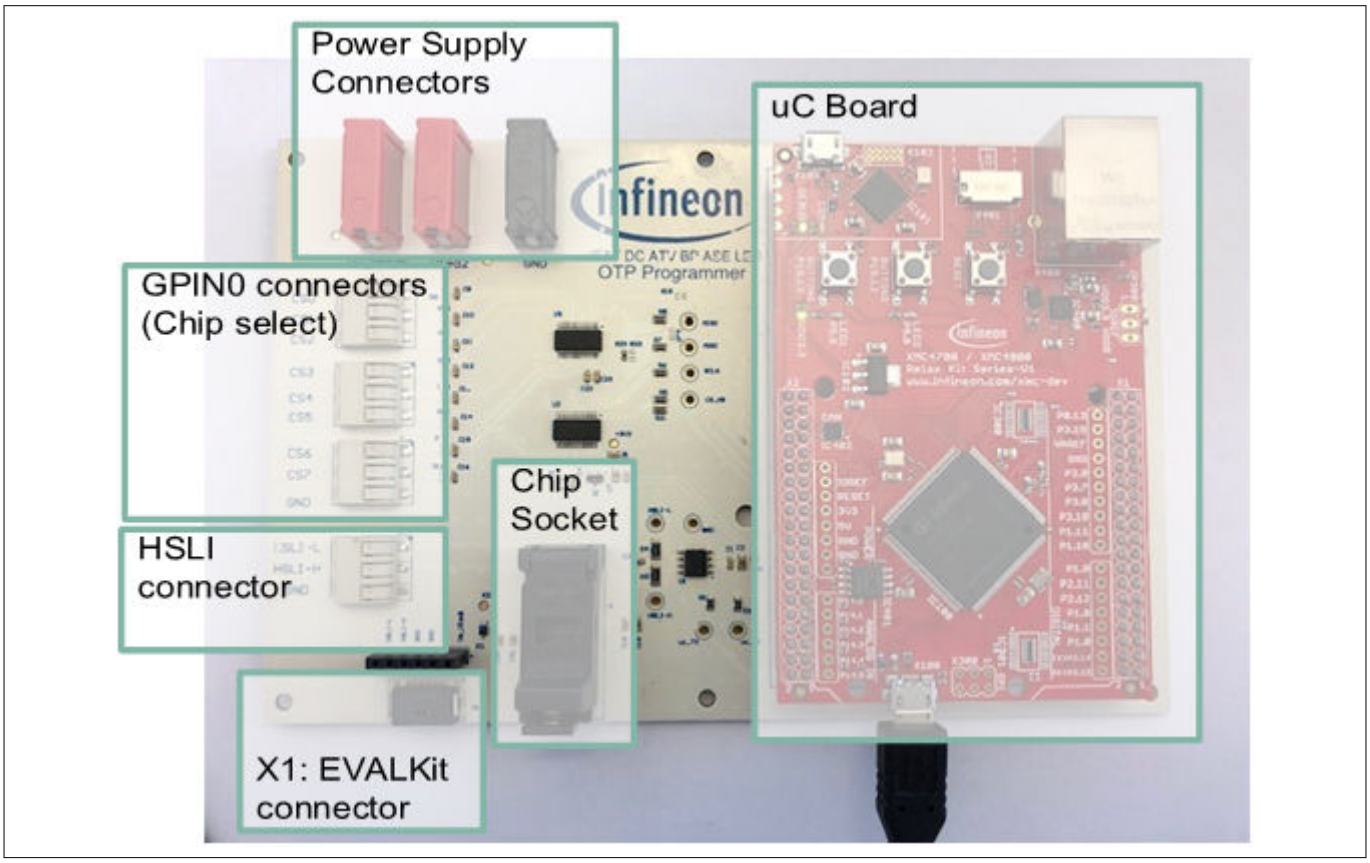


Figure 3 OTP Programmer interface

Table 2 OTP Programmer board

PCB Label	Function
CS1-CS7	GPIN0 control (act as chip select) to W/E an external TLD7002-16ES
CS0	• GPIN0 control (act as chip select) to W/E the TLD7002-16ES in the chip socket • GPIN0 control (act as chip select) to W/E an external TLD7002-16ES
HSLI-H	*HSLI* high
HSLI-L	HSLI low
X1	Interface for the TLD7002-16ES Evalkit board
Vled2	To supply the Vled2 of the evaluation kit board connected to the X1 connector
Vs_Vled1	• To supply the Vs_Vled1 of the evaluation kit board connected to the X1 connector • To supply the device on the chip socket: - 18 V to program the OTP memory - > 7 V to read or emulate the OTP memory

2.2.1 OTP board drivers

The Windows operating systems either already contain the required drivers or are downloaded automatically after you connect the OTP Programmer board to the PC for the first time.

If the LITIX™ Pixel Rear Configuration Wizard does not detect the OTP Programmer board, install the drivers manually:

1. From the **Actions** menu, select **Open driver directory**
2. Select the drivers from the list

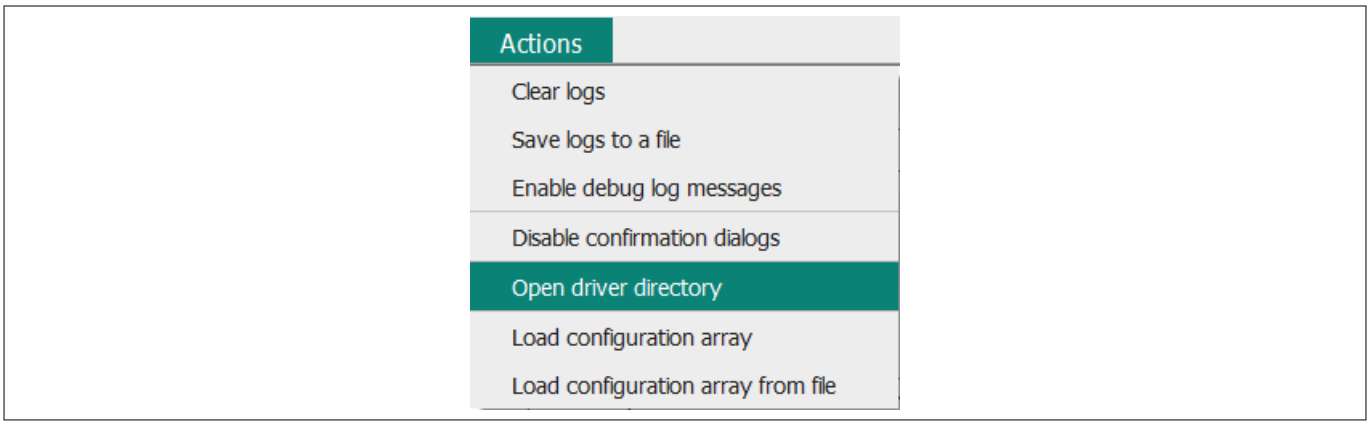


Figure 4 Open driver directory

2.2.2 Read before operating

Connections

CSx output connections are push-pull types (0 V or 5 V without series resistors).

You can find the *universal serial bus (USB)* connection to connect the tool at the bottom of the OTP Programmer board.

Use the USB connection on the top of the OTP Programmer board (debug port) only when updating the firmware.

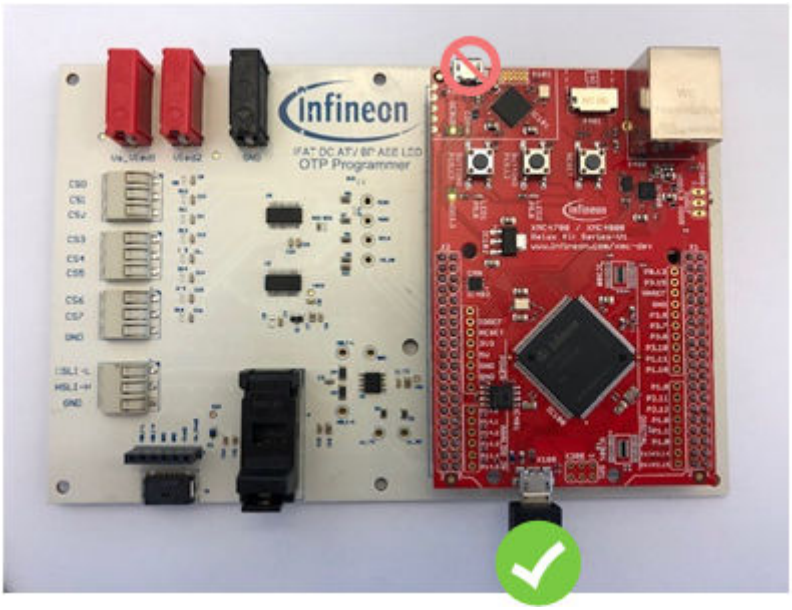


Figure 5 OTP board to tool USB connection

Key terms

Table 3 Explanation of key terms

Key term	Explanation
To write or to program a device	To permanently write a configuration to the OTP memory of a device
To emulate a device	To temporarily emulate the device configuration (a power cycle resets the emulated configuration)

Configurations

Configurations, including the following parameter values, can be emulated only once. To emulate them more than once, you must perform a power cycle of the device.

Table 4 Configurations

Configuration	GPINO configurations
1	GPINO = analog input

***Note**: A device cannot be emulated again after it has been written with the above configuration.*

2.2.3 Method 1: Use the chip socket on the OTP Programmer board

1. Insert a device into the socket with the correct orientation (pin 1, as shown in the figure below)
2. Supply the device using the red connector **Vs_Vled1** on the programmer board (18 V for programming mode, and within the range >7 V and <18 V if you are reading or emulating)

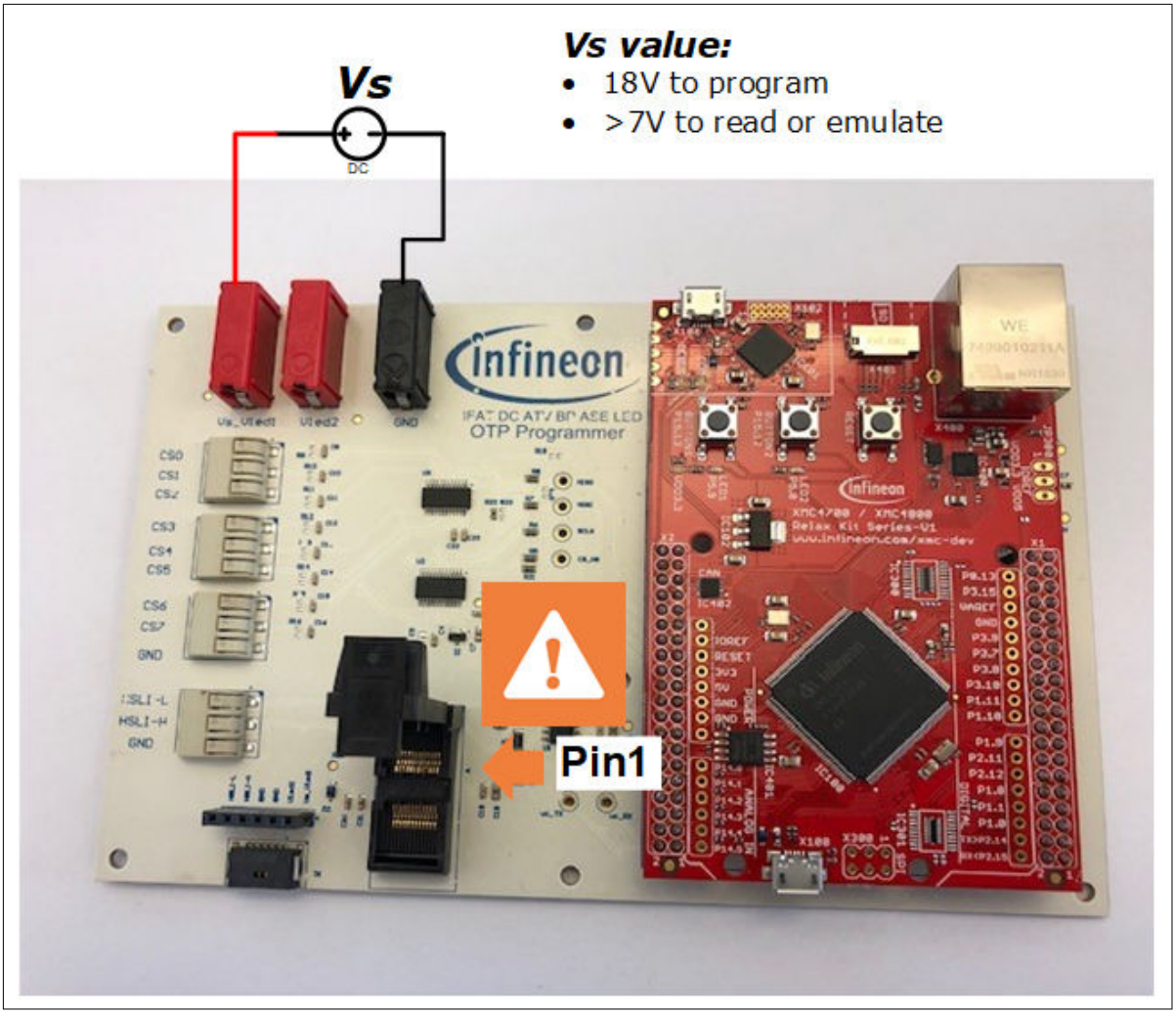


Figure 6 OTP programmer board chip orientation

***Note**: CS0 must be selected in the tool because CS0 is physically routed to the GPIN0 pin of the device in the socket.*

2.2.4 Method 2: Use the TLD7002-16ES Evalkit board

1. Connect the evaluation kit board to the OTP board
2. Use a cable to connect the OTP Programmer board to the VTEMP test point on the evaluation kit board (yellow arrow). The figure below shows an example using CS0; ensure that you select the same **Chip Select** in the tool
3. Supply the evaluation kit board using the connector **Vs_Vled1** on the OTP Programmer board (18 V to program, or within the range > 7 V and < 18 V to read or emulate)

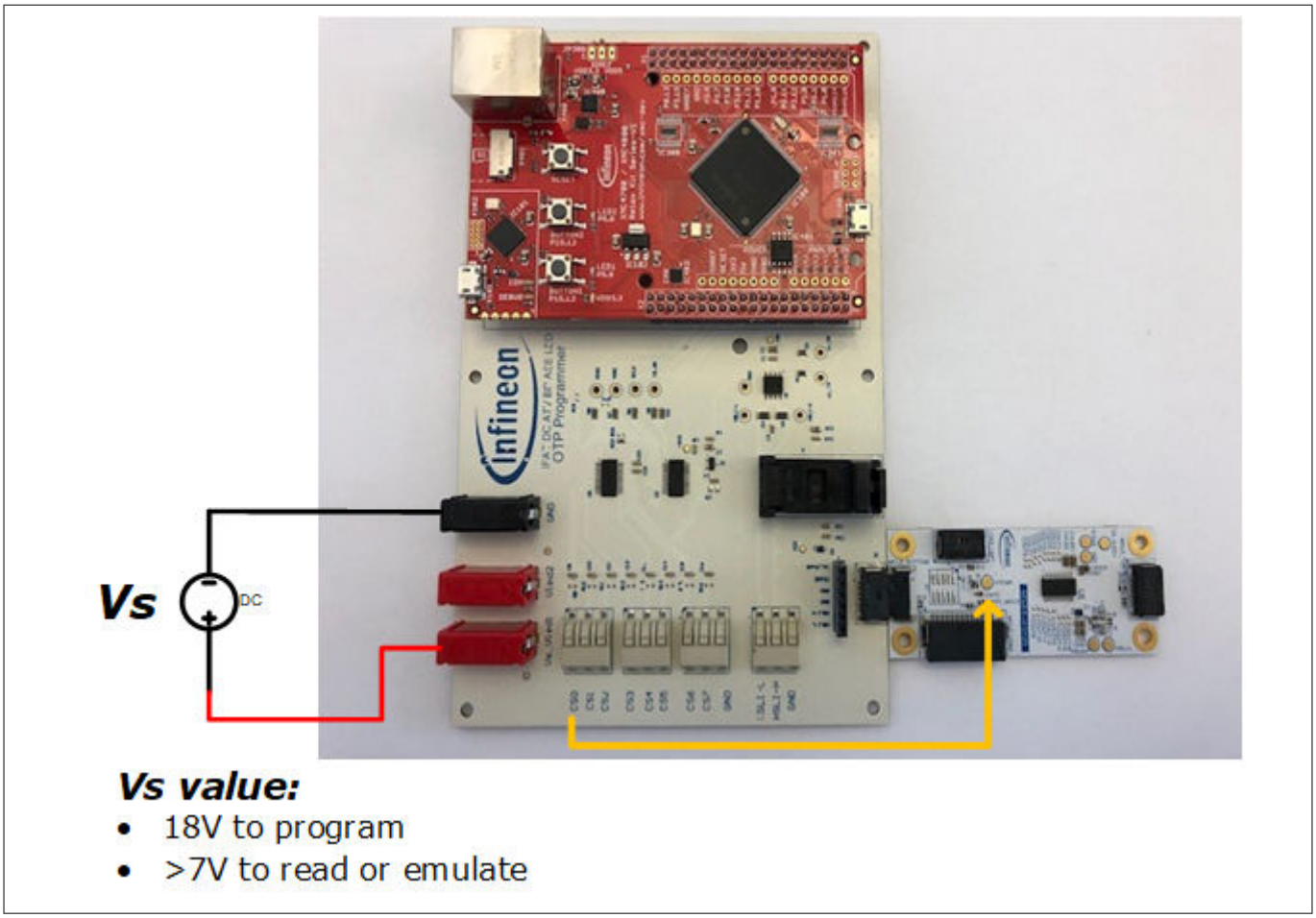


Figure 7 OTP Programmer board with evaluation kit board

2.2.5 Method 3: Use external boards with the TLD7002-16ES device

1. Connect the *HSLI* bus from the OTP board to the device(s)
2. Connect the GPIN0 pin(s) of the device(s) to the chip socket (CS) output(s) of the OTP board
3. Connect the ground between the device(s) and the OTP board
4. Supply the VS pin of the device(s) on the external boards (18 V to program or >7 V and < 18 V to read or emulate)

2 Interface boards

Single device configuration

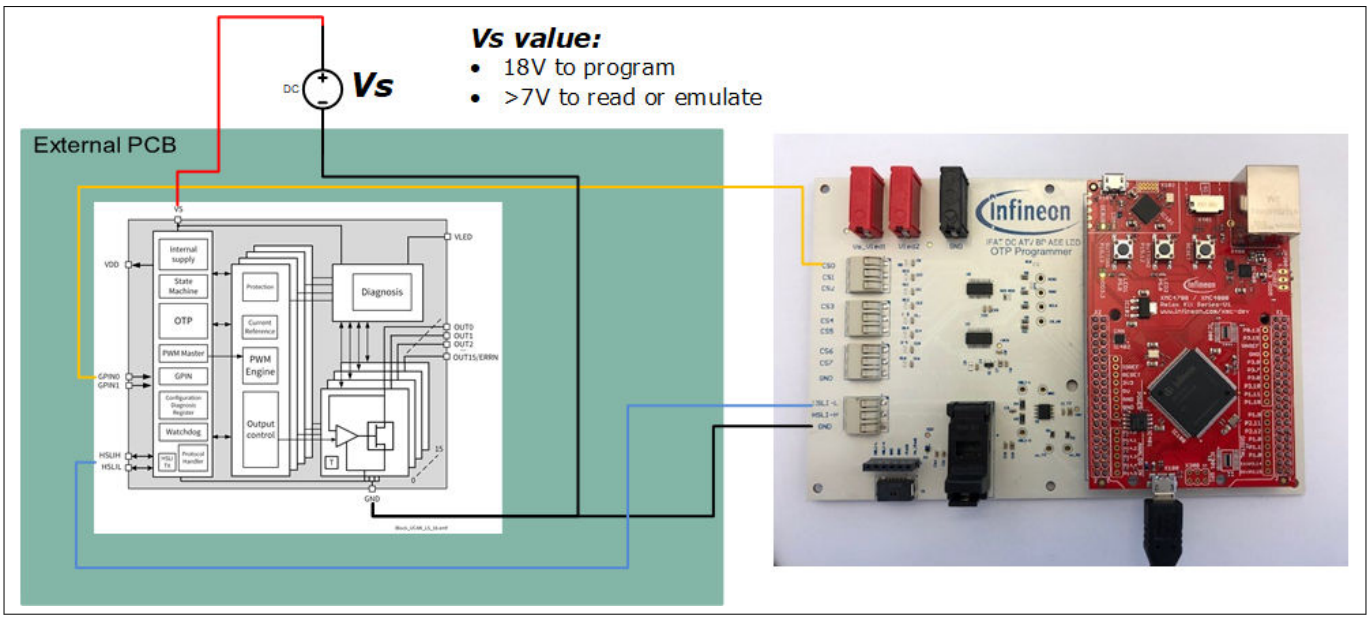


Figure 8 OTP board with one device

Bulk device configuration

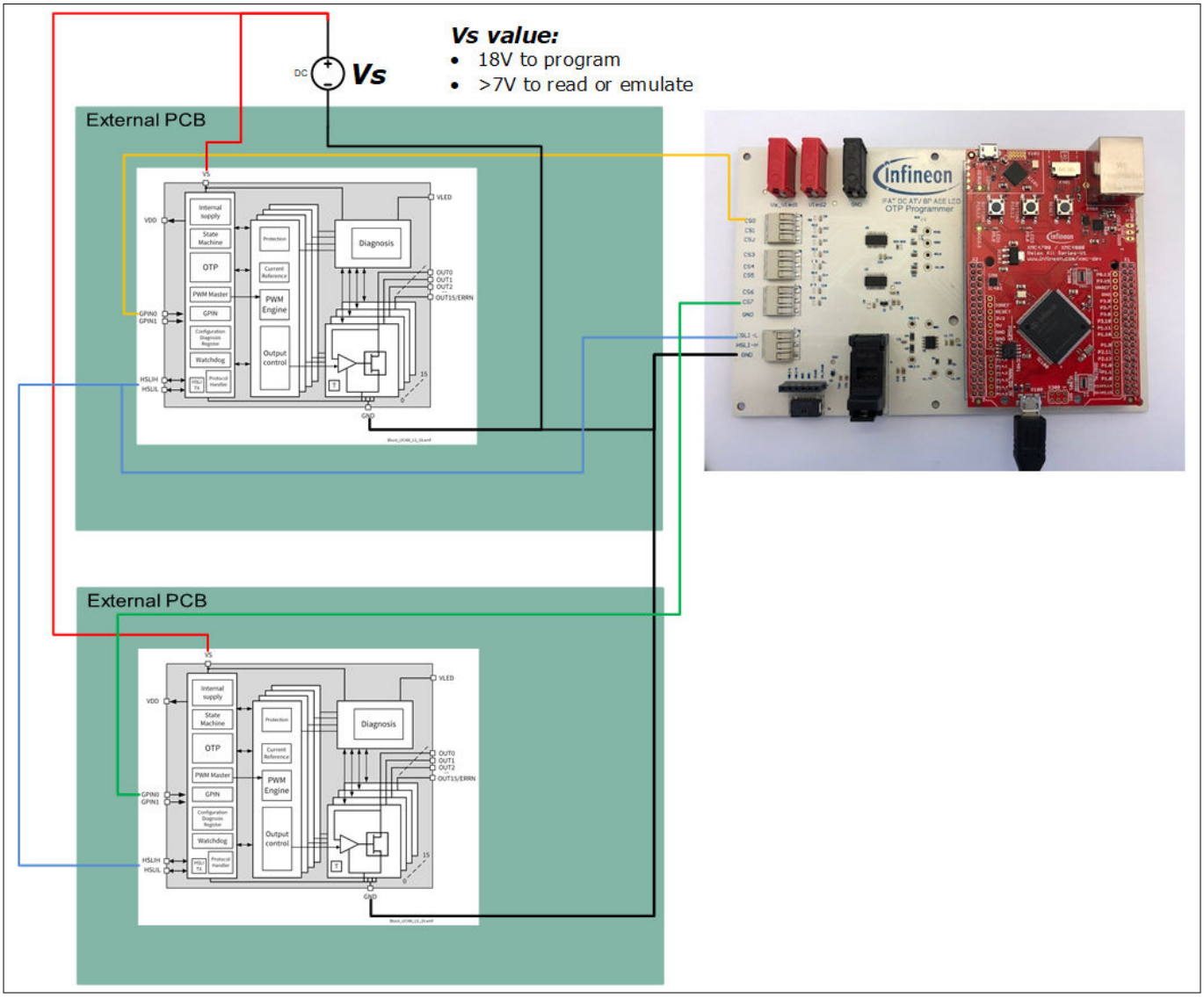


Figure 9 OTP board with multiple devices

3 TLD7002-16ES

3.1 Navigating the OTP configuration

The LITIX™ Pixel Rear Configuration Wizard is a *graphical user interface (GUI)* that enables reading, emulating, and writing of the internal *OTP* of the TLD7002-16ES by using the OTP Programmer board to access the *HSLI* bus.

To speed up the emulation and programming process of multiple devices, the tool implements the functionality to emulate or program up to eight devices at once by defining the OTP content used for each device.

The tool is organized into several tab pages. The following sub-chapters provide a detailed description of each tab page.

3.1.1 OTP Management tab page

On the device selection page select the OTP Programmer board after connecting it to the computer.

***Note**: OTP Programmer boards have the `"OTP Programmer board"` suffix in their names.*

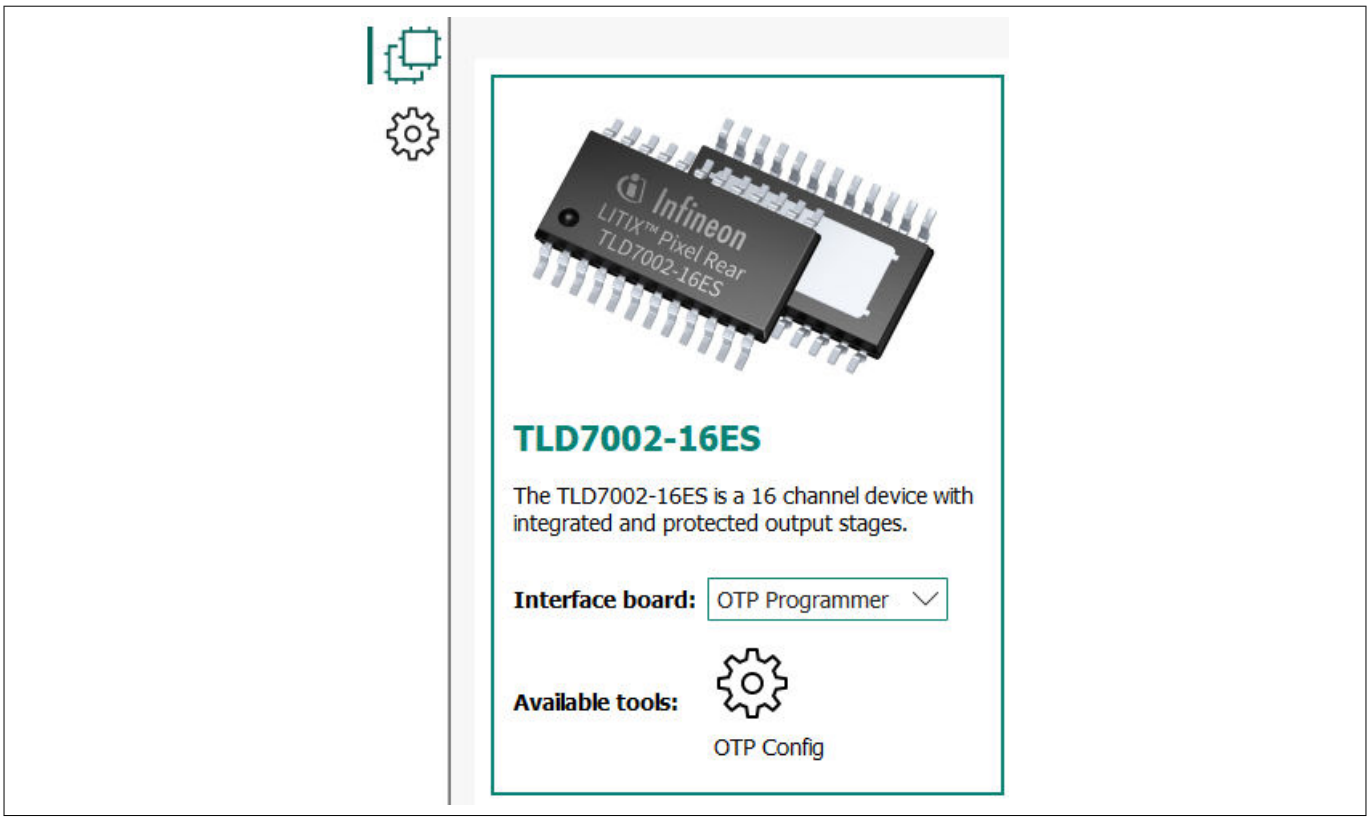


Figure 10 OTP Programmer interface selection

The TLD7002-16ES can configure the OTP using the OTP Programmer board. To access the OTP configuration, click **OTP Config**.

- To manage only one device, select **Single device**. This enables the compact view in the **Register Configuration** tab
- To manage multiple devices simultaneously, select **Multiple devices**

Depending on which OTP configuration management viewing mode you select, you can save your configuration as a file on your PC and load your already saved configuration files.

- When **Single device**mode is selected, you can load `.ocfg` configurations files, which contain a configuration for a single device
- When **Multiple devices**mode is selected, you can load `.bcfg` configuration files, which contain a configuration of up to 8 devices

3.1.1.1 Single device management mode

If you want to work with a single device, flag **Single device**, and select the corresponding **Chip Select** from the dropdown list. Select the chip select that you want to use to program or emulate operations.

The numbers of chip select that you choose in this screen corresponds to the CSx outputs on the OTP Programmer board. For example, selecting `0` for **Chip Select** in the dropdown list corresponds with **CS0** on the board.

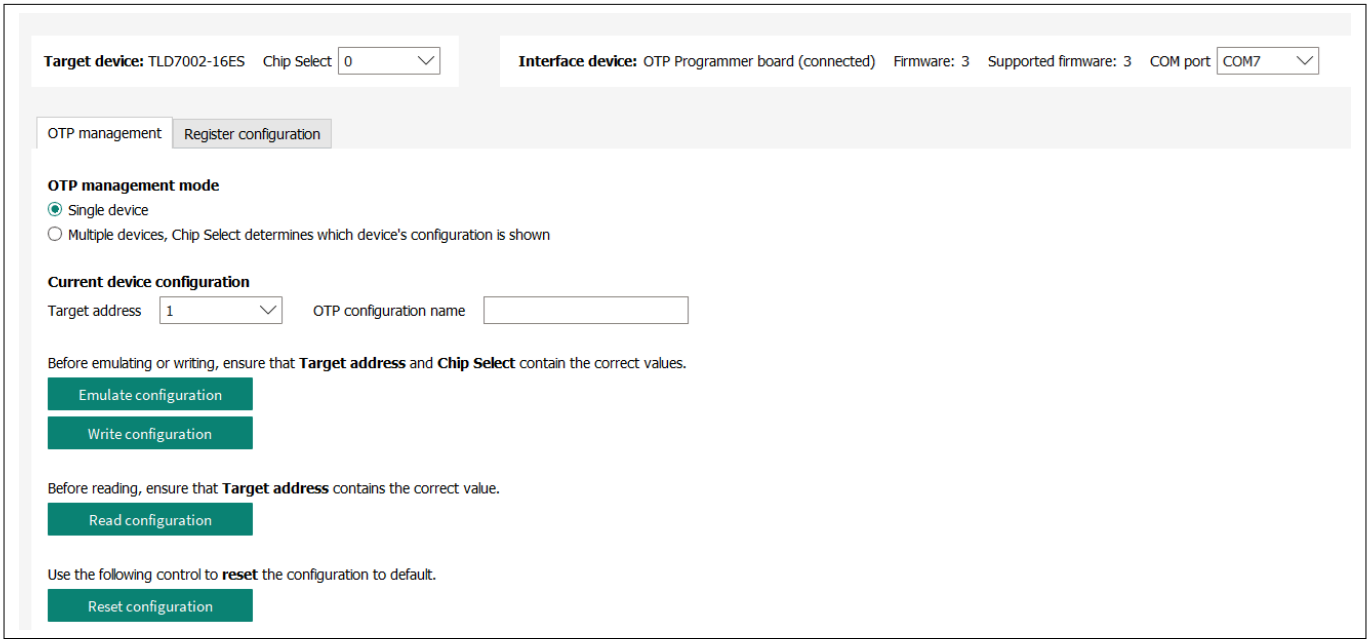


Figure 11 Making selections for single device management

After you choose the mode and chip select, you can define in the **Register Configuration** tab, the configuration for `CFG0 to CFG11`.

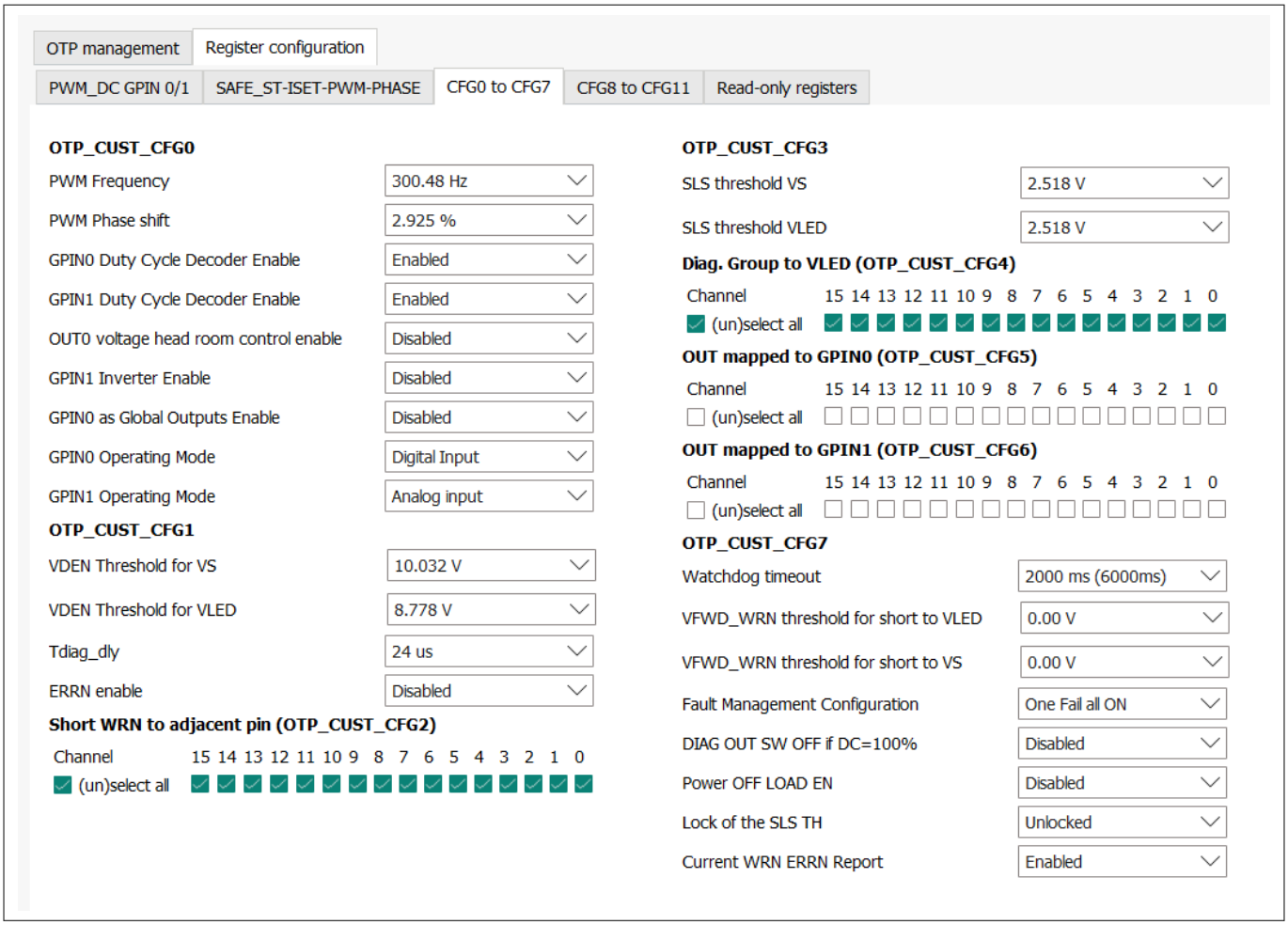


Figure 12 Example of CFG0 to CFG7 configuration

To carry out the correct write/emulate operation, go back to the **OTP management** tab page and click the corresponding functions (**Write configuration**or **Emulate configuration**)

***Note**: If you want to use the read configuration, click **Read configuration**, and define the **Slave ID**. Leave the other input fields empty.*

When you define the *OTP* content, the system displays mouseover descriptions of the OTP parameters in the input fields.

If there is a problem with an input field, a warning icon appears next to input parameters, and a mouseover text describes the problem. The warning icon disappears when the conflict is resolved.

If you request an operation (read, write, or emulate) but one or more required inputs contain(s) an error, the tool reports an error message in the **Logging area**.

If one or more input fields display(s) a warning icon, the system issues a warning dialog when you try to write or emulate a configuration.

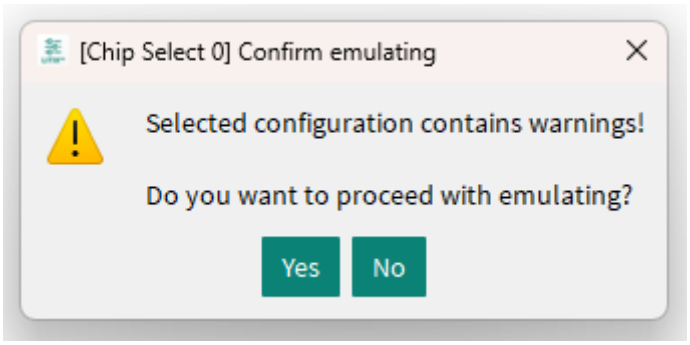


Figure 13 Confirmation dialog for writing with warnings

3.1.1.2 Multiple device management mode

To manage multiple devices at once, select **Multiple devices** in the configuration management viewing mode. This mode provides the same core functionality as the single device mode.

When multiple device viewing mode is selected, the tool also shows the tab **Bulk OTP management**. Here you can emulate and write configurations from the **Register Configuration** tab to multiple devices at once.

1. Select the devices to be emulated or programmed during the writing/emulating sequence on the **Bulk OTP Management** tab page
2. Select the devices, then run the sequence by clicking **Emulate selected** and **Write selected**
3. Check the execution status of the operation in the **Logs** area
4. To load and save the bulk configuration, click **File** >> **Load** >> **Save**

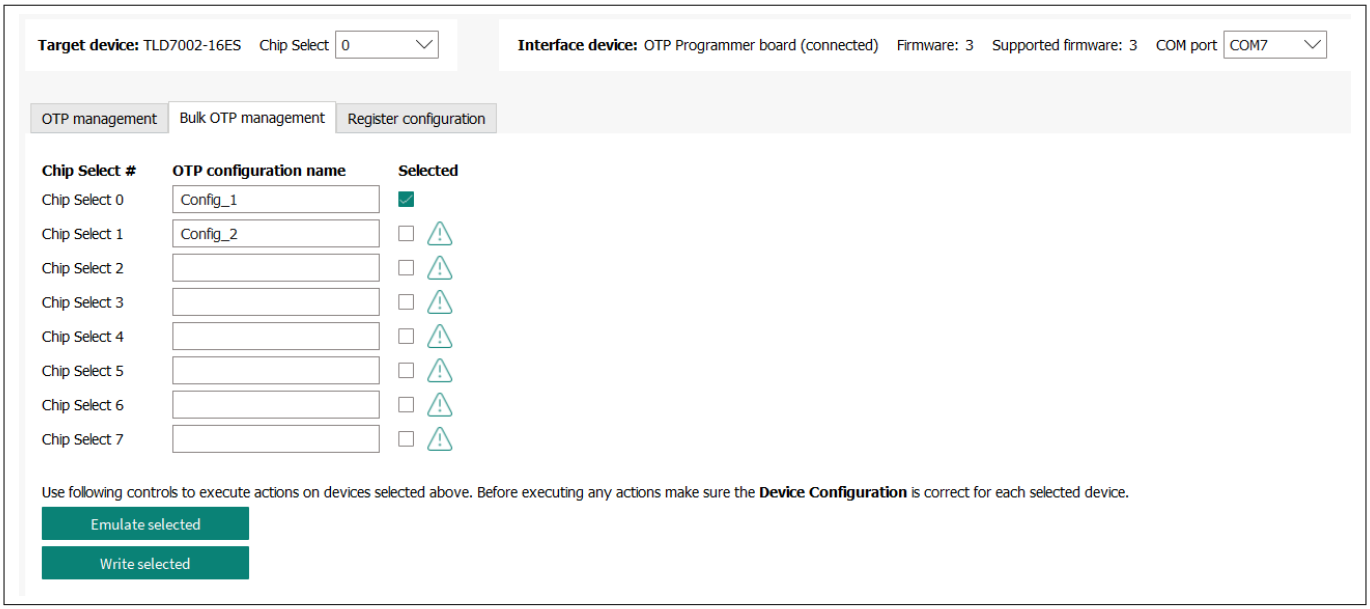


Figure 14 Bulk OTP management

3.1.1.3 Save and load configurations

You can use the LITIX™ Pixel Rear Configuration Wizard to save configurations as a file on your computer, and to load a previously saved configuration.

Saving a configuration to a file does not require all parameters to be defined.

There are two types of configurations in the tool:

- Single device OTP configuration
- Bulk OTP configuration

To load either type of configuration, click **File** >> **Load**, depending on which OTP Configuration Management viewing mode is selected at the time. Single device OTP configuration files have the file extension `.ocfg`. Bulk OTP configuration has file extension `.bcfg`.

Loading configuration from a file: the extension of this is `.txt`, which loads the register configuration array.

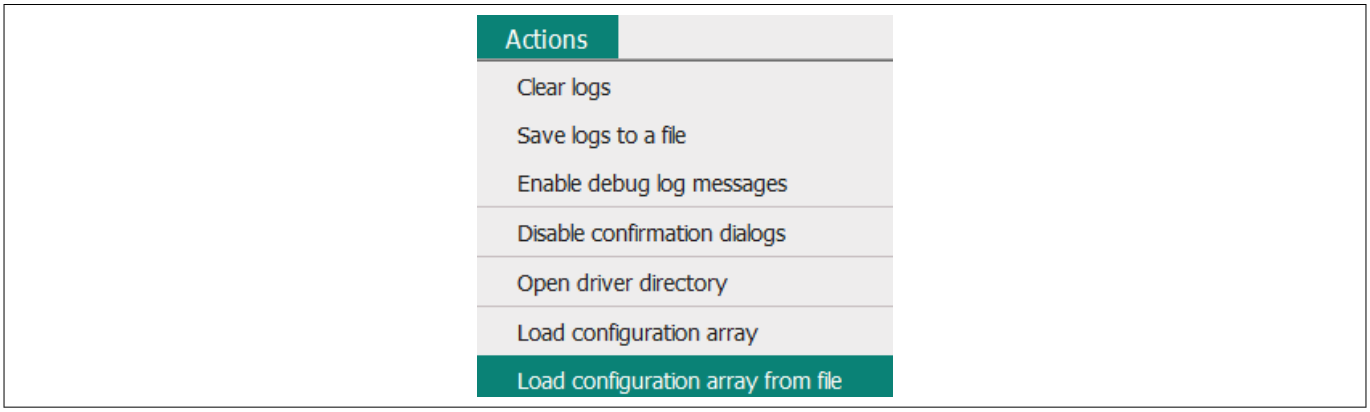


Figure 15 Loading configuration from a file

You can load the configuration array, which is located at the end of the `.ocfg` saved file in the HEX_DATA_16BIT field, directly in the text box or from a `.txt` file as a 40 words, comma separated list, for example , 0x3300, 0x3333, ……, 0x00EE.

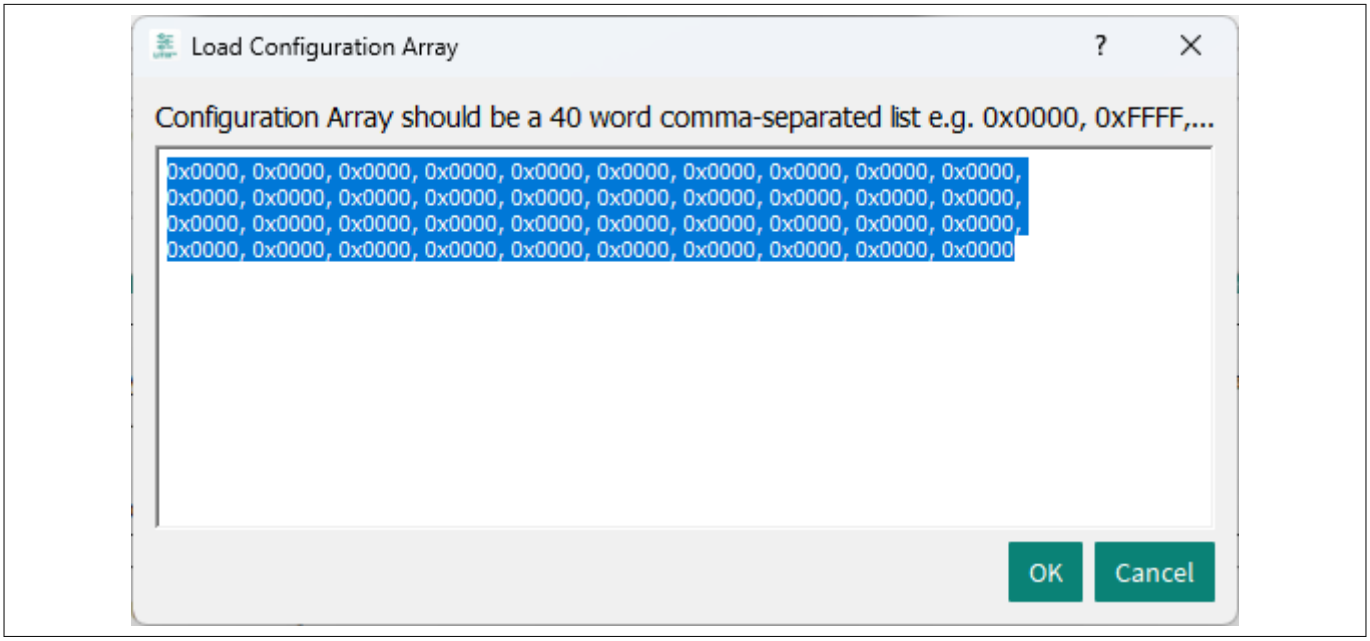


Figure 16 Loading a configuration from a hex array

3.1.2 Register configuration tab page

To specify the define memory content of your device(s), follow the steps below.

1. Select **Multiple device**on the **OTP managment** tab page
2. Define the configuration for up to eight devices

Device configuration tab - Single device mode

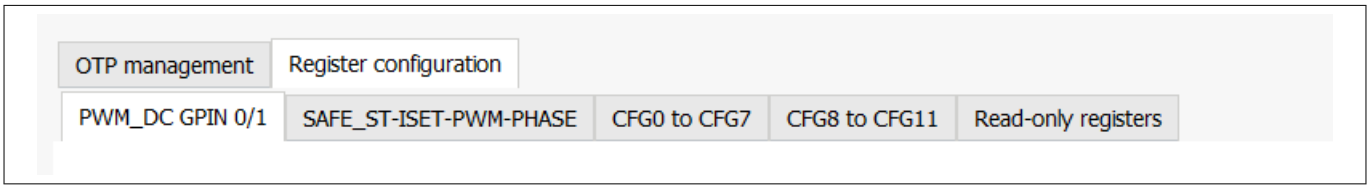


Figure 17 OTP read/write for single device management mode

Device configuration tab - Multiple device mode

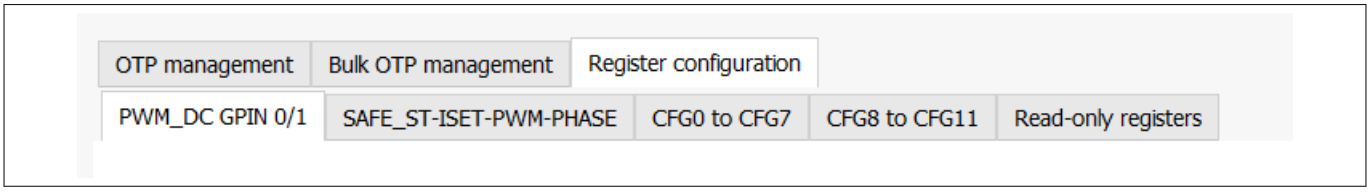


Figure 18 OTP read/write for multiple device management mode

Each device's configuration tab page consists of 6 sub-tabs, in the following order:

- OTP Management
- PWM_DC GPIO0/1
- SAFE_ST-ISET-PWM-PHASE
- CFG0 to CFG7
- CFG8 to CFG11
- Read-only registers

The **OTP Management** tab page contains all controls related to the selected configuration, and all the other tabs are solely configuration input tabs.

OTP Management tab page

Use this tab page to:

- Define the target address
- Control the OTP configuration management

A detailed description of these controls is provided in the table below.

3 TLD7002-16ES

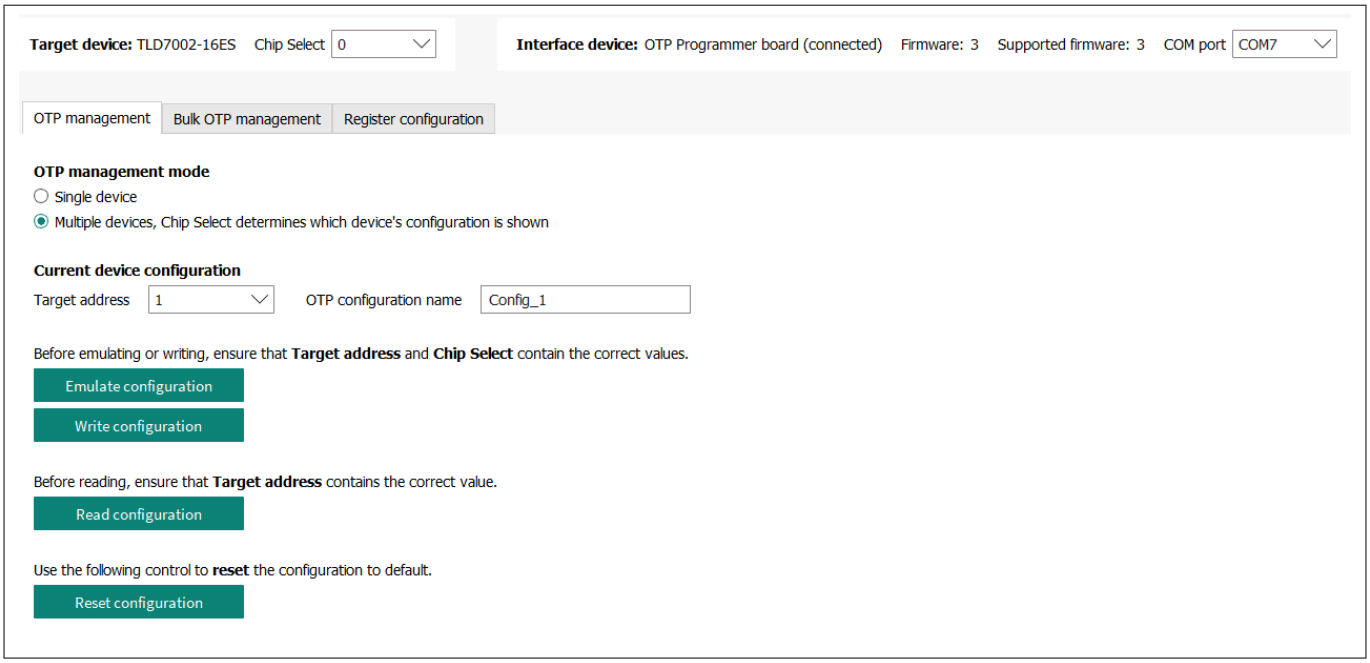


Figure 19 OTP Management (in multiple device management mode)

Action	Description
Emulate configuration and **Write configuration**	These buttons are used to emulate or write OTP memory content of a device. The device is to be connected to the bus and to the CSx of the OTP Programmer board output selected in the tool. The OTP memory content can be defined on the tab pages from **OTP Management** `CFG8 to CFG11`. Important: • You must enter data in all fields • To emulate configuration, the device must have a supply of *VS* higher than 7 V • To write configuration, the device must be supplied with VS of 18 V • The device must be connected to the HSLI bus • The CSx OTP Programmer board output selected on the **General** tab page must be connected to the GPIN0 of the device

Action	Description
Read configuration	Use this button to launch the read operation of the *OTP* content of a device connected to the bus with the target address. The OTP content is shown in the input fields available on the sub-tab pages of the **OTP Management** tab page. ***Note**:* • *The device requires a supply and must connect to the HSLI bus of the OTP Programmer board* • *Only one target address can be present on the* HSLI *bus* • *For this operation, CSx outputs of the OTP Programmer board are not necessary*
Reset configuration	Click **Reset** to reset the configuration to default.

3.1.3 Updating the OTP Programmer board firmware

If the LITIX™ Pixel Rear Configuration Wizard reports the following error message, you must update the firmware for the OTP Programmer board.

***Note**:* *The firmware of this program is newer than the one on the board. Update the firmware of the board first (see help for instructions).*

Before updating the firmware :

1. Download J-Link Software and Documentation pack (version tested: V7.62b), https://www.segger.com/downloads/jlink,

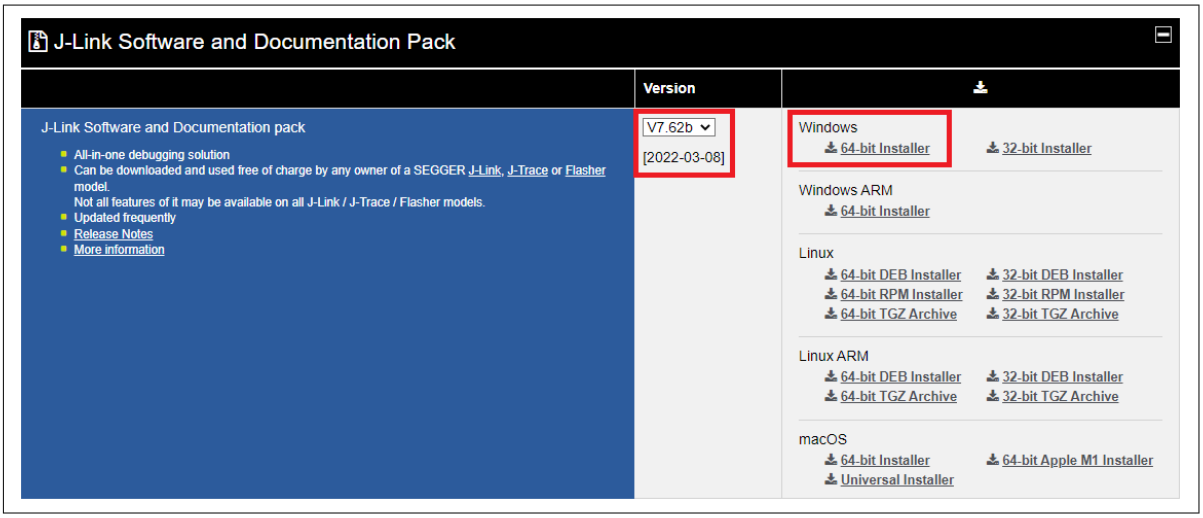


Figure 20 J-Link software download

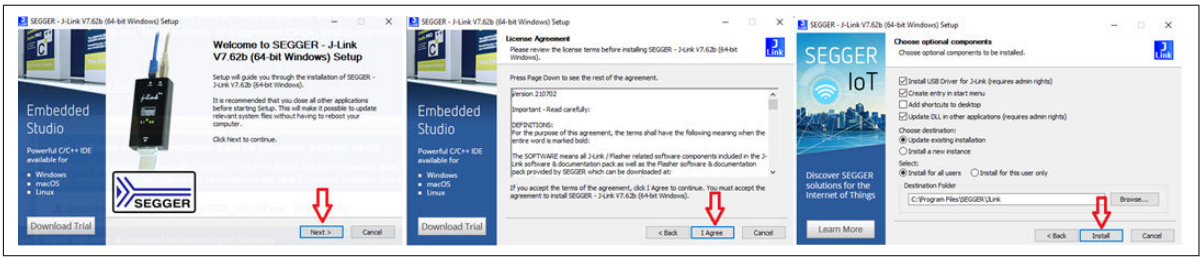


Figure 21 J-Link software installation

2. Download the latest firmware `.hex` file from https://softwaretools.infineon.com/tools/com.ifx.tb.tool.otpprogrammerfirmware

The table below shows which version of the firmware is required, depending on the currently installed version of the tool.

Table 5 OTP Programmer board firmware compatibility

Pixel Rear Configuration Wizard	Firmware
v2.1.0	0x03
v1.3.0	0x03
v1.1.1	0x03

To update the firmware:

1. Connect the debug port of the OTP board to a PC using a micro-USB cable

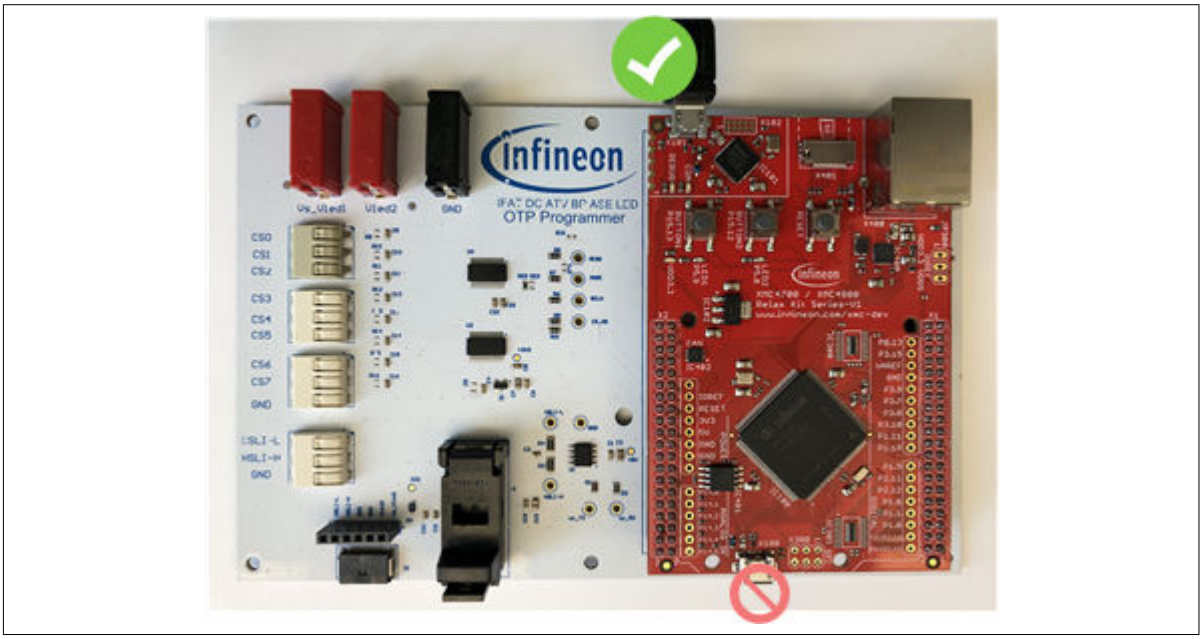


Figure 22 OTP board debug USB connection

2. Open the J-Flash Lite software on your PC. (You can search for it using the Windows search bar), then select the device and the interface, as shown in the figure below. Click **OK**

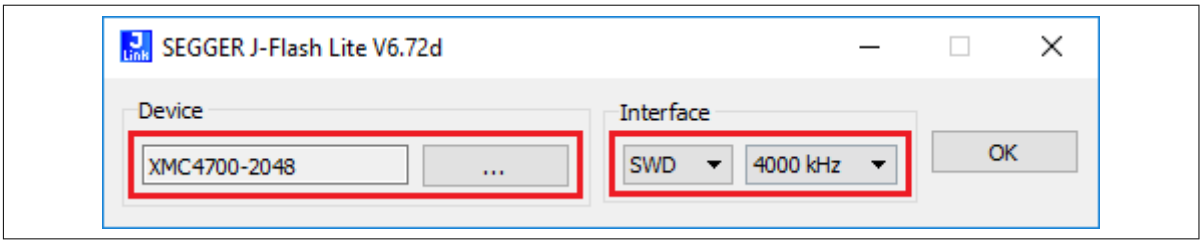


Figure 23 J-Flash Lite board selection

3. From the following screen, select the firmware `.hex` file (1) that you downloaded as a data file, then click **Program Device**(2)

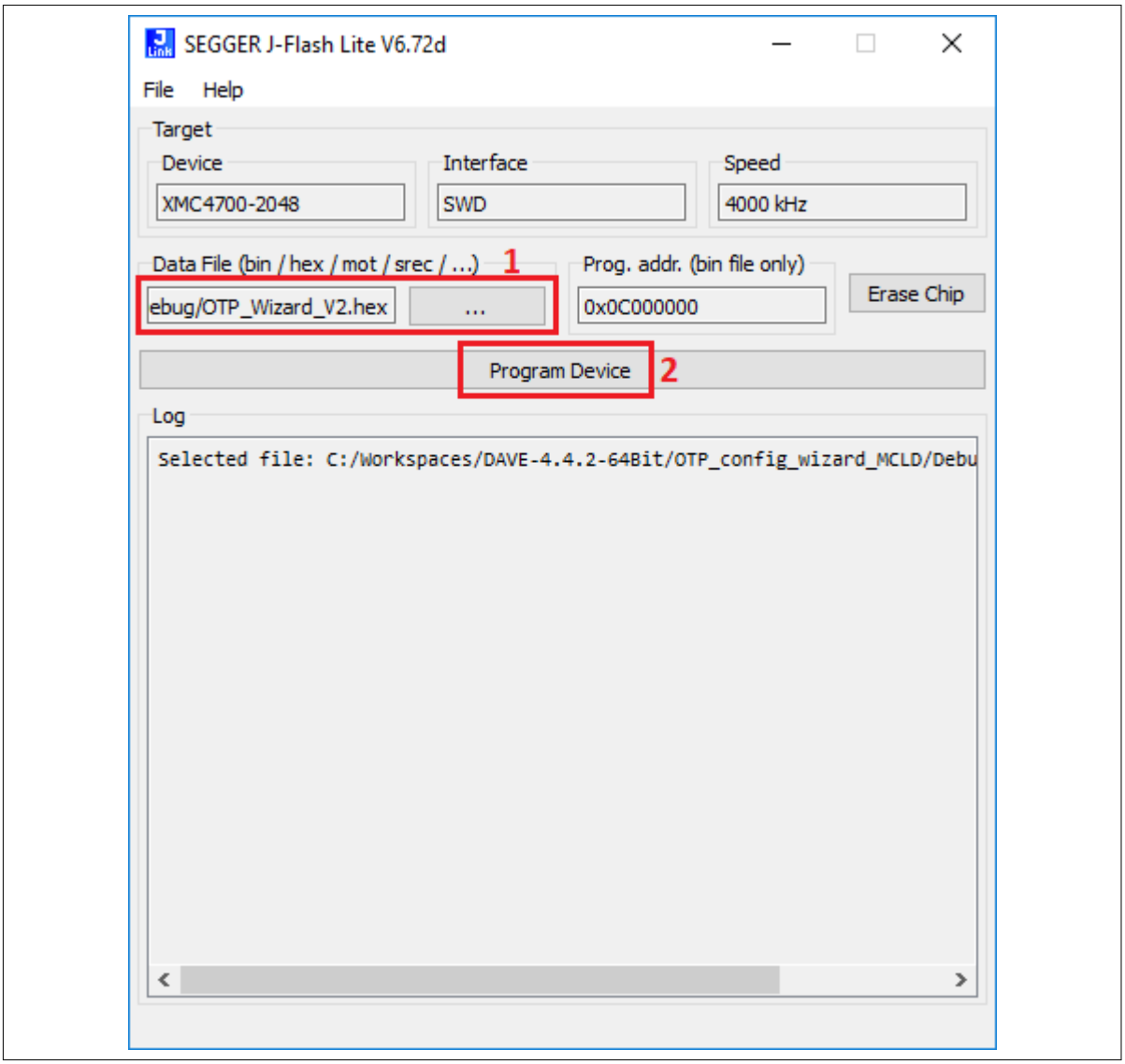


Figure 24 J-Flash Lite board flashing

4. Perform a power cycle of the board by removing all USB connections

***Note**: If the J-Flash Lite software is not able to recognize the OTP Programmer board, ensure the following:*

- *The USB cable is connected to the debug USB port on the board*
- *The USB drivers from J-Link are installed (to reinstall the J-Link Software and Documentation pack, flag the checkbox **Install USB Driver for J-Link (requires administrator rights)**)*

3.2 Navigating to HSLI control

The **HSLI Control** tool can be found only if the Interface board: uIO Stick is selected.



Figure 25 Overview of the HSLI control

Table 6 Sections in HSLI control

Number	Area name	Information
1	**Device**	TLD7002-16ES device information
2	**Board**	*uIO-Stick* information
3	**Diagnostic feedback**	TLD7002-16ES diagnostic feedback
4	**Basic mode**	Basic HSLI control (current and duty cycle channel)
5	**Animations**	Some animations examples
6	**Expert mode**	Complete HSLI control
7	**Batch mode**	It allows the rapid development and testing of command sequences that would be executed on a microcontroller

These sections are described in more detail in this chapter.

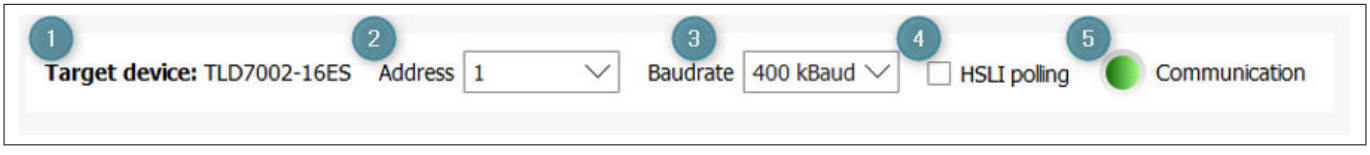


Figure 26 Target device: TLD7002-16ES

Table 7 1. Device

Number	Information
1	Target device

(table continues...)

Table 7 (continued) 1. Device

Number	Information
2	Target **Slave ID**
3	Change communication baud rate between uIO-Stick and target device
4	Enable/disable **HSLI polling** (prevents target from entering fail safe mode)
5	Indicates if communication with the Slave is possible (if Slave replied to last request)



Figure 27 Interface device: uIO-Stick information

Table 8 2. Board

Number	Information
1	uIO-Stick connection status
2	uIO-Stick board firmware
3	Support firmware version

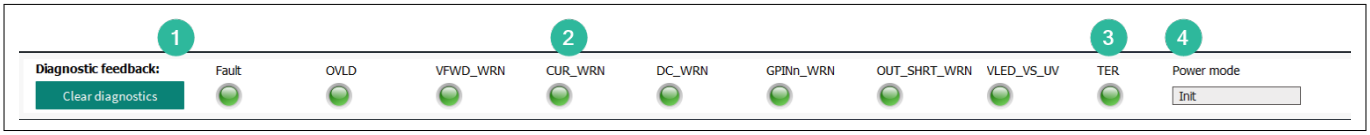


Figure 28 Target diagnostic

Table 9 3. Target diagnostic

Number	Information
1	**Clear diagnostics** of TLD7002-16ES
2	Status flags (from output status byte)
3	Status flag (from ACK byte)
4	**Power mode** of device (from ACK byte)

Diagnostic feedback is received in the Output status byte and ACK byte of Slave responses. Therefore a HSLI command has to sent or HSLI polling has to be enabled for diagnostic feedback to update.

The **Basic mode** tab allows basic HSLI control of the target device:

3 TLD7002-16ES

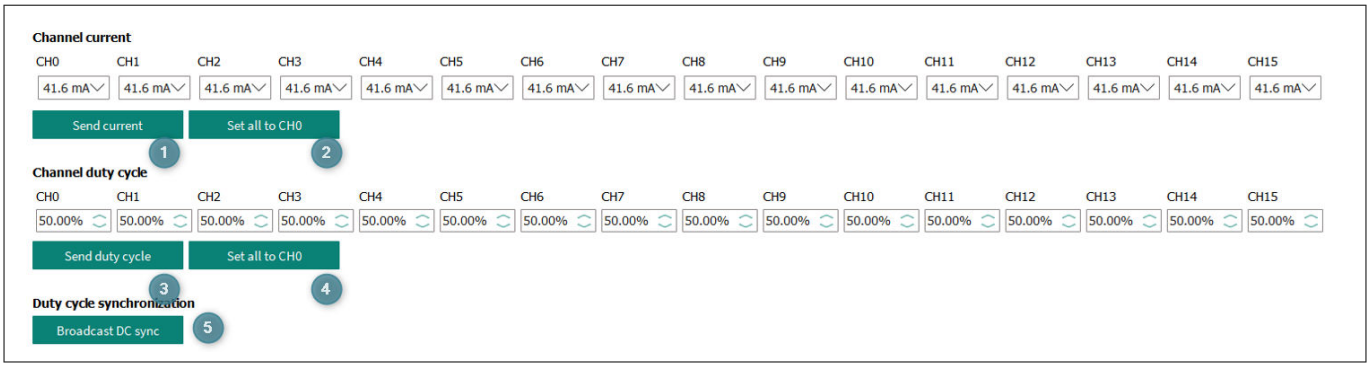


Figure 29 Basic HSLI control (current and duty cycle channel)

Table 10 4. Basic mode

Number	Information
1	**Send current**: Current is written to the device with a WRITE_REG DLC6 to address 0x10
2	**Set all to CH0**: Convenience function to copy current value of CH0 to all other channels
3	**Send duty cycle**: Percentage is converted to 14-bit value and sent with a DC_UPDATE frame ***Note**: "Broadcast DC sync" is required to change the duty cycles to the updated values*
4	**Set all to CH0**: Convenience function to copy current value of CH0 to all other channels
5	**Broadcast DC sync** (DC_SYNC frame), needed after DC_UPDATE

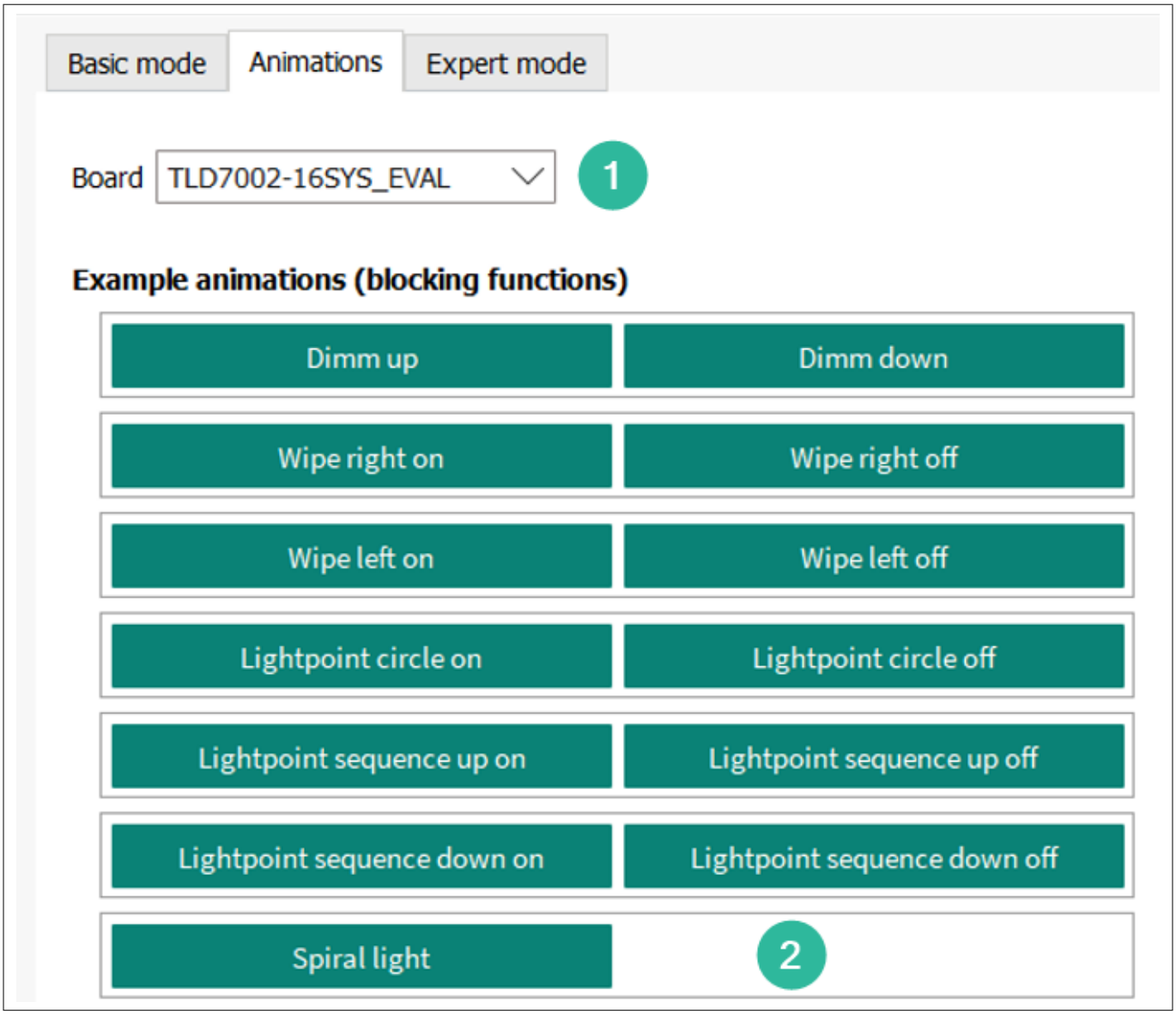


Figure 30 **Some animations examples**

Table 11 **5. Animations**

Number	Information
1	Evaluation board selection, is necessary because different boards have different LED layout
2	Example animations

On the **Expert mode** tab page, you can send various commands to the target device. You can also see the commands on the **batch mode** tab page.

3 TLD7002-16ES

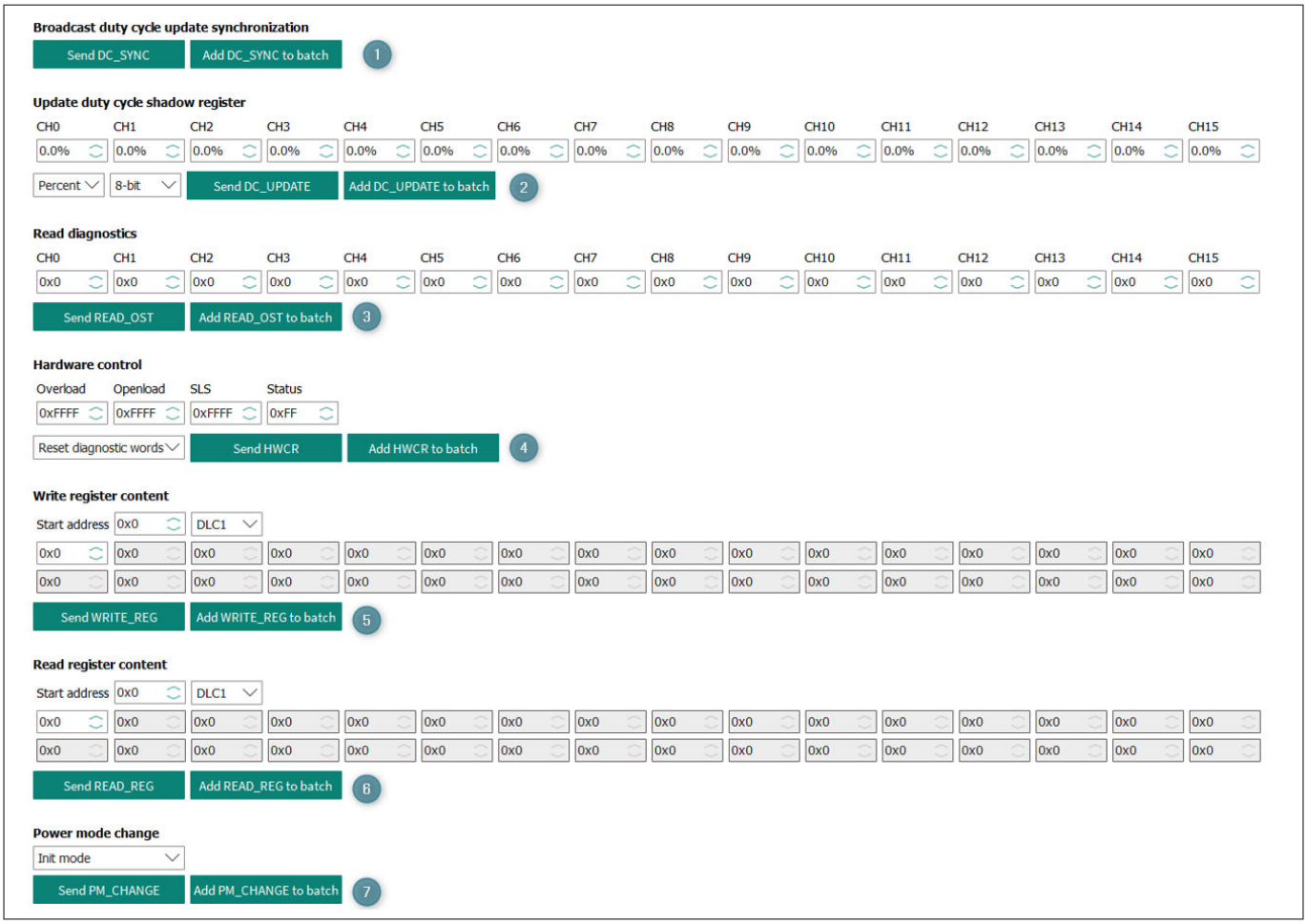


Figure 31 Complete HSLI control

Table 12 6. Expert mode

Number	Information
1	**Send DC_SYNC**: Duty cycle synchronization is needed to use updated duty cycle values
2	**Send DC_UPDATE**: Update duty cycle with either 8-bit or 14-bit values. A DC_SYNC must follow
3	**Send READ_OST**: Read channel diagnostics. Refer to the TLD7002-16ES datasheet for the meaning of the flags
4	**Send HWCR**: Send hardware control frame. If **Output status only** is selected no flags are reset. This is useful for getting diagnostic feedback without changing the state of the device. To reset the flags select **Reset diagnostic words**
5	**Send WRITE_REG**: Write device registers starting from start address. Number of registers written is determined by the data length code (DLC). Changing the DLC changes the number of enabled spinboxes
6	**Send READ_REG**: Read device register from start address. Number of registers read is determined by the DLC. Changing the DLC changes the number of enabled spinboxes
7	**Send PM_CHANGE**: Change power mode of device

The **Batch mode** tab page enables you to manage and filter incoming responses based on user-defined parameters. In general, the tab page covers the following functionalities:

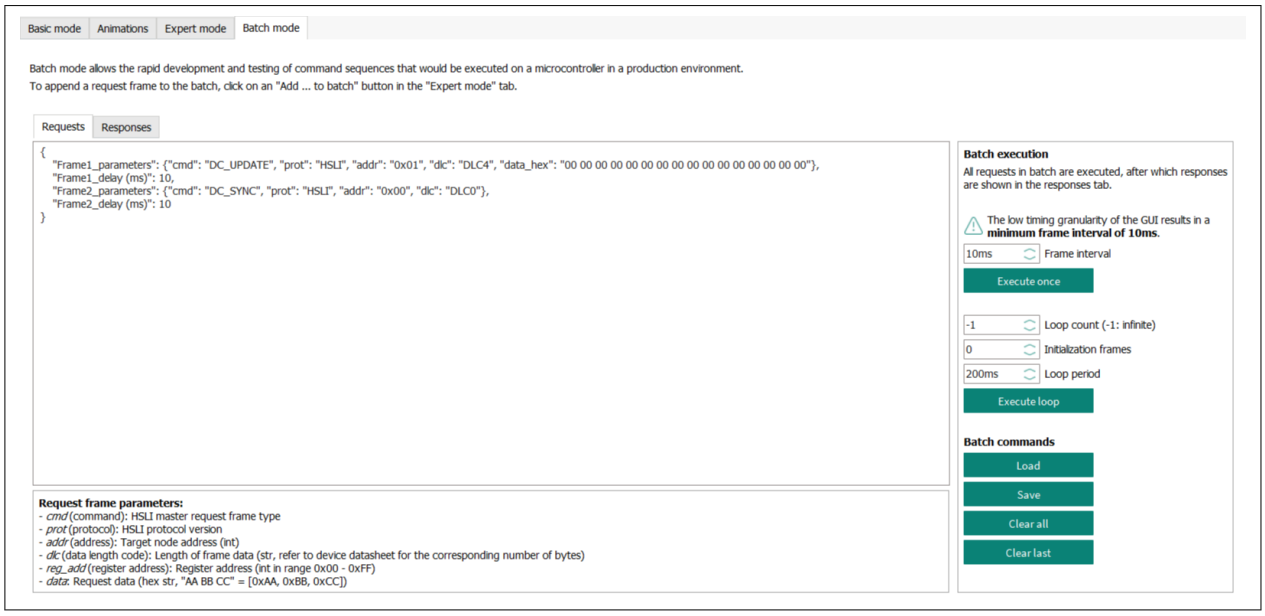
- **Batch Execution:** All requests in the batch are executed, after which responses are shown in the responses tab
  - **Delay time constraint:** You cannot set a delay time smaller than 10ms, ensuring a minimum time interval between commands
  - **Accuracy warning:** The accuracy of the delay time relies on the granularity of USB commands, resulting in an approximate delay time of XX ms (to be measured). This warning highlights the potential for variability in delay times
  - **Loop time warning:** If a series of commands and delays is smaller than the loop time, a warning is displayed to alert users of potential issues
- 

Figure 32 Batch mode: Requests example

- **Filter parameters:** You can set filter parameters to control the display and discarding of incoming responses. These parameters include:
  - Address
  - Frame type
  - Output status byte
- **Response filtering:** Incoming responses are filtered based on the parameters set, allowing users to focus on relevant data and discard unnecessary information

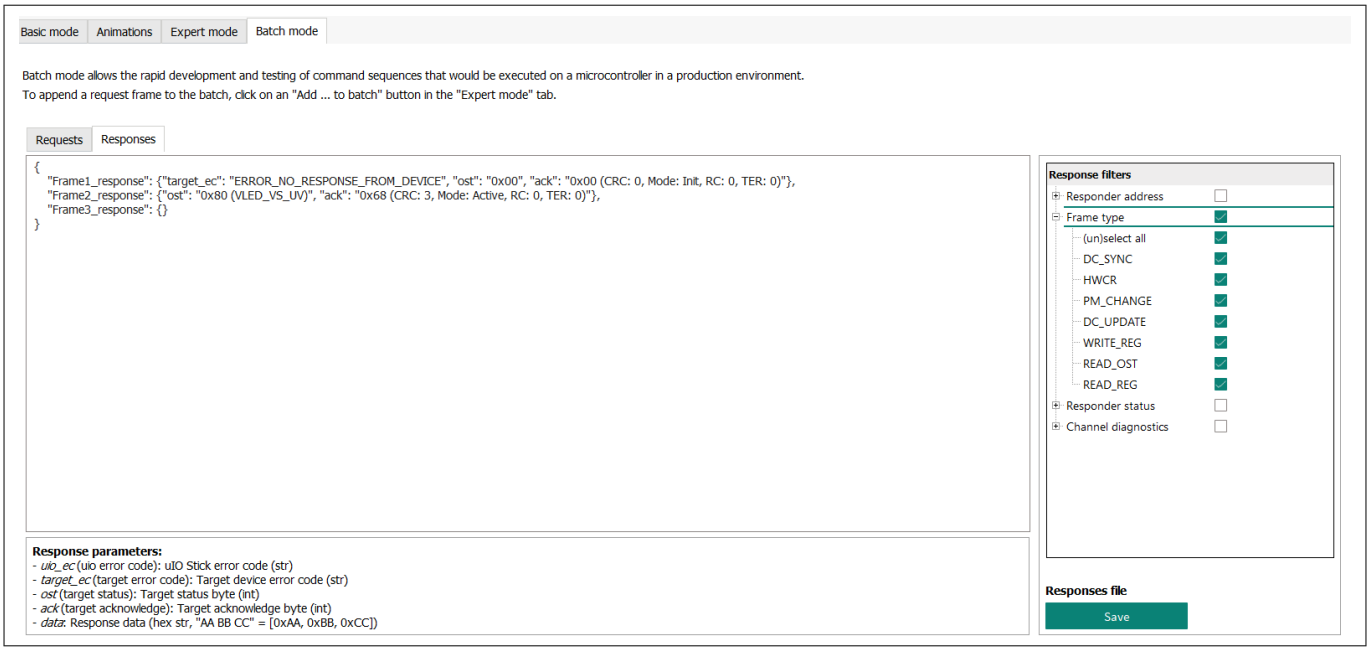


Figure 33 Batch mode: Responses example

4 General tool features

4.1 Knowledge base articles

Knowledge base articles address common questions, issues, and best practices related to your most recent interaction in the tool.

To access the knowledge base articles, click the icon at the top right of the screen, as shown in the figure below. The **Knowledge Base Articles** dialog box appears, and shows a list of articles. Once you have identified a relevant article, click to open it on the Infineon Developer Community website. We regularly update the knowledge base articles.

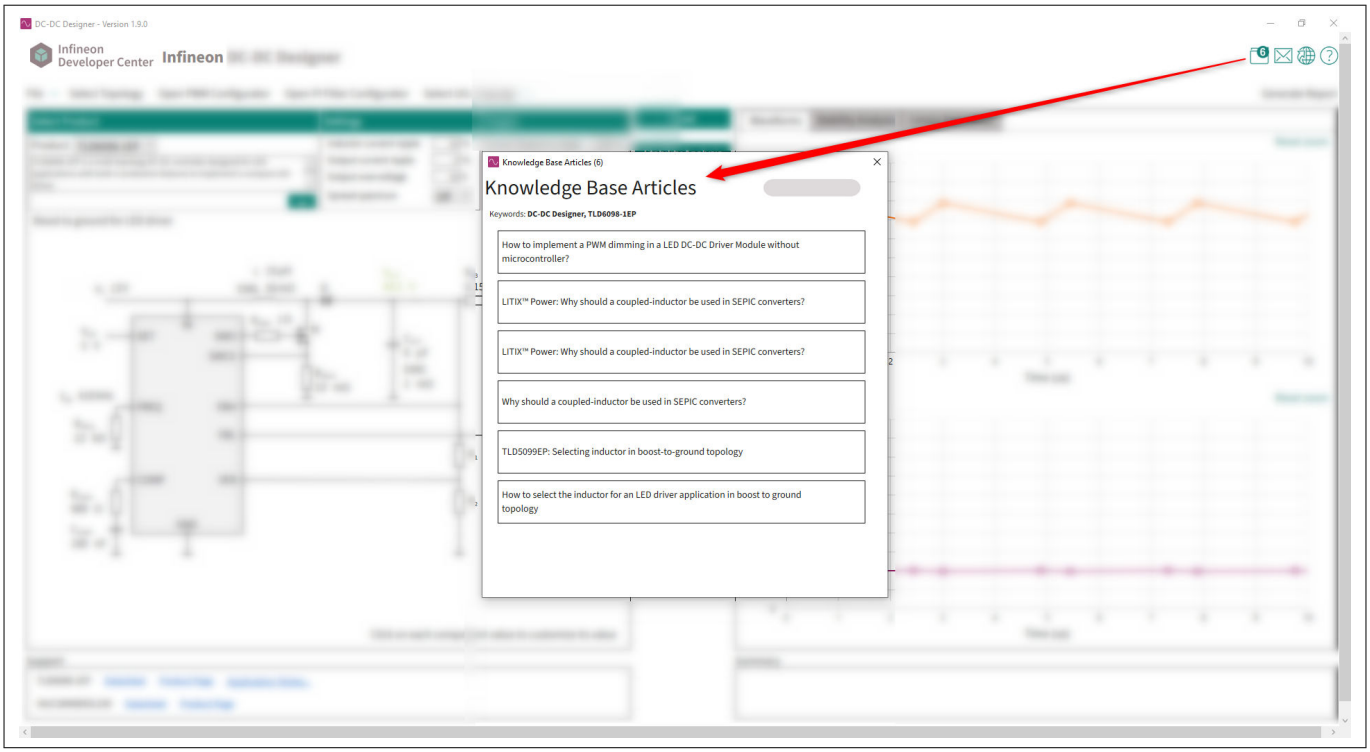


Figure 34 Relevant article in the knowledge base

For more support options, refer to Contact us.

4.2 Logs

The OTP tool contains a **Logging area**, in which status messages of all operations are reported.

To clear logs, click **Clear logs**, from the **Actions** menu or right click on the **Logging area** and select clear console. To save logs as a text file, call **Save logs to a file** from the **Actions** menu.

Error messages are reported in red. Success messages, info messages, and warning messages are reported in green, gray, and orange, respectively.

5 How to switch languages

To translate the User Guide into a preferred language, it is recommended to use the browser's translation feature. The steps provided below explain how to change the language settings in the Edge browser. Steps may differ slightly depending on your browser version and operating system.

Enable automatic translation from Edge Browser

1. On your Edge browser go to "Settings" → "Languages"
2. Click “Add language” under "Preferred languages"
3. In the translation section, turn on "Offer to translate pages that aren't in a language I read"
4. Once you open your user guide go to the tree dots on your browser and click on translate

5 How to switch languages

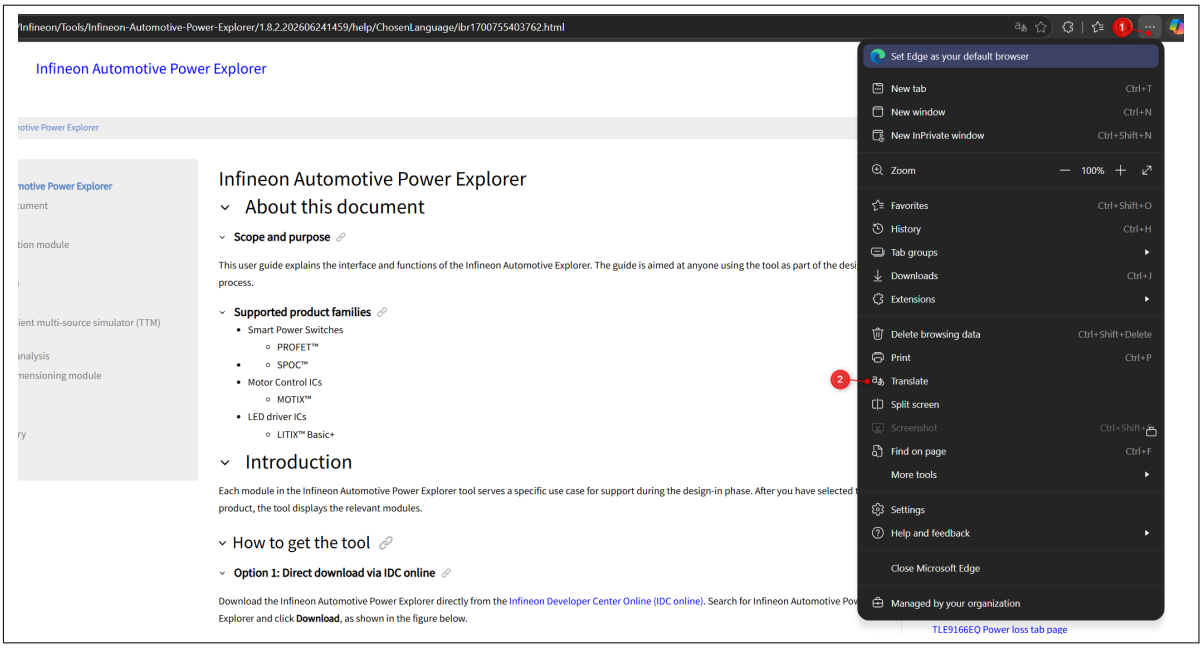


Figure 35 Example: Open the User Guide and change the language

5. Clicking on translate will open the several options you have and after selecting your desired language, the content of the user guide will be in the selected language. E.g. Spanish

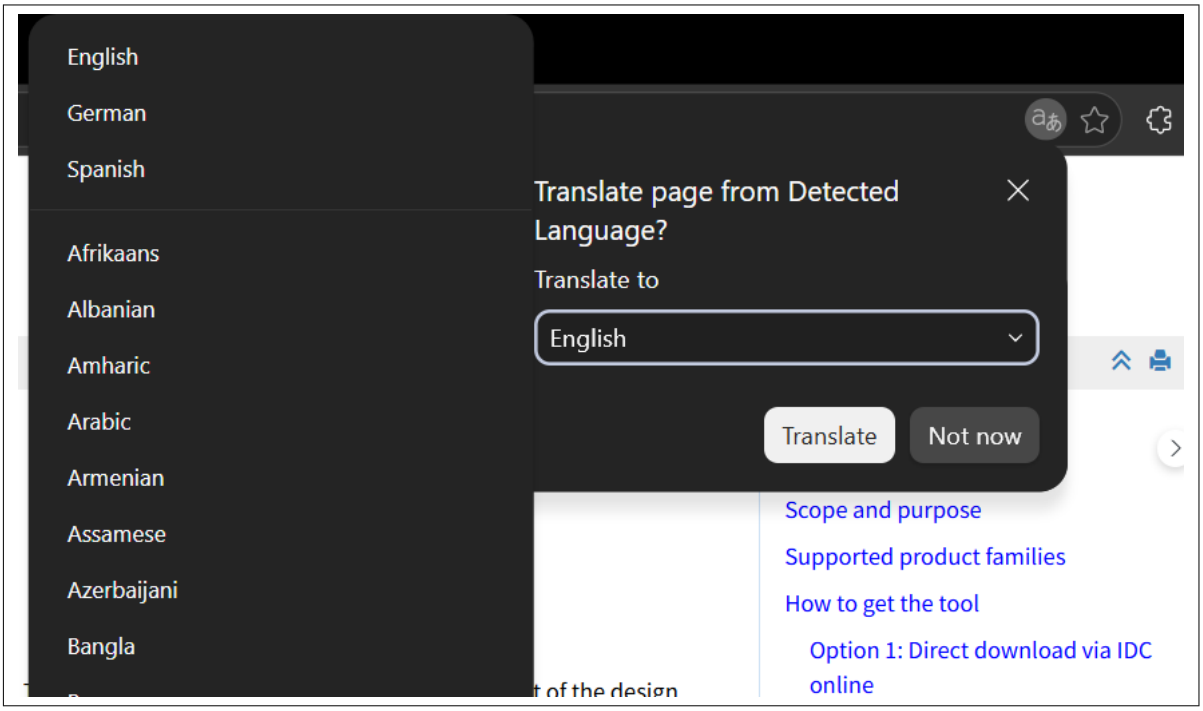


Figure 36 Example: Translate to <Your Language>

6. You can always go back to the English version of it, by clicking on “Show Original”

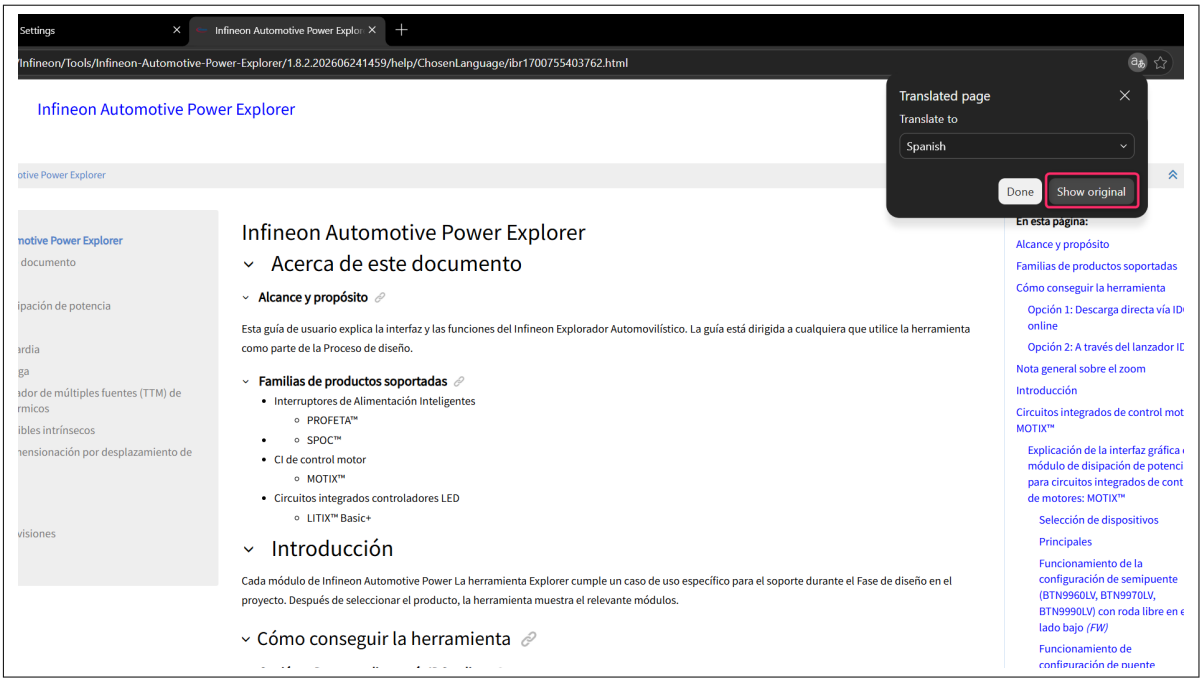


Figure 37 Example: Translate page and show original

6 Contact us

To get in contact with our support about the following topics, click the link www.infineon.com/services:

- A bug
- An improvement idea
- A new feature request
- A support request

Glossary

GUI

graphical user interface (GUI)

An interface that enables users to interact with electronic devices through icons and visual indicators.

HSLI

high-speed lighting interface (HSLI)

An Infineon-specific protocol for exchanging data among leaders and lighting-device followers.

OTP

one-time programmable memory (OTP)

A form of memory that permits data to be written only once. After writing, the data is stored permanently and cannot be further changed.

uIO-Stick

uIO-Stick

An interface device for controlling Infineon boards or kits during runtime.

USB

universal serial bus (USB)

An industry standard that defines cables, connectors, and communication protocols used in a bus for connection, communication, and power supply between computers and electronic devices.

VS

voltage supply (VS)

Revision history

Document version	Date of release	Description of changes
Rev. 1.50	2026-09-18	• Editorial changes
Rev. 1.40	2026-06-26	• Several structural changes together with minor editorial changes
Rev. 1.20	2025-03-20	• Editorial changes • New content on batch mode in chapter Navigating to HSLI control • Glossary added
Rev 1.10	2024-11-29	• Update overview • Added: uIO-Stick support • Added: HSLI control description • Rename tool and user guide
Rev. 1.00	2024-01-16	• Initial version

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