

# PROFET™ +2 12V - Daughterboard description

## User guide

### About this document

#### Scope and purpose

This document explains the latest evaluation boards for the PROFET™ +2 12V family. It focuses on the [daughterboard \(DB\)](#)s, which are part of a kit that includes a [motherboard \(MB\)](#). The DB requires a MB to function, as they are not functional as standalone components. For information on the MB, visit the company website.

#### Intended audience

This user guide is intended for automotive electronics engineers, designers, and technicians who are responsible for the development, testing, and implementation of high side switch solutions using the PROFET™ +2 12V family of devices. The target audience includes individuals who require a detailed understanding of the DB's features, functionality, and operation, as well as those who need to integrate the device into their automotive systems. This document is also relevant for application engineers, system designers, and quality assurance personnel who are involved in the development and validation of automotive ECUs that utilize high-side switch technology. Additionally, the guide may be useful for students and researchers in the field of automotive electronics who are interested in learning about the application and implementation of high side switch devices in automotive systems. The intended audience is assumed to have a basic understanding of automotive electronics, electrical engineering principles, and microcontroller-based systems.

#### About this product group

##### Target applications

- Headlight control modules
- Fuel pump control
- Horn and accessory control
- Battery management systems
- Heated seat and window control
- Trailer and tow control
- Body control modules

##### Product family

PROFET™ +2 12V

**Note:** [printed circuit board \(PCB\)](#) and auxiliary circuits are NOT optimized for final customer design.

Safety precautions

## Safety precautions

**Note:** Note the following warnings regarding the hazards associated with development systems.

**Table 1** Safety precautions

	<p><b>Warning:</b> The DC-link potential of this board is up to 1000 VDC. When measuring voltage waveforms by oscilloscope, high voltage differential probes must be used. Failure to do so may result in personal injury or death.</p>
	<p><b>Warning:</b> The evaluation or reference board contains DC bus capacitors which take time to discharge after removal of the main supply. Before working on the drive system, wait five minutes for capacitors to discharge to safe voltage levels. Failure to do so may result in personal injury or death. Darkened display LEDs are not an indication that capacitors have discharged to safe voltage levels.</p>
	<p><b>Warning:</b> The evaluation or reference board is connected to the grid input during testing. Hence, high-voltage differential probes must be used when measuring voltage waveforms by oscilloscope. Failure to do so may result in personal injury or death. Darkened display LEDs are not an indication that capacitors have discharged to safe voltage levels.</p>
	<p><b>Warning:</b> Remove or disconnect power from the drive before you disconnect or reconnect wires, or perform maintenance work. Wait five minutes after removing power to discharge the bus capacitors. Do not attempt to service the drive until the bus capacitors have discharged to zero. Failure to do so may result in personal injury or death.</p>
	<p><b>Caution:</b> The heat sink and device surfaces of the evaluation or reference board may become hot during testing. Hence, necessary precautions are required while handling the board. Failure to comply may cause injury.</p>
	<p><b>Caution:</b> Only personnel familiar with the drive, power electronics and associated machinery should plan, install, commission and subsequently service the system. Failure to comply may result in personal injury and/or equipment damage.</p>
	<p><b>Caution:</b> The evaluation or reference board contains parts and assemblies sensitive to electrostatic discharge (ESD). Electrostatic control precautions are required when installing, testing, servicing or repairing the assembly. Component damage may result if ESD control procedures are not followed. If you are not familiar with electrostatic control procedures, refer to the applicable ESD protection handbooks and guidelines.</p>
	<p><b>Caution:</b> A drive that is incorrectly applied or installed can lead to component damage or reduction in product lifetime. Wiring or application errors such as undersizing the motor, supplying an incorrect or inadequate AC supply, or excessive ambient temperatures may result in system malfunction.</p>
	<p><b>Caution:</b> The evaluation or reference board is shipped with packing materials that need to be removed prior to installation. Failure to remove all packing materials that are unnecessary for system installation may result in overheating or abnormal operating conditions.</p>



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### 1 General information

## 1 General information

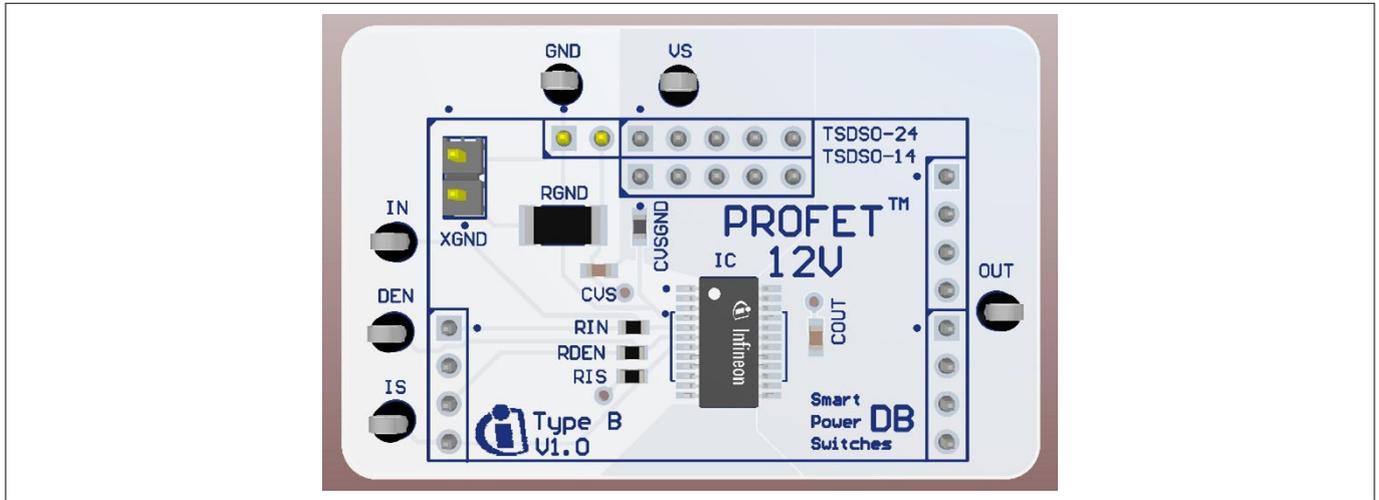
It is essential to note that the setup and operation of the *DB* are contingent upon its combination with a *MB*. The two components are designed to work in tandem, and the DB's functionality is optimized when used with a MB. For users requiring information regarding the MB, including its specifications, setup, and operation, Infineon recommends consulting the relevant documentation available on the homepage. This will provide a more comprehensive understanding of the entire evaluation kit and its capabilities.

The usage of a ground resistor RGND is recommended. Therefore, it is suggested to close the jumper XGND. Depending on which DB is plugged onto the MB, a different number of banana jacks OUT0 to OUT7 are in use. For the best current capability, it is recommended to use the maximum available banana jacks. This is described in each DB chapter.

**Note:** *Usually in the device's datasheet it is recommended to use an RIS of 1.2 kΩ. This refers to a 5 V logic system. Since the MB and the *microcontroller* provides a 3.3 V logic system, the resistor value RIS was reduced accordingly.*

**2 1-channel daughterboard**

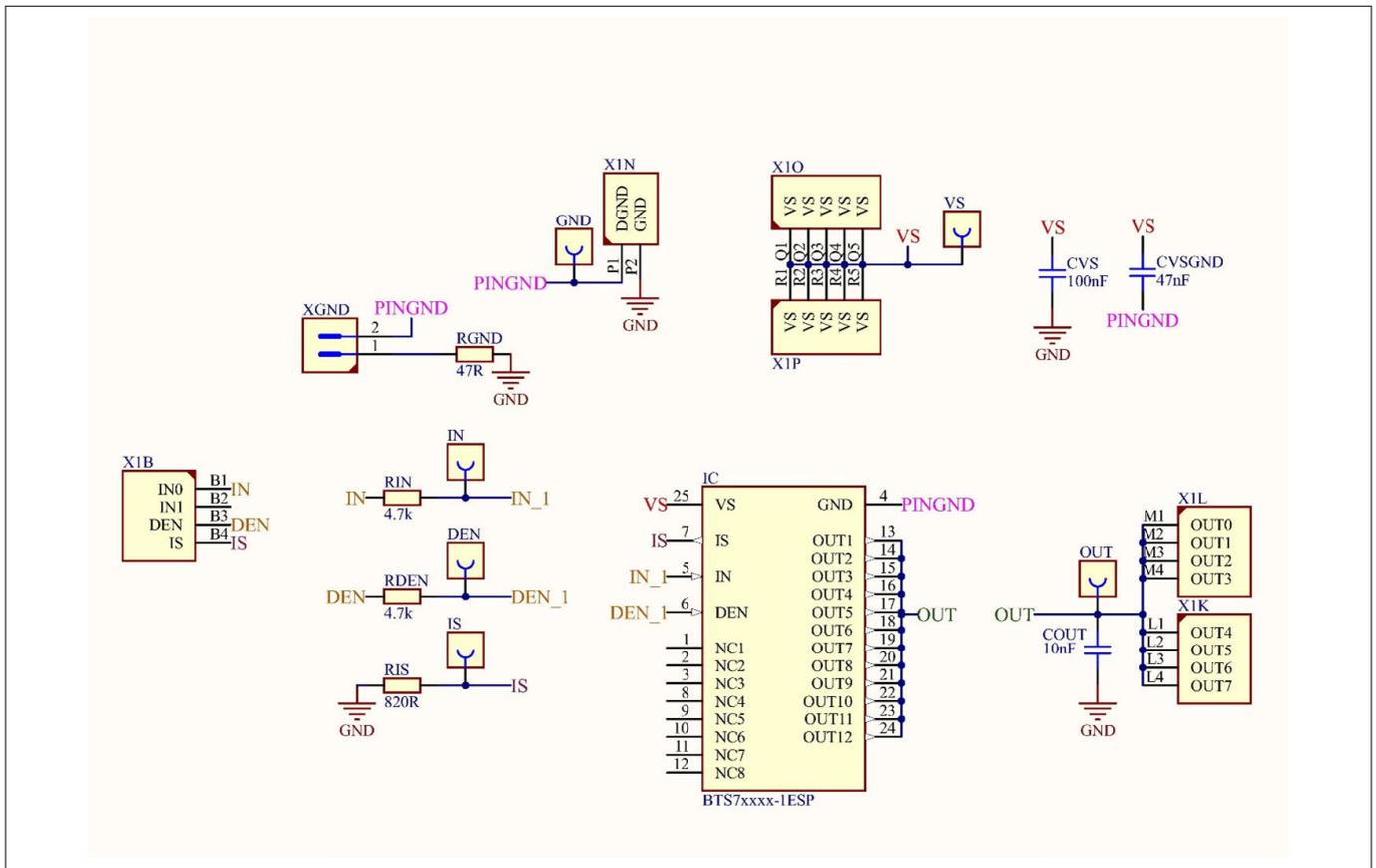
**2 1-channel daughterboard**



**Figure 1 1-channel daughterboard - 3D**

Because of the pin-to-pin compatibility the *DB* can handle 1-channel PG-TSDSO-14 (for example, BTS7008-1EPA) and PG-TSDSO-24 (for example, BTS70012-1ESP). This DB is not able to handle multichannel devices. The correct position of a 14-pin device is indicated by the blue lines on both sides of the footprint and the dot marking the GND-pin position. The correct position of the 24-pin device is marked by the dot in the upper left corner of the footprint for the GND-pin.

The OUT current is provided via banana jacks OUT0 – OUT3.



**Figure 2 1-channel daughterboard - schematic**

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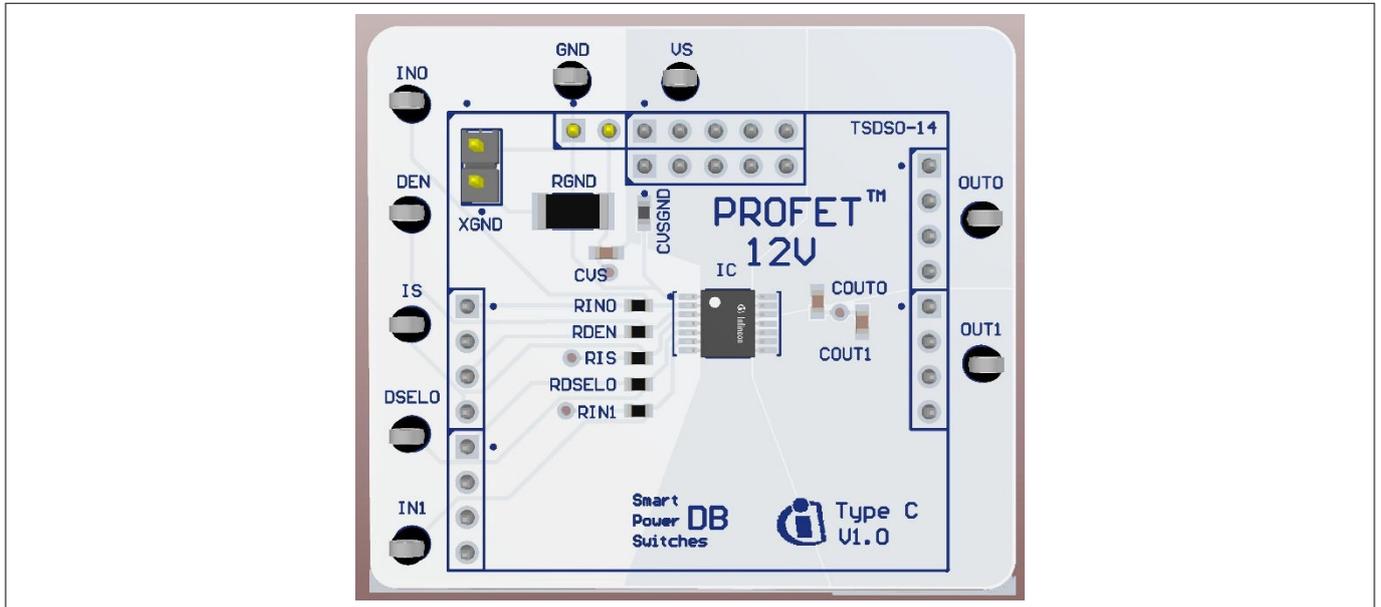
**2 1-channel daughterboard**

**Table 2**                    **Product list of compatible devices for this DB**

<b>Device name</b>	<b><math>I_{L(NOM)}</math> [A]</b>
BTS70012-1ESP	31.3
BTS70015-1ESP	27.6
BTS70020-1ESP	23.2
BTS7002-1EPP	21
BTS7004-1EPP	15
BTS7004-1EPR	15
BTS7006-1EPP	12.5
BTS7008-1EPA	10
BTS7008-1EPP	11
BTS7008-1EPR	11
BTS7010-1EPA	9
BTS7012-1EPA	8.5
BTS7040-1EPA	4.5

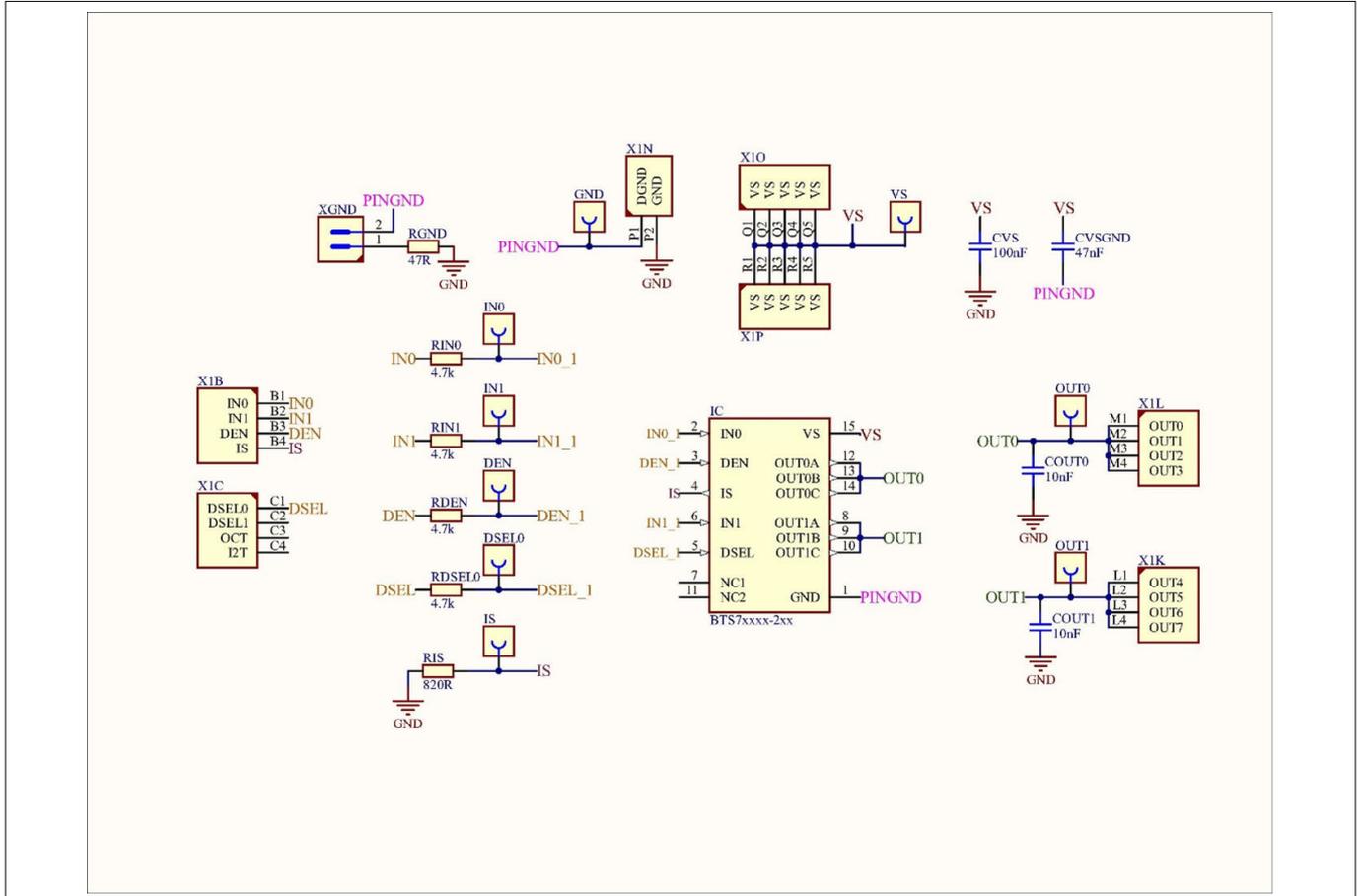
**3 2-channel daughterboard**

**3 2-channel daughterboard**



**Figure 3 2-channel daughterboard - 3D**

The OUT0 current is provided via banana jacks OUT0 and OUT1.  
 The OUT1 current is provided via banana jacks OUT2 and OUT3.



**Figure 4 2-channel daughterboard - schematic**

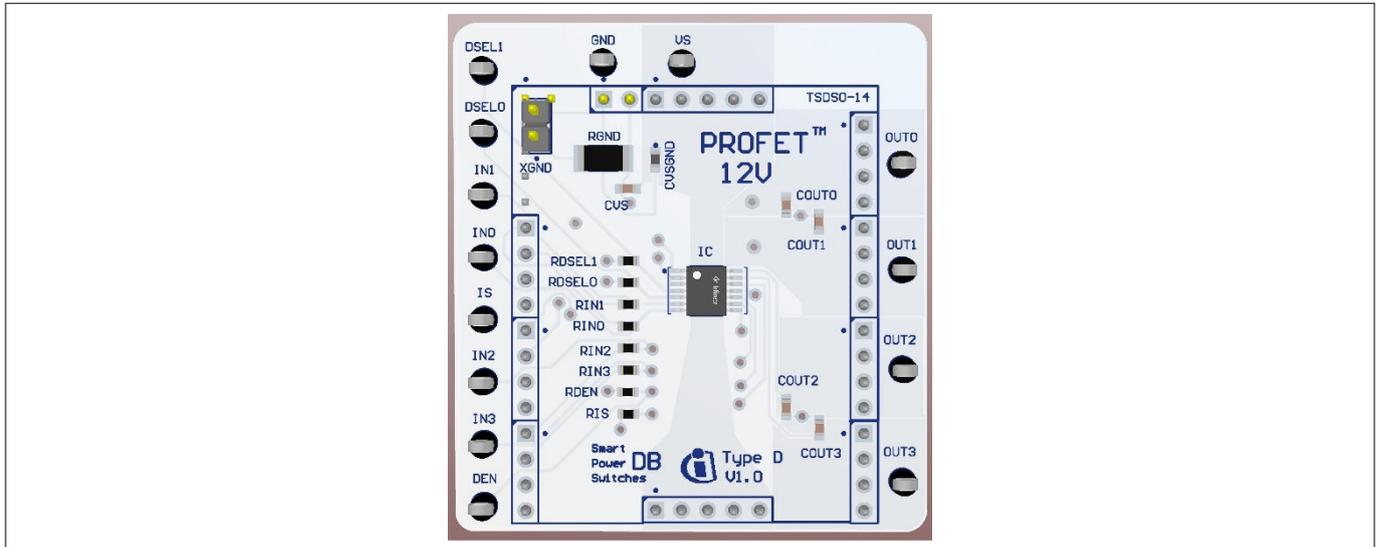
**3 2-channel daughterboard**

**Table 3**                    **Product list of compatible devices for this DB**

<b>Device name</b>	<b><math>I_{L(NOM)}</math> [A]</b>
BTS7008-2EPA	7.5
BTS7008-2EPG	7.5
BTS7010-2EPA	6.5
BTS7012-2EPA	6
BTS7012-2EPG	6
BTS7020-2EPA	5
BTS7030-2EPA	4.5
BTS7030-2EPG	4.5
BTS7040-2EPA	3.5
BTS7040-2EPG	3.5
BTS7050-2EPL	3
BTS7080-2EPA	3
BTS7090-2EPL	2
BTS7120-2EPA	2
BTS7120-2EPG	2
BTS7200-2EPA	1.2
BTS7200-2EPC	1.2

**4 4-channel daughterboard**

**4 4-channel daughterboard**



**Figure 5 4-channel daughterboard - 3D**

This DB is designed to handle 4-channel devices. The PROFET™ +2 12V 4-channel devices are not pin-to-pin compatible with other devices of this family.

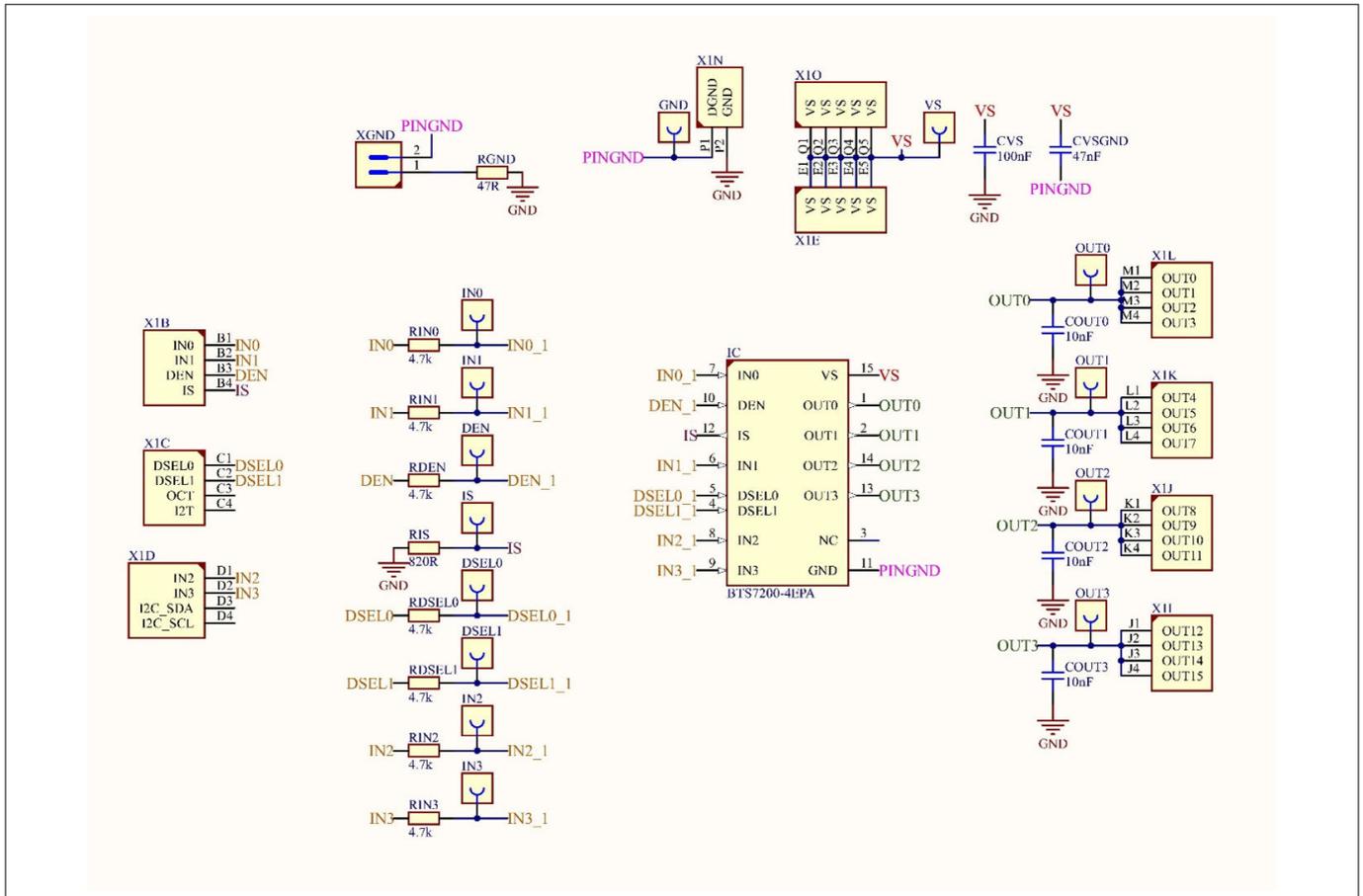
The OUT0 current is provided via banana jacks OUT0 and OUT1.

The OUT1 current is provided via banana jacks OUT2 and OUT3.

The OUT2 current is provided via banana jacks OUT3 and OUT4.

The OUT3 current is provided via banana jacks OUT5 and OUT6.

**4 4-channel daughterboard**



**Figure 6 4-channel daughterboard - schematic**

**Table 4 Product list of compatible devices for this DB**

Device name	$I_{L(NOM)}$ [A]
BTS7200-4EPA	1

## **5 Related resources**

- [Product family page](#)
- [PROFET™+2 12V Frequently asked questions](#)

## **6 Appendices**

### **6.1 Daughterboard ordering information**

**Table 5 Daughterboard ordering information**

<b>Sales part name</b>	<b>Sales product number</b>
D-BTS70012-1ESP-1	SP006142517
D-BTS70015-1ESP-1	SP006142519
D-BTS70020-1ESP-1	SP006142521
D-BTS7002-1EPP-1	SP006142469
D-BTS7004-1EPP-1	SP006142509
D-BTS7004-1EPR-1	SP006142511
D-BTS7006-1EPP-1	SP006142513
D-BTS7008-1EPA-1	SP006142523
D-BTS7008-1EPP-1	SP006142515
D-BTS7008-1EPR-1	SP006239342
D-BTS7008-2EPA-1	SP006142525
D-BTS7008-2EPG-1	SP006239414
D-BTS7010-1EPA-1	SP006142527
D-BTS7010-2EPA-1	SP006142529
D-BTS7012-1EPA-1	SP006142531
D-BTS7012-2EPA-1	SP006142533
D-BTS7012-2EPG-1	SP006239503
D-BTS7020-2EPA-1	SP006142535
D-BTS7030-2EPA-1	SP006142537
D-BTS7030-2EPG-1	SP006239507
D-BTS7040-1EPA-1	SP006142539
D-BTS7040-2EPA-1	SP006142541
D-BTS7040-2EPG-1	SP006239511
D-BTS7050-2EPL-1	SP006142543
D-BTS7080-2EPA-1	SP006142545
D-BTS7090-2EPL-1	SP006142547
D-BTS7120-2EPA-1	SP006142549
D-BTS7120-2EPG-1	SP006239515
D-BTS7200-2EPA-1	SP006142556
D-BTS7200-2EPC-1	SP006142558
D-BTS7200-4EPA-1	SP006142560

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**6 Appendices**

**6.2 Motherboard ordering information**

**Table 6 Motherboard ordering information**

<b>Board name</b>	<b>Sales part name</b>	<b>Sales product number</b>
SPS Motherboard Lite	M-SMART-POWER-LITE-5	SP006136123
SPS Motherboard	M-SMART-POWER-SWT-5	SP006041408

## Glossary

### **DB**

*daughterboard (DB)*

An expansion circuit card connected to a motherboard.

### **MB**

*motherboard (MB)*

The main printed circuit board (PCB) in general-purpose computers and other expandable systems. It holds and allows communication between many of the crucial electronic components of a system, such as the central processing unit (CPU) and memory, and provides connectors for other peripherals.

### **microcontroller**

*microcontroller*

A small computer on a single integrated circuit containing a processor core, memory, and programmable input/output peripherals.

### **PCB**

*printed circuit board (PCB)*

A board that mechanically supports and electrically connects electronic components using conductive tracks, pads, and other features etched from copper sheets laminated onto a non-conductive substrate.

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**Revision history**

**Revision history**

<b>Document version</b>	<b>Date of release</b>	<b>Description of changes</b>
Rev. 1.00	2025-12-17	Initial release

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