

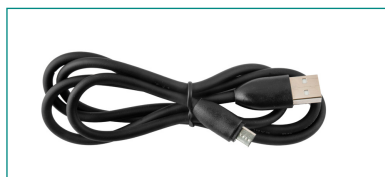
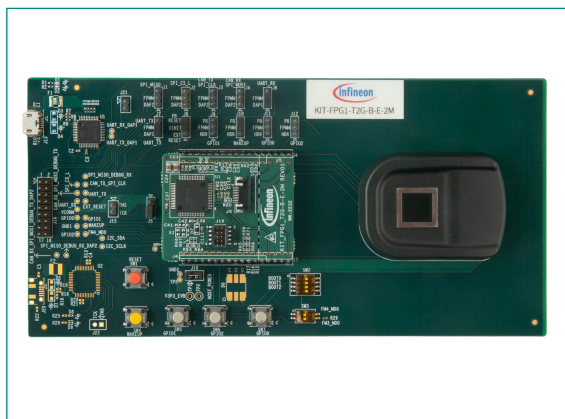
QUICK START GUIDE

Automotive fingerprint evaluation kit

KIT-FPG1-T2G-B-E-2M

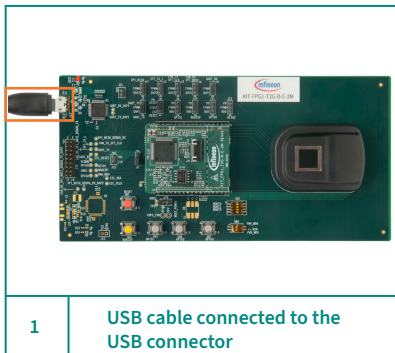
Kit contents

1. KIT-FPG1-T2G-B-E-2M evaluation board with
 - Baseboard
 - TRAVEO™ T2G matcher module
2. Micro-B type USB-cable
3. Quick start guide (this document)



www.infineon.com/evaluation-board/KIT-FPG1-T2G-B-E-2M



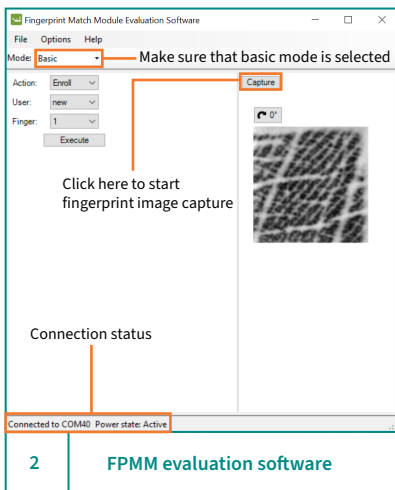


Ensure that you have the following:

1. PC with USB port
2. Fingerprint matcher module (FPMM) evaluation software installed (See the FPMM evaluation software user guide for more details)
3. TRAVEO™ T2G Matcher module mated with the KIT-FPG1-T2G-B-E-2M baseboard

Connect and power up the board

1. Ensure that jumper (J5) is at position 2–3 to select 5 V power to be supplied from the USB.
2. Connect the USB connector (J13) to your PC.



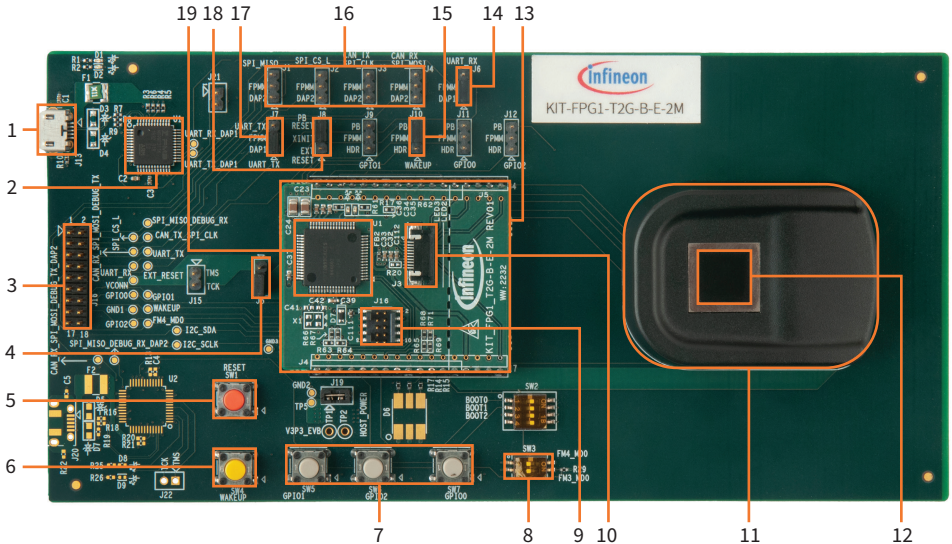
Connect the kit with the FPMM evaluation software

1. Open the FPMM evaluation software.
2. Ensure that **Basic** mode is selected.
3. Wait for the software to scan for the evaluation board. If successful, the connection status is updated on the status bar as Connected to COMxx Power state: Active (xx is the virtual COM port number).

Get fingerprint capture

1. Click **Capture**.
2. When prompted, place your finger on the fingerprint sensor and wait for capture.
3. The fingerprint image is displayed below the capture button.

KIT-FPG1-T2G-B-E-2M evaluation board details



- | | |
|----------------------------------|--|
| 1 USB connector (J13) | 11 Fingerprint guide |
| 2 FM3-based USB-UART bridge (U1) | 12 CYFP10020S00 (fingerprint sensor) |
| 3 Expansion connector (J16) | 13 TRAVEO™ T2G matcher module
KIT-FPG1_T2G-B-E-2M |
| 4 Power selection header (J5) | 14 UART RX selection header (J6) |
| 5 Reset button (SW1) | 15 Wakeup selection header (J10) |
| 6 Wakeup button (SW4) | 16 SPI selection headers (J1, J2, J3, J4) |
| 7 GPIO buttons (SW5, SW6, SW7) | 17 UART TX selection header (J7) |
| 8 FM3 mode selector (SW3) | 18 RESET selection header (J8) |
| 9 SWD header for FPMM MCU (J16) | 19 TRAVEO™ T2G MCU (CYT2B93CAE- U1) |
| 10 ZIF connector for FPG1 (J3) | |

The default jumper connections in KIT-FPG1-T2G-B-E-2M baseboard are listed in the following table:

Test to jumper# on board	Position
J5	2-3
J6	1-2
J7	1-2
J8	2-3
J10	2-3
J19	1-2

Next steps

Visit <https://www.infineon.com/evaluation-board/KIT-FPG1-T2G-B-E-2M> for information on code examples supported for this kit and kit documentation.