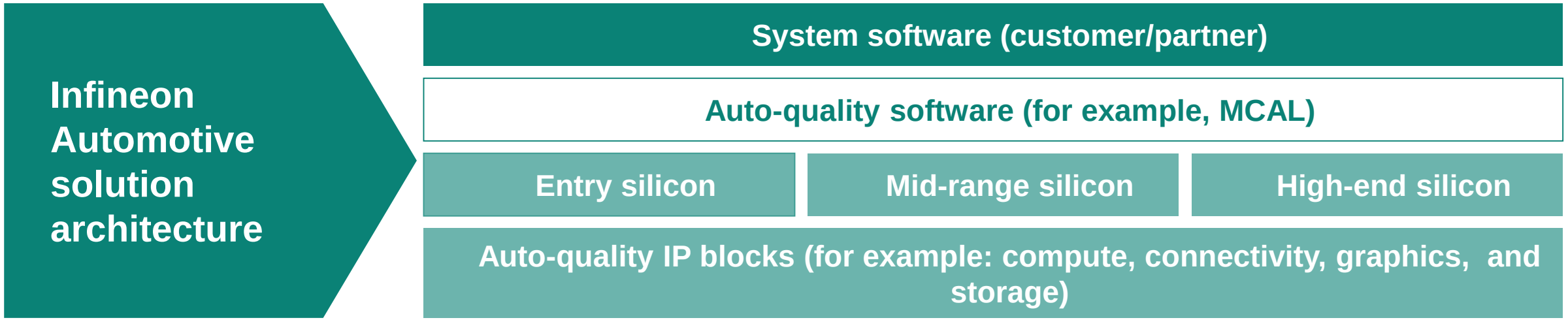
Product presentation
TRAVEO™T2G Body

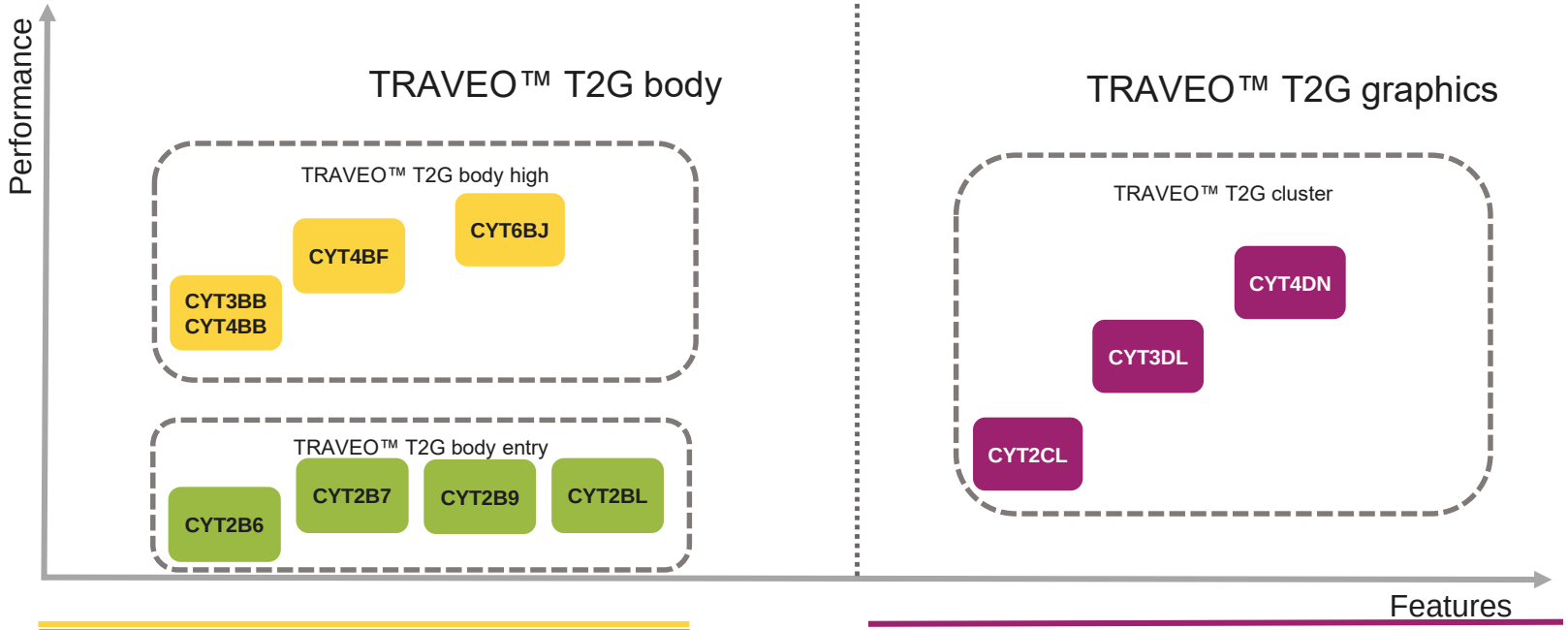
Dec., 2025

Infineon scalable platform solutions

- Deliver full range of silicon products per platform
- Enable OEM / Tier 1 software reuse (consistent across platform, generations)
- Deliver best-in-class, auto-quality solution components
- Reduce risk: Infineon is an established Automotive Semiconductor supplier



Family Portfolio / Product Variants



TRAVEO™ T2G body

- Up to 16 MB flash and 2048 KB RAM
- Up to 4x Cortex®-M7 up to 350 MHz
- 48-QFN to 320-BGA
- eMMC interface for data storage

TRAVEO™ T2G graphics

- Up to 6 MB flash and 896 KB RAM
- Up to Dual core Cortex®-M7 @320 MHz
- Graphic accelerator supports up to 1080p
- Video input and output
- JPEG decoder
- HYPERBUS™ support
- eMMC interface for data storage

TRAVEO™ T2G features

- Arm® Cortex®-based
- Low-power consumption
- CAN FD and up to 1 Gb Ethernet
- 10Base-T1S supported
- EVITA FULL HSM
- ISO 21434-compliant
- Ready for SW updates over-the-air
- ISO 26262 up to ASIL-B
- Compliant up to AEC-Q100 grade 1 (Ta 125°C)
- AUTOSAR-compliant MCAL drivers available

TRAVEO™ T2G Body overview

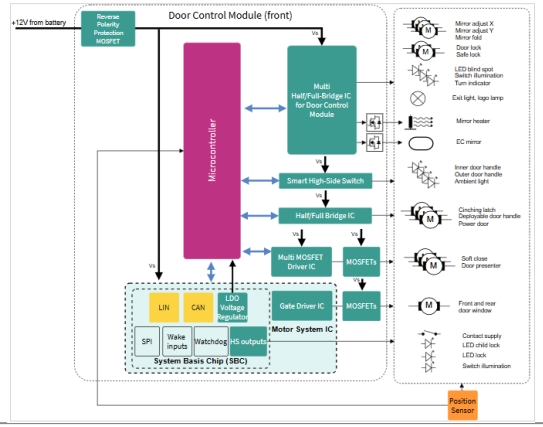
Product highlights

- Multiple core combinations
  Single core Cortex®-M4, Single/Dual/Quad core Cortex®-M7
- Low-power consumption
  35 µA in DeepSleep mode
- Wide voltage input
  2.7 V – 5.5 V for both Cortex®-M4 and Cortex®-M7 devices
- True FOTA
  Implemented mostly by Hardware, simple to realize
- Pin-to-pin compatibility
  Across the body entry (Cortex®-M4) and body high (Cortex®-M7) family
- Adapting to market needs 10Base-T1S, ISO26262 ASIL-B, ISO 21434 compliance

Key specifications

Part number	CPU clock	Code Flash	Work Flash	SRAM	Packages
CYT2B6	80 MHz	576 KB	64 KB	64 KB	LQFP-64/80/100
CYT2B7	160 MHz	1088 KB	96 KB	128 KB	QFN-48, LQFP-64/80/100/144/176
CYT2B9	160 MHz	2112 KB	128 KB	256 KB	LQFP-64/80/100/144/176
CYT2BL	160 MHz	4160 KB	128 KB	512 KB	LQFP-64/80/100/144/176
CYT3BB	250 MHz	4160 KB	256 KB	768 KB	TQFP-100/144/176, BGA 272
CYT4BB	250 MHz x2	4160 KB	256 KB	768 KB	TQFP-100/144/176, BGA 272
CYT4BF	350 MHz x2	8384 KB	256 KB	1024 KB	TQFP-176, BGA 272/320
CYT6BJ	320 MHz x4	16768 KB	512 KB	2048 KB	TQFP-176, BGA 272/320

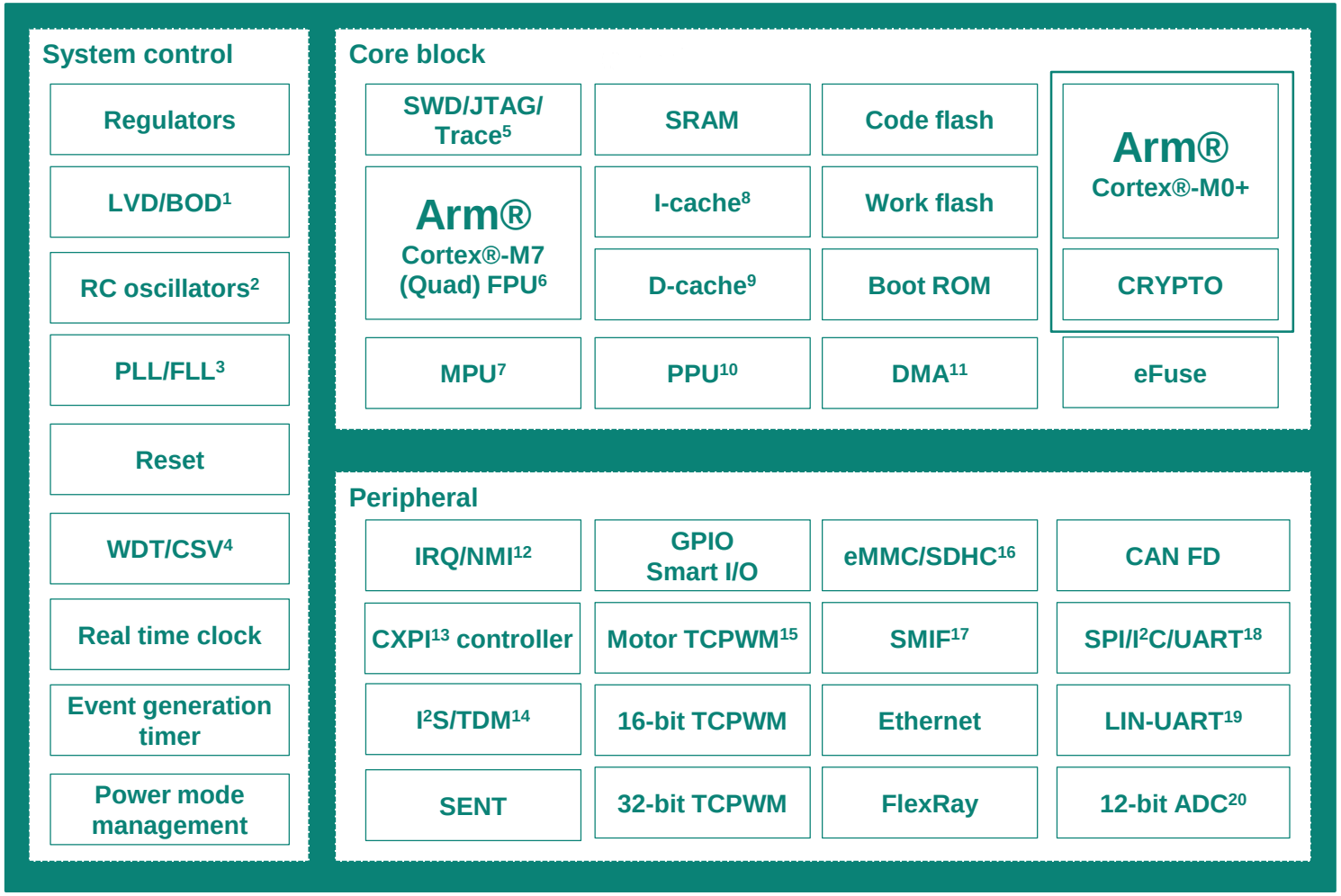
Door Control Module application block diagram



Typical applications

- Zone control unit
- Body control modules (BCM)
- Automotive power distribution
- Automotive auxiliary systems
- Automotive gateway
- Zonal DC-DC converter 48 V-12 V
- Door control module
- Smart car access
- Seat control module
- Smart power closure system

TRAVEO™ T2G body feature overview



1 Low-voltage detection/brownout detection
2 Resistor capacitor
3 Phase-locked loop/frequency-locked loop
4 Watchdog timer/clock supervisor
5 Serial wire debug/Joint test action group
6 Floating point unit
7 Memory protection unit
8 Instruction cache
9 Data cache
10 Peripheral protection unit
11 Direct memory access
12 Interrupt request/Non-maskable interrupt
13 Clock extension peripheral
14 Inter-IC sound/time division multiplexed
15 Timer/counter/pulse-width modulation
16 Embedded Multi-Media Card/Secure Digital High Capacity
17 Serial memory interface
18 Serial peripheral interface/Inter-integrated circuit/universal asynchronous receiver transmitter
19 Local Interconnect Network - Universal asynchronous receiver transmitter
20 Analog-to-digital converter

TRAVEO™ Body : Package Line Up
Easily scalable package options for Automotive Body and Zone applications

Part number	Flash memory size	Pin count							
			LQFP/TQFP					BGA	
		48-pin*	64-pin	80-pin	100-pin	144-pin	176-pin	272-ball	320-ball
CYT6BJ	16 MB	T2G Body High					2048 KB	2048 KB	2048 KB
CYT4BF	8 MB						1024 KB	1024 KB	1024 KB
CYT3BB/4BB	4 MB				768 KB	768 KB	768 KB	768 KB	
CYT2BL	4 MB		512 KB	512 KB	512 KB	512 KB	512 KB		
CYT2B9	2 MB		256 KB	256 KB	256 KB	256 KB	256 KB	T2G Body Entry	
CYT2B7	1 MB	128KB*	128 KB	128 KB	128 KB	128KB	128 KB		
CYT2B6	512 KB		64 KB	64 KB	64 KB			*Legend: RAM*	

Platform Software

Software Offering

- MCAL[1]
- STL[2]
- FEE[3]

[1] MCAL: microcontroller abstraction layer
[2] STL: Self-test library
[3] FEE: flash EEPROM emulation

**** Under development***

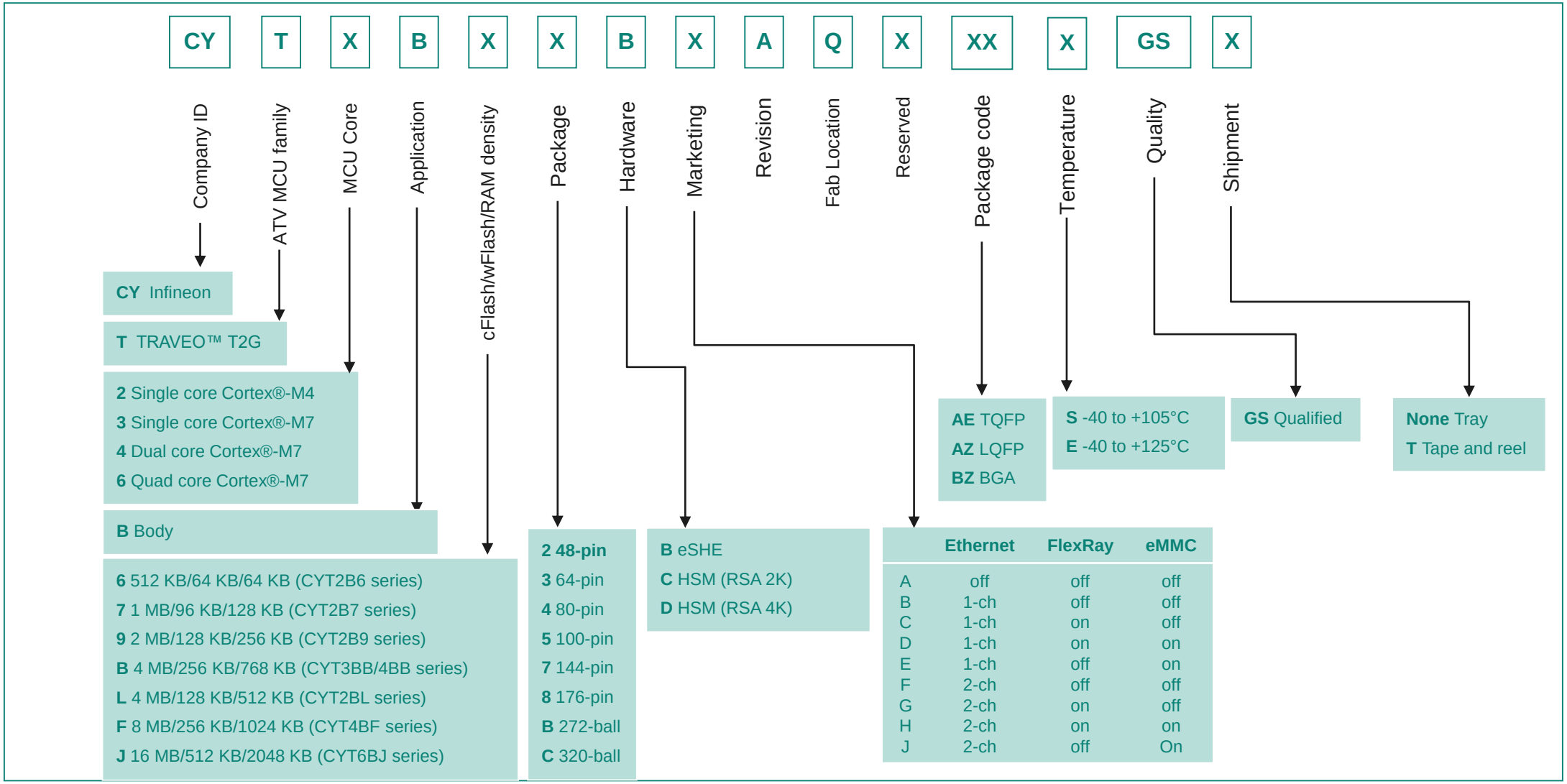
TRAVEO™ T2G Body Entry portfolio – differentiation of product variants

Product variant	CPU	CPU clock	Code Flash	Work Flash	SRAM	CXPI	Packages	key features
CYT2B63	Arm® Cortex®-M4F	80 MHz	576 KB	64 KB	64 KB	Not supported	LQFP 64	Hardware Crypto engine for Evita full security, CAN FD 3 ch, 12 bit A/D converter 3 units 22 ch
CYT2B64	Arm® Cortex®-M4F	80 MHz	576 KB	64 KB	64 KB	Not supported	LQFP 80	Hardware Crypto engine for Evita full security, CAN FD 4 ch, 12 bit A/D converter 3 units 28 ch
CYT2B65	Arm® Cortex®-M4F	80 MHz	576 KB	64 KB	64 KB	Not supported	LQFP 100	Hardware Crypto engine for Evita full security, CAN FD 4 ch, 12 bit A/D converter 3 units 32 ch
CYT2B72	Arm® Cortex®-M4F	160 MHz	1088 KB	96 KB	128 KB	Not supported	QFN 48	Hardware Crypto engine for Evita full security, CAN FD 5 ch, 12 bit A/D converter 3 units 20 ch
CYT2B73	Arm® Cortex®-M4F	160 MHz	1088 KB	96 KB	128 KB	Not supported	LQFP 64	Hardware Crypto engine for Evita full security, CAN FD 5 ch, 12 bit A/D converter 3 units 27 ch
CYT2B74	Arm® Cortex®-M4F	160 MHz	1088 KB	96 KB	128 KB	Not supported	LQFP 80	Hardware Crypto engine for Evita full security, CAN FD 7 ch, 12 bit A/D converter 3 units 34 ch
CYT2B75	Arm® Cortex®-M4F	160 MHz	1088 KB	96 KB	128 KB	Not supported	LQFP 100	Hardware Crypto engine for Evita full security, CAN FD 6 ch, 12 bit A/D converter 3 units 39 ch
CYT2B77	Arm® Cortex®-M4F	160 MHz	1088 KB	96 KB	128 KB	Not supported	LQFP 144	Hardware Crypto engine for Evita full security, CAN FD 6 ch, 12 bit A/D converter 3 units 54 ch
CYT2B78	Arm® Cortex®-M4F	160 MHz	1088 KB	96 KB	128 KB	Not supported	LQFP 176	Hardware Crypto engine for Evita full security, CAN FD 6 ch, 12 bit A/D converter 3 units 64 ch
CYT2B93	Arm® Cortex®-M4F	160 MHz	2112 KB	128 KB	256 KB	2 ch	LQFP 64	Hardware Crypto engine for Evita full security, CAN FD 5 ch, 12 bit A/D converter 3 units 27 ch
CYT2B94	Arm® Cortex®-M4F	160 MHz	2112 KB	128 KB	256 KB	3 ch	LQFP 80	Hardware Crypto engine for Evita full security, CAN FD 7 ch, 12 bit A/D converter 3 units 34 ch
CYT2B95	Arm® Cortex®-M4F	160 MHz	2112 KB	128 KB	256 KB	4 ch	LQFP 100	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 39 ch
CYT2B97	Arm® Cortex®-M4F	160 MHz	2112 KB	128 KB	256 KB	4 ch	LQFP 144	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 54 ch
CYT2B98	Arm® Cortex®-M4F	160 MHz	2112 KB	128 KB	256 KB	4 ch	LQFP 176	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 64 ch
CYT2BL3	Arm® Cortex®-M4F	160 MHz	4160 KB	128 KB	512 KB	2 ch	LQFP 64	Hardware Crypto engine for Evita full security, CAN FD 5 ch, 12 bit A/D converter 3 units 27 ch
CYT2BL4	Arm® Cortex®-M4F	160 MHz	4160 KB	128 KB	512 KB	3 ch	LQFP 80	Hardware Crypto engine for Evita full security, CAN FD 7 ch, 12 bit A/D converter 3 units 34 ch
CYT2BL5	Arm® Cortex®-M4F	160 MHz	4160 KB	128 KB	512 KB	4 ch	LQFP 100	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 39 ch
CYT2BL7	Arm® Cortex®-M4F	160 MHz	4160 KB	128 KB	512 KB	4 ch	LQFP 144	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 54 ch
CYT2BL8	Arm® Cortex®-M4F	160 MHz	4160 KB	128 KB	512 KB	4 ch	LQFP 176	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 64 ch

TRAVEO™ T2G Body High portfolio – differentiation of product variants

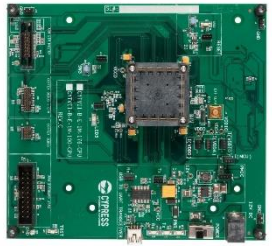
Product variant	CPU	CPU clock	Code Flash	Work Flash	SRAM	Ethernet	Packages	key features
CYT3BB5	Arm® Cortex®-M7F	250 MHz	4160 KB	256 KB	768 KB	10BASE-T1S, 10/100M	TQFP-100	Hardware Crypto engine for Evita full security, CAN FD 7 ch, 12 bit A/D converter 3 units 39 ch
CYT3BB7	Arm® Cortex®-M7F	250 MHz	4160 KB	256 KB	768 KB	10BASE-T1S, 10/100M	TQFP-144	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 54 ch
CYT3BB8	Arm® Cortex®-M7F	250 MHz	4160 KB	256 KB	768 KB	10BASE-T1S, 10/100M	TQFP-176	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 64 ch
CYT3BBB	Arm® Cortex®-M7F	250 MHz	4160 KB	256 KB	768 KB	10BASE-T1S, 10/100M	BGA-272	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 72 ch
CYT4BB5	Arm® Cortex®-M7F	250 MHz	4160 KB	256 KB	768 KB	10BASE-T1S, 10/100M	TQFP-100	Hardware Crypto engine for Evita full security, CAN FD 7 ch, 12 bit A/D converter 3 units 39 ch
CYT4BB7	Arm® Cortex®-M7F	250 MHz	4160 KB	256 KB	768 KB	10BASE-T1S, 10/100M	TQFP-144	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 54 ch
CYT4BB8	Arm® Cortex®-M7F	250 MHz	4160 KB	256 KB	768 KB	10BASE-T1S, 10/100M	TQFP-176	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 64 ch
CYT4BBB	Arm® Cortex®-M7F	250 MHz	4160 KB	256 KB	768 KB	10BASE-T1S, 10/100M	BGA-272	Hardware Crypto engine for Evita full security, CAN FD 8 ch, 12 bit A/D converter 3 units 72 ch
CYT4BF8	Arm® Cortex®-M7F	350 MHz	8384 KB	256 KB	1024 KB	10BASE-T1S, 10/100M	TQFP-176	Hardware Crypto engine for Evita full security, CAN FD 10 ch, 12 bit A/D converter 3 units 81 ch
CYT4BFB	Arm® Cortex®-M7F	350 MHz	8384 KB	256 KB	1024 KB	10BASE-T1S, 10/100M/1G	BGA-272	Hardware Crypto engine for Evita full security, CAN FD 10 ch, 12 bit A/D converter 3 units 96 ch
CYT4BFC	Arm® Cortex®-M7F	350 MHz	8384 KB	256 KB	1024 KB	10BASE-T1S, 10/100M/1G	BGA-320	Hardware Crypto engine for Evita full security, CAN FD 10 ch, 12 bit A/D converter 3 units 96 ch
CYT6BJ8	Arm® Cortex®-M7F	320 MHz	16768 KB	512 KB	2048 KB	10BASE-T1S, 10/100M/1G	TQFP-176	Hardware Crypto engine for Evita full security, CAN FD 10 ch, 12 bit A/D converter 3 units 81 ch
CYT6BJB	Arm® Cortex®-M7F	320 MHz	16768 KB	512 KB	2048 KB	10BASE-T1S, 10/100M/1G	BGA-272	Hardware Crypto engine for Evita full security, CAN FD 10 ch, 12 bit A/D converter 3 units 96 ch
CYT6BJC	Arm® Cortex®-M7F	320 MHz	16768 KB	512 KB	2048 KB	10BASE-T1S, 10/100M/1G	BGA-320	Hardware Crypto engine for Evita full security, CAN FD 10 ch, 12 bit A/D converter 3 units 96 ch

TRAVEO™ T2G body Product naming nomenclature



Kits and Boards

TRAVEO™ T2G Body Entry



- Develop and test the key functionalities provided by TRAVEO™ T2G such as User **Switch**, User **LED**, and **UART** communication

- CYTVII-B-E-1M-100-CPU
- CYTVII-B-E-1M-176-CPU
- CYTVII-B-E-2M-100-CPU
- CYTVII-B-E-2M-176-CPU
- CYTVII-B-E-4M-176-CPU

TRAVEO™ T2G Body High

- Develop and test functionalities such as **Audio Interface**, Automotive **Ethernet**, **SD Card**, **SMIF**, **Dual QSPI** User **Switch**, User **LED**, and **UART** communication

- CYTVII-B-H-4M-176-CPU
- CYTVII-B-H-4M-272-CPU
- CYTVII-B-H-8M-176-CPU
- CYTVII-B-H-8M-272-CPU
- CYTVII-B-H-8M-320-CPU
- CYTVII-B-H-16M-176-CPU
- CYTVII-B-H-16M-320-CPU

TRAVEO™ T2G Body Low cost kits

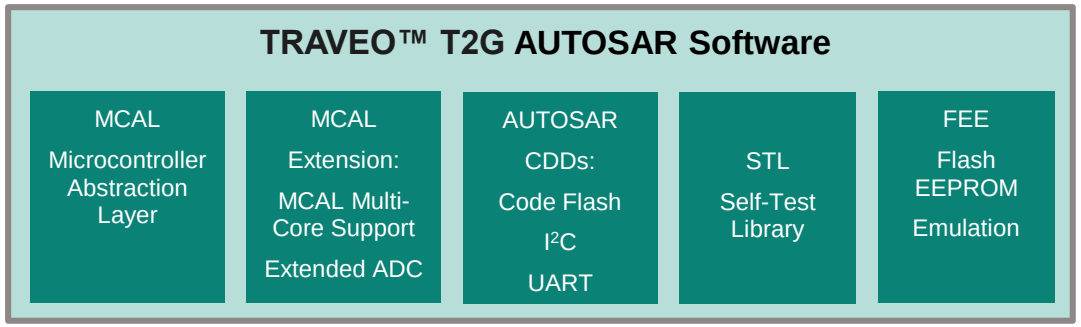
- **Low-cost**
- **Easy to use** evaluation board based on the TRAVEO™ T2G body **Entry/High** families **Ethernet**, **Arduino, mikroBUS**
- **Modustoolbox™ compatibility**

- CYTVII-B-E-1M-SK
- KIT_T2G-B-E_LITE
- KIT_T2G-B-H_LITE

Software offer

- AUTOSAR version 4.2.2 compliant (T2G Body) and ASR24-11 (T2G Body Extension)
- Development process ISO26262 audited/certified by TÜV Süd
- Developed as safety element out of context (SEooC) for ASIL B

- Functional safety documentation available
- Released for GHS compiler version 2024.1.4 + IAR EWARM FS 8.22.3.15992
- EB tresos Studio v29.7.5 for TRAVEO™ included in delivery (node locked license)



✓Automotive Qualified Software

Ecosystem

Color Code	In development	On Request	Open Source

Toolsuite (Compiler with Debugger)

- **IAR EWARM FS**
- **Greenhills Multi**
- HighTec*
- TASKING* VX-toolset for Arm
- KEIL/ARM µVision*
- GCC (OSS)*

Toolsuite (Debugger)

- Lauterbach Trace32
- ModusToolbox™ (GCC)
- Segger
- LLVM (OSS)
- GDB (OSS)
- TASKING WinIDEA (ISYSTEM)

Communication

- ETH (Elektrobit) (Vector)
- Flexray (Elektrobit) (Vector)

Security

- Vector veHSM
- Intron/G-Pulse gHSM_TVII
- Fescaro
- Siemens
- ETAS CycurHSM
- GRC Crypto Stack
- Elektrobit ZenturHSM
- GHS/ISS FlexHSM

OS (Non ASR)

- PXROS (Hightec)
- SafeRTOS (Wittenstein)
- µVelocity (GHS)
- TockOS (OSS RUST)
- NuttX
- FreeRTOS
- Zephyr

AUTOSAR BSW

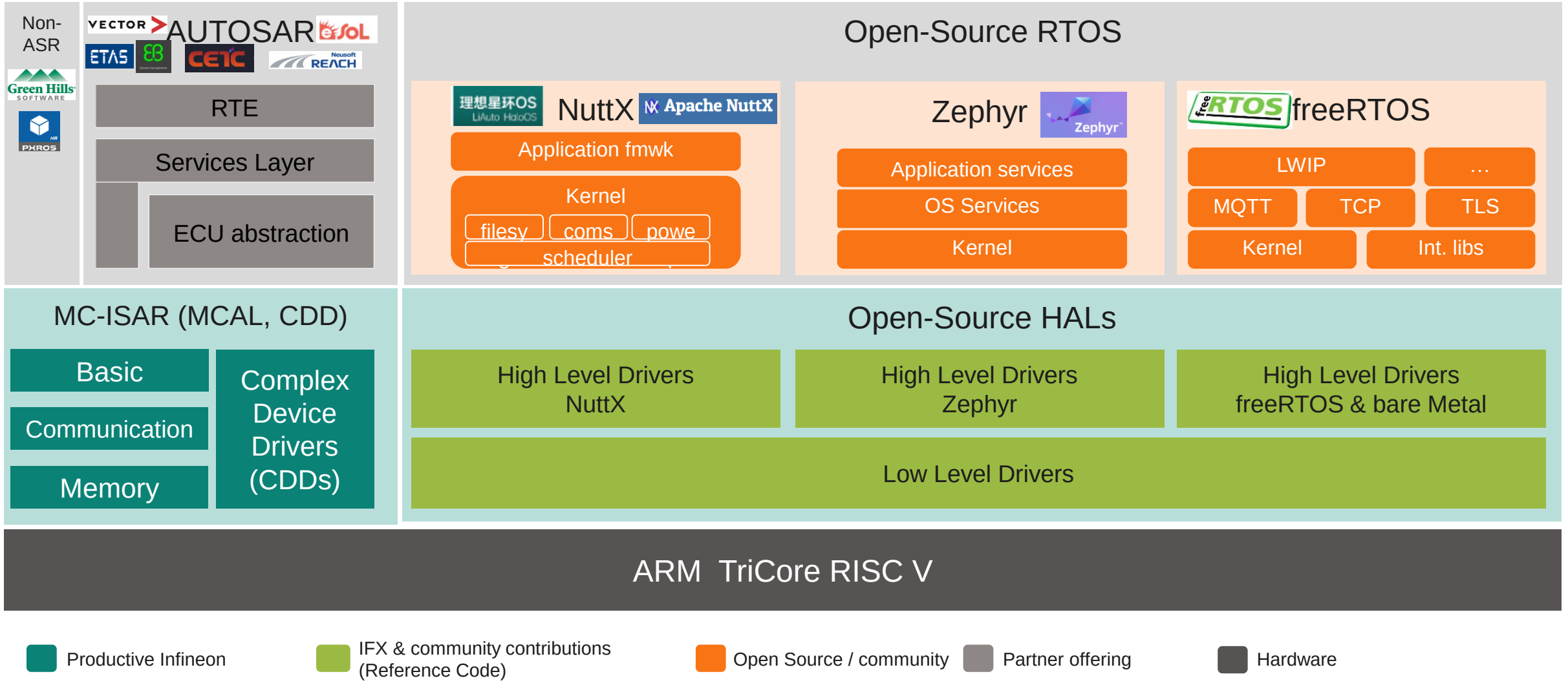
- Vector MICROSAR Classic
- Elektrobit tresos AutoCore
- ETAS RTA-CAR
- Siemens

Others

- dSpace TargetLink
- Elektrobit Lean HV
- ETAS ETK Calibration
- Vector VX1000 Calibration

***TRAVEO™ MCAL is not released for this compiler**

Infineon is supporting the major open-source initiatives



ModusToolbox™ – ease of use

TRAVEO™ T2G Lite kits
KIT_T2G-B-E_LITE
Fully supported by ModusToolbox™
KIT_T2G-B-H_LITE
Fully supported by ModusToolbox™

ModusToolbox™

ModusToolbox™ Software is a modern, extensible development ecosystem supporting a wide range of Infineon microcontroller devices, including **PSOC™ Arm® Cortex® microcontrollers**, **TRAVEO™ T2G Arm® Cortex® microcontroller**. Provided as a collection of development tools, libraries, and embedded runtime assets, ModusToolbox™ Software is architected to provide a flexible and comprehensive development experience

- User manual
- Getting started
- ModusToolbox™ product presentation
- GitHub
- Community support

Get Started

Access to additional technical documentation

By registering in the myInfineon collaboration platform (MyICP), you can get access to add-on technical documentation, trainings, tools, and much more for all TRAVEO™ T2G devices.

How to get access

If not already available, please create a myInfineon account on www.infineon.com. Contact traveo@infineon.com and request access to TRAVEO™ T2G myICP.

Link to TRAVEO™ T2G MyICP


Infineon