Product presentation
TRAVEO™ T2G CYT6BJ series

Q1 FY2026

Table of contents

Table of contents

How [TRAVEO™ T2G Body] brings value to the target applications/use cases?

Zone Control unit

Zone control unit

- 2.7 to 5.5 V power supply
- Ethernet
- Low power consumption
- Security
- FOTA

- Gigabit Ethernet, 10BASE-T1S
- Active 800 mA (Max), DeepSleep 50 uA (Typ)
- ISO 21434 compliance
- Dual bank Flash memory

Gateway

Telematics gateway

- 2.7 to 5.5 V power supply
- Quad Arm® Cortex® -M7 performance
- Ethernet, CAN FD, LIN
- Security
- FOTA

- 2,700 DMIPS
- Gigabit Ethernet, 10BASE-T1S, CAN FD
- ISO 21434 compliance
- Dual bank Flash memory

Infotainmnet

Infotainment

- 3.3 V power supply
- CAN FD communication
- Low power consumption
- Security
- FOTA

- CAN FD
- Active 800 mA (Max), DeepSleep 50 uA (Typ)
- ISO 21434 compliance
- Dual bank Flash memory

What are the market and technology trends in the target markets to help you stay ahead?

Ethernet

- Up to one gigabit Ethernet communication
- 10 BASE-T1S support
- Ethernet AVB

FOTA

- High speed programming and erasing
- Flash in dual bank mode
- Flash memory retention
- Security for the protection

CyberSecurity

- Secure boot
- eVita full HSM
- ISO 21434 compliance
- Diagnostics via OBD

What are the market and technology trends in the target markets to help you stay ahead?

Ethernet

- Gigabit Etthernet
- 10BASE-T1S
- Ethernet AVB

FOTA

- High speed programming and erasing
- Flash in dual bank mode
- Flash memory retention
- Security for the protection

CyberSecuirty

- Secure boot
- eVita full HSM
- ISO 21434 compliance
- Diagnostics via OBD

Table of contents

How [TRAVEO™ T2G Body] addresses the challenges in [Zone control]?

Key requirements in application

1. Gateway Functions of Zone Control
2. Power Distribution and Actuation functions of Zone Control
3. High-Efficiency Power Management
4. Cybersecurity & Functional Safety
5. Cover multiple applications, including Body Zone Control, I/O Aggregators and End nodes such as sensors and actuators

Typical challenges

1. Network management for multiple network interfaces
   - Multiple types of network interfaces with security features integrated into the T2G Body
2. Control of power distribution, power load, sensors, and actuators
   - Control and facilitation of ON/OFF switch outputs, motor drives, smart/distributed sensors, and actuators via serial interfaces and flexible I/Os
3. High Power Dissipation in Zone Controller
   - Lower power consumption and wake-up handling
4. Data Protection against Attacks and Enhancing safety and Reliability
   - EVITA FULL HSM and ASIL-B support for Body Zone Controller
5. Various types of software used in Zonal E/E architecture
   - Wide package and performance scalability within a single one microcontroller family

Infineon solution features and benefits

1. Robust network interface for gateway functions
   - Up to 2x 1G Ethernet, up to 10 CAN, up to 20 LIN with Cybersecurity
2. Key Functions in T2G for Power Distribution and Actuation
   - A High number of serial interfaces and flexible I/Os including SPI, I^2C, PWM, UART and SMART I/O
   - Multiple channels with high precision of ADCs
3. High-Efficiency Power Management
   - Abundant low power modes, low power consumption, and fast wake-up times.
4. Appropriate Cybersecurity & Functional Safety for Body Zone Control
   - Secure environment for cryptographic operations and key management
   - Supports applications up to ASIL-B level.
5. T2G Body lineup can accommodate a wide range of applications
   - CYT6BJ expands support for 16 MB and 2 MB memory in the T2G Body family

What is Infineon's [TRAVEO™ T2G Body] positioning? [Arm® Cortex® -M7, Flash up to 16 MB, Gigabit/10Base-T1S Ethernet]

TRAVEO™ T2G Body High

NEW

	CYT6BJ	CYT4BF	CYT3BB/CYT4BB
TQFP 100			CYT3BB5CEBQ1AESGS CYT4BB5CEBQ1AESGS
			CYT3BB5CEBQ1AEEGS CYT4BB5CEBQ1AEEGS
TQFP 144			CYT3BB7CEBQ1AESGS CYT4BB7CEBQ1AESGS
			CYT3BB7CEBQ1AEEGS CYT4BB7CEBQ1AEEGS
TQFP 176	CYT6BJ8DDBQ0AESGS*	CYT4BF8CEDQ0AESGS CYT4BF8CDDQ0AESGS	CYT3BB8CEBQ1AESGS CYT4BB8CEBQ1AESGS
	CYT6BJ8DDBQ0AEEGS*	CYT4BF8CEDQ0AEEGS CYT4BF8CDDQ0AEEGS	CYT3BB8CEBQ1AEEGS CYT4BB8CEBQ1AEEGS
BGA 272	CYT6BJBDHBQ0BZSGS	CYT4BFBCJDQ0BZSGS CYT4BFBCHDQ0BZSGS	CYT3BBBCEBQ1BZSGS CYT4BBBCEBQ1BZSGS
	CYT6BJBDHBQ0BZEGS	CYT4BFBCJDQ0BZEGS CYT4BFBCHDQ0BZEGS	CYT3BBBCEBQ1BZEGS CYT4BBBCEBQ1BZEGS
BGA 320	CYT6BJCDHBQ0BZSGS	CYT4BFCCJDQ0BZSGS CYT4BFCCJDQ0BZEGS	
	CYT6BJCDHBQ0BZEGS	CYT4BFCCHDQ0BZSGS CYT4BFCCHDQ0BZEGS	

CYT6BJ

- Quad Arm® Cortex® -M7, 320 MHz
- Code Flash 16 MB
- SRAM 2 MB
- Packages: BGA 320, BGA 272, TQFP 176*
- Gigabit Ethernet and 10 BASE-T1S: up to 2 ch
- CAN FD: up to 10 ch

CYT4BF

- Dual Arm® Cortex® -M7, 350 MHz
- Code Flash 8 MB
- SRAM 1 MB
- Packages: BGA 320, BGA 272, TQFP 176
- Gigabit Ethernet and 10 BASE-T1S: up to 2 ch
- CAN FD: up to 10 ch

CYT3BB/CYT4BB

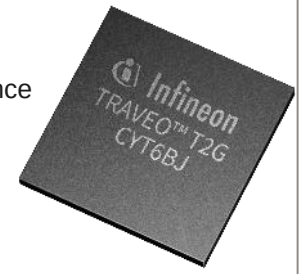
- Single/Dual Arm® Cortex® -M7, 250 MHz
- Code Flash 4 MB
- SRAM 768 KB
- Packages: BGA 272, TQFP 176, TQFP-144, TQFP-100
- 100 Mbps Ethernet and 10 BASE-T1S: 1 ch
- CAN FD: up to 8 ch

*Contact Sales

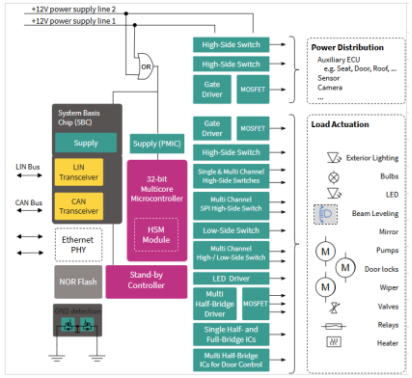
[TRAVEO™ T2G Body] CYT6BJ overview

Product highlights and benefits

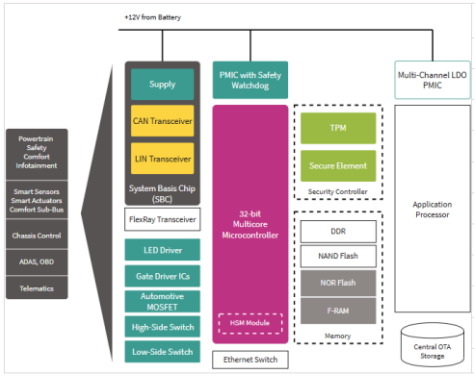
- Quad Arm® Cortex® -M7, 320 MHz
- Code Flash: 16 MB, Work Flash: 512 KB, SRAM 2 MB
- ISO26262 ASIL -B Functional safety
- Embedded automotive security, EVITA full HSM, ISO 26262 compliance
- True FOTA support
- Gigabit Ethernet and 10BASE-T1S: up to 2 ch, CAN FD: 10 ch
- Most scalable portfolio in the market, package: BGA 320, BGA 272, TQFP 176*
- Low power consumption, DeepSleep 50 uA (typ)
- AUTOSAR 4.2 supported software
- Automotive quality and Business Continuity



Typical connection diagram/application diagram



Zone control



Gateway

Key specifications

Part number	Packages	CPU	Code Flash/SRAM	Ethernet
CYT6BJCDHBQ0BZxGS	BGA 320	Quad Arm® Cortex®-M7	16 MB / 2 MB	2 ch
CYT6BJBDHBQ0BZxGS	BGA 272	Quad Arm® Cortex®-M7	16 MB / 2 MB	2 ch
CYT6BJ8DDBQ0AExGS*	TQFP 176*	Quad Arm® Cortex®-M7	16 MB / 2 MB	1 ch

Typical applications

- Zone control
- Body domain control
- Gateway
- Cockpit domain

Product information

- TRAVEO™ T2G CYT6BJ series web page
- TRAVEO™ T2G CYT6BJ series Datasheet
- CYT6BJ series Evaluation board

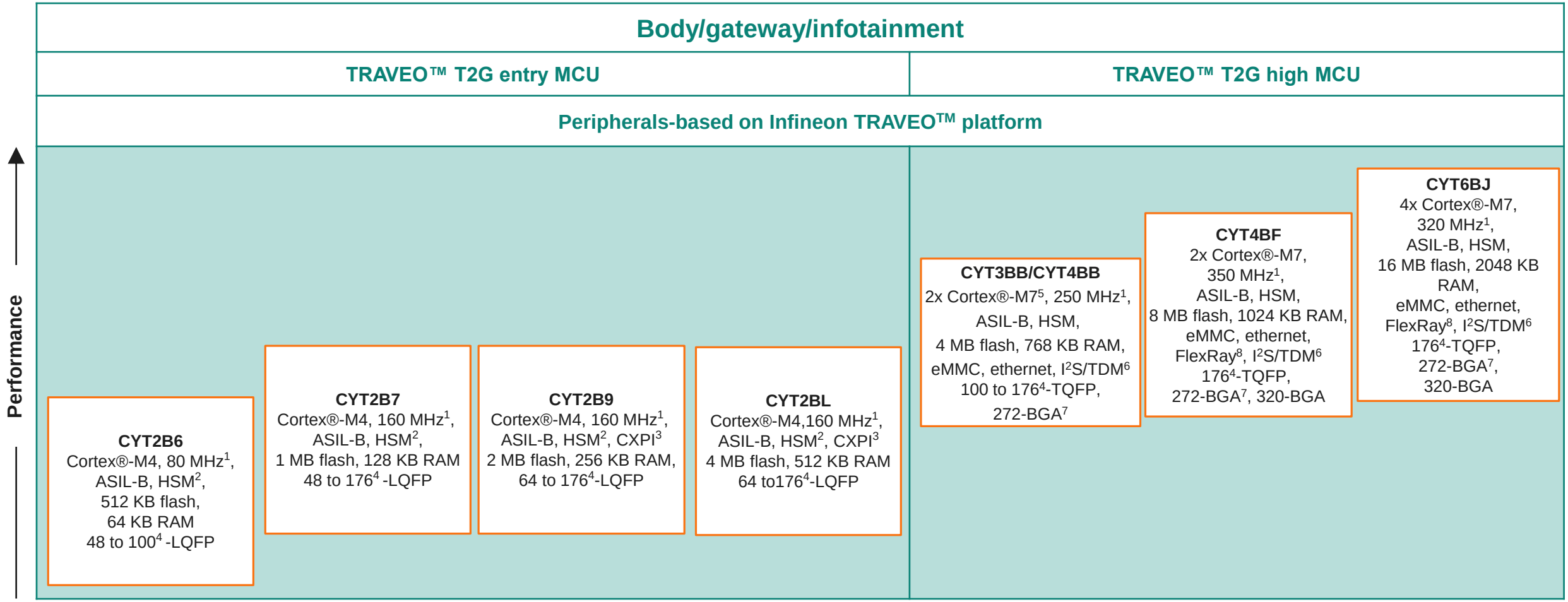
*Contact Sales

Key features TRAVEO™ T2G Body

[1] eSHE: enhanced secure hardware extension
[2] HSM: Hardware security module
[3] FOTA: Firmware update over-the-air
[4] RWW: Read while write
[5] Embedded multimedia card

Low-power	– Energy-efficient processing power – Deep Sleep: 50 uA (typ)
Performance	– Quad Arm® Cortex® -M7F – Double precision FPU – 2700 DMIPS
Scalability	– Complete portfolio with pin to pin compatibility – Memory density – Package lineup and performance
Safety	– ISO 26262 ASIL-B – Safety Manual and FMEDA
Audio	– I²S/TDM – High resolution 96 kHz
Security	– Hardware security module: eSHE[1], HSM[2] – Evita full – ISO21434 compliance
Connectivity	– LIN. CXPI – CAN FD – 1 Gb ethernet, 10BASE-T1S, Ethernet AVB
Updatability	– FOTA[3] with RWW[4] flash – eMMC[5] – QSPI/HS-SPI

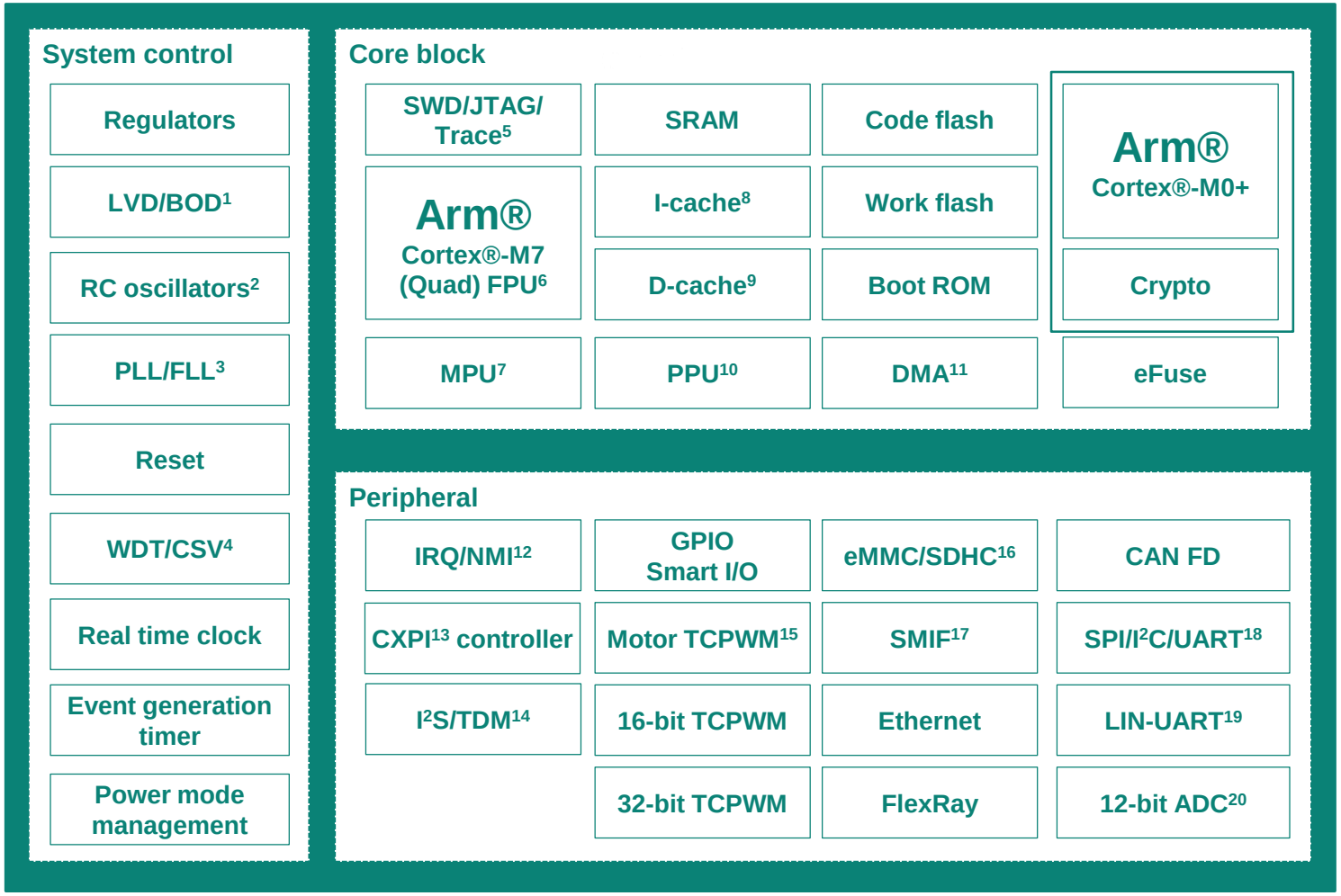
TRAVEO™ T2G body MCU portfolio



[1] Maximum operating frequency
[2] Hardware security module, SHE-only mode available
[3] Clock eXtension peripheral interface
[4] Package pin count, on request for 48 pin
[5] Single or dual core option
[6] Time division multiplexing
[7] Fine pitch ball grid array
[8] Optional feature

Status: Sampling | Production

TRAVEO™ T2G body feature overview



[1] Low-voltage detection/brownout detection
[2] Resistor capacitor
[3] Phase-locked loop/frequency-locked loop
[4] Watchdog timer/clock supervisor
[5] Serial wire debug/Joint test action group
[6] Floating point unit
[7] Memory protection unit
[8] Instruction cache
[9] Data cache
[10] Peripheral protection unit
[11] Direct memory access
[12] Interrupt request/Non-maskable interrupt
[13] Clock extension peripheral
[14] Inter-IC sound/time division multiplexed
[15] Timer/counter/pulse-width modulation
[16] Embedded Multi-Media Card/Secure Digital High Capacity
[17] Serial memory interface
[18] Serial peripheral interface/Inter-integrated circuit/universal asynchronous receiver transmitter
[19] Local Interconnect Network - Universal asynchronous receiver transmitter
[20] Analog-to-digital converter

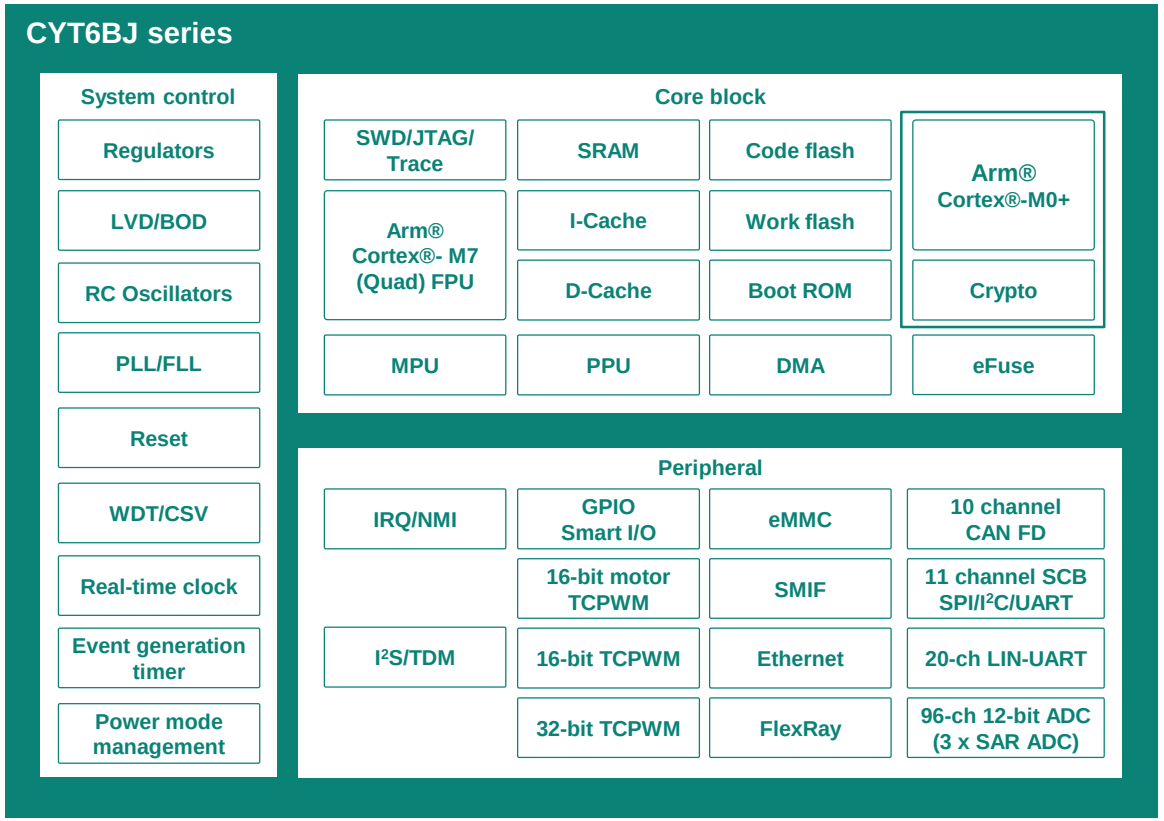
TRAVEO™ T2G CYT6BJ series

Applications

Body control module (BCM), gateway, and infotainment

Features

- **32-bit MCU core systems**
  - 320 MHz Arm® Cortex®-M7 Quad, I/D-cache
  - 16 MB flash, 512 KB work flash, 2048 KB SRAM, and Cortex®-M0+ for crypto
- **2.7 V to 5.5V supply voltages**
- **Interfaces**
  - Up to 10 channel CAN FD, up to 11 channel SCB, and 20 channel LIN-UART
  - eMMC, SMIF (QSPI/HS-SPI), up to 2 channel 10/100/1000-Mbit ethernet and FlexRay
  - I²S/TDM with TX 3 channel and RX 3 channel
- **AD converter**
  - Up to 96 channel, 12-bit with 3x successive approximation ADC (SAR ADC) units
- **Timers**
  - Up to 12 channel motor control, 87 channel 16-bit timer/counter/pulse-width modulation (TCPWM), and 16 channel 32-bit TCPWM
  - Event generation timer
- **High-speed I/O at 3.3 V in BGA**
  - Second operation voltage required for 3.3 V I/O
- **Packages**
  - 176-TQFP*, 272-BGA, 320-BGA
- **ISO 21434**
  - Compliance



*Contact Sales

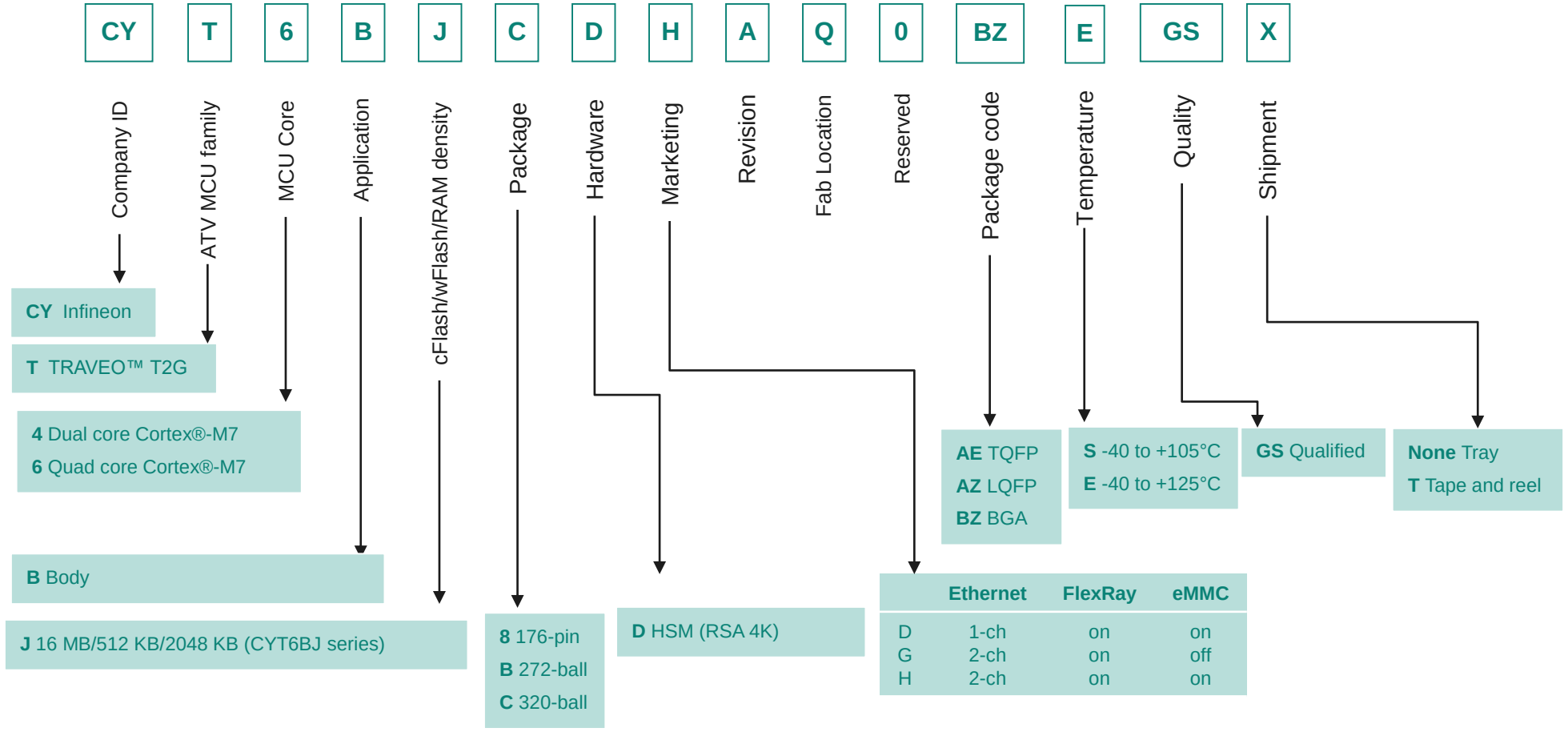
[TRAVEO™ T2G Body High CYT6BJ] portfolio – differentiation of product variants

Specifications	CYT6BJCDHBQ0BZxGS	CYT6BJBDHBQ0BZxGS	CYT6BJ8DDBQ0AExGS*
Technology	CMOS 40 nm	CMOS 40 nm	CMOS 40 nm
Supply voltage	2.7 V to 5.5V	2.7 V to 5.5V	2.7 V to 5.5V
Package	BGA 320	BGA 272	TQFP 176
CPU (Arm Cortex-M7 cores)	4	4	4
Code Flash	16,768 KB	16,768 KB	16,768 KB
Work Flash	512 KB	512 KB	512 KB
SRAM	2,048 KB	2,048 KB	2,048 KB
A/D converter	3 units, 96 ch	3 units, 96 ch	3 units, 77 ch
Serial Communication (SCB)	11 ch	11 ch	10 ch
LIN communication	20 ch	20 ch	17 ch
CAN FD commucation	10 ch	10 ch	10 ch
Ethernet	2 ch (10/100/1000 Mbps)	2 ch (10/100/1000 Mbps)	1 ch (10/100 Mbps)
TCPWM timer	16 bit x 87 ch 32 bit x 16 ch	16 bit x 87 ch 32 bit x 16 ch	16 bit x 87 ch 32 bit x 16 ch
Temperature grade	Ta = -40 to +105C/+125C	Ta = -40 to +105C/+125C	Ta = -40 to +105C/+125C

Supported shipment types are “Tray” (default) and “Tape and Reel”. Add the character ‘T’ at the end to get the ordering code for “Tape and Reel” shipment type.

*Contact Sales

Product naming nomenclature



Kits/boards overview

TRAVEO™ T2G B-H-16M 320 CPU Evaluation Board

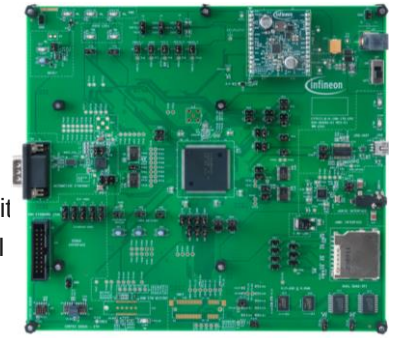
Key features.

- TRAVEO™ T2G CYT6BJ device (MCU) embedded.
- Supports up to 10 CAN FD channels
- Up to 20 independent LIN channels
- Two 10/100/1000 Mbps Ethernet MAC interfaces
- 64 Mbit HYPERRAM™ DRAM memory (S27KL) and 512 Mbit SEMPER™ octal flash memory (S26HL), 512 Mbit Dual QSPI NOR flash (S25HL)
- A mini-B type connector for an USB device interface
- Four different program/debug interfaces

TRAVEO™ T2G B-H-16M 176 CPU Evaluation Board

Key features.

- TRAVEO™ T2G CYT6BJ device(MCU) embedded.
- Support up to 10 CAN FD channels.
- Up to 17 independent LIN channels.
- One 10/100 Mbps Ethernet MAC interface
- 64 Mbits HYPERRAM DRAM memory (S27KL) and 512 Mbit SEMPER octal flash memory (S26HL) , 512 Mbit Dual QSPI NOR flash (S25HL)
- A mini-B type connector for an USB device interface.
- Four different program/Debug interface.



TRAVEO™ T2G B-H-16M Lite kit*

Key features.

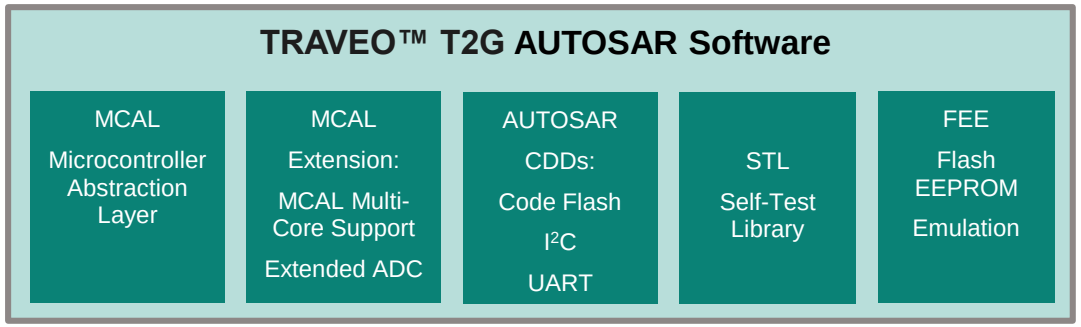
- TRAVEO™ T2G CYT6BJ device(MCU) embedded.
- Support up to 10 CAN FD channels.
- Up to 17 independent LIN channels.
- 10BASE-T1S interface form Multidrop Ethernet network
- Two 512 Mbit Dual QSPI NOR flash (S25HL)
- A USB type-C connector for an USB device interface.
- Onboard programmer/debugger - KitProg3.

*Contact Sales

Software offer

- AUTOSAR version 4.2.2 compliant (T2G Body)
- Development process ISO26262 audited/certified by TÜV Süd
- Developed as safety element out of context (SEooC) for ASIL B

- Functional safety documentation available
- Released for GHS compiler version 2024.1.4 + IAR EWARM FS 9.50.3
- EB tresos Studio v29.7.5 for TRAVEO™ included in delivery (node locked license)



✓Automotive Qualified Software

Ecosystem

Color Code	In development	On Request	Open Source

Toolsuite (Compiler with Debugger)

- **IAR EWARM FS**
- **Greenhills Multi**
- HighTec*
- TASKING* VX-toolset for Arm
- KEIL/ARM µVision*
- GCC (OSS)*

Toolsuite (Debugger)

- Lauterbach Trace32
- ModusToolbox™ (GCC)
- Segger
- LLVM (OSS)
- GDB (OSS)
- TASKING WinIDEA (ISYSTEM)

Communication

- ETH (Elektrobit) (Vector)
- Flexray (Elektrobit) (Vector)

Security

- Vector veHSM
- Intron/G-Pulse gHSM_TVII
- Fescaro
- Siemens
- ETAS CycurHSM
- GRC Crypto Stack
- Elektrobit ZenturHSM
- GHS/ISS FlexHSM

OS (Non ASR)

- PXROS (Hightec)
- SafeRTOS (Wittenstein)
- µVelocity (GHS)
- TockOS (OSS RUST)
- NuttX
- FreeRTOS
- Zephyr

AUTOSAR BSW

- Vector MICROSAR Classic
- Elektrobit tresos AutoCore
- ETAS RTA-CAR
- Siemens

Others

- dSpace TargetLink
- Elektrobit Lean HV
- ETAS ETK Calibration
- Vector VX1000 Calibration

*TRAVEO™ MCAL is not released for this compiler

ModusToolbox™ – ease of use

TRAVEO™ T2G Lite kits
KIT_T2G-B-H-16M_LITE
Fully supported by ModusToolbox™

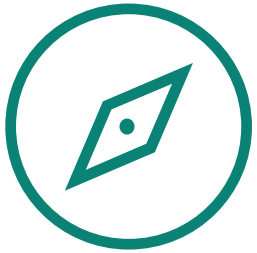
ModusToolbox™

ModusToolbox™ Software is a modern, extensible development ecosystem supporting a wide range of Infineon microcontroller devices, including **PSOC™ Arm® Cortex® microcontrollers**, **TRAVEO™ T2G Arm® Cortex® microcontroller**. Provided as a collection of development tools, libraries, and embedded runtime assets, ModusToolbox™ Software is architected to provide a flexible and comprehensive development experience

- User manual
- Getting started
- ModusToolbox™ product presentation
- GitHub
- Community support

Table of contents

Further resources



Product and application information

- TRAVEO™ T2G Body Product family page
- Automotive body electronics & power distribution applications

Reference designs & boards

- Evaluation board (Will available in Oct 2025)
- TRAVEO™ T2G AUTOSAR Software
- TRAVEO™ Software & Tools

Technical documents

- Application note and Technical reference manual
- MCU Power Estimator

Trainings

- online trainings


Infineon