Application Note, V 1.0, Feb. 2004

AP16012

(Thissens)

C16x

Bootstrap Loader - IDB / Half Duplex

Microcontrollers



Never stop thinking.

C16x

Revision History: Previous Version:		2004-02				
		-				
Page	Subjects (r	(major changes since last revision)				
All	Updated Layout to Infineon Corporate Design, updated release to 1.0 Content unchanged!					
	e entent an					

Controller Area Network (CAN): License of Robert Bosch GmbH

We Listen to Your Comments

Any information within this document that you feel is wrong, unclear or missing at all? Your feedback will help us to continuously improve the quality of this document. Please send your proposal (including a reference to this document) to: mcdocu.comments@infineon.com



Edition 2004-02-01 Published by Infineon Technologies AG 81726 München, Germany © Infineon Technologies AG 2006.

All Rights Reserved.

LEGAL DISCLAIMER

THE INFORMATION GIVEN IN THIS APPLICATION NOTE IS GIVEN AS A HINT FOR THE IMPLEMENTATION OF THE INFINEON TECHNOLOGIES COMPONENT ONLY AND SHALL NOT BE REGARDED AS ANY DESCRIPTION OR WARRANTY OF A CERTAIN FUNCTIONALITY, CONDITION OR QUALITY OF THE INFINEON TECHNOLOGIES COMPONENT. THE RECIPIENT OF THIS APPLICATION NOTE MUST VERIFY ANY FUNCTION DESCRIBED HEREIN IN THE REAL APPLICATION. INFINEON TECHNOLOGIES HEREBY DISCLAIMS ANY AND ALL WARRANTIES AND LIABILITIES OF ANY KIND (INCLUDING WITHOUT LIMITATION WARRANTIES OF NON-INFRINGEMENT OF INTELLECTUAL PROPERTY RIGHTS OF ANY THIRD PARTY) WITH RESPECT TO ANY AND ALL INFORMATION GIVEN IN THIS APPLICATION NOTE.

Information

For further information on technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies Office (www.infineon.com).

Warnings

Due to technical requirements components may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies Office.

Infineon Technologies Components may only be used in life-support devices or systems with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system, or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body, or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.



Table of Contents

Table of Contents			
1	Identification Byte (IDB) / Half Duplex Mode	4	



Identification Byte (IDB) / Half Duplex Mode

1 Identification Byte (IDB) / Half Duplex Mode

No Half Duplex Mode support

Some members of the C166 family do not support half duplex mode directly during bootstrap loader mode. The problem is that the receiver of the ASC0 is enabled during transmission of the IDB from the ASC0 to the host. Because of the connection of TxD0 and RxD0 (done by external hard ware in half duplex mode) the IDB is received as the first byte of the 32bytes which are expected from the bootstrap loader sent by the host. In despite of this behaviour the half duplex mode can be used with the devices of the C166 family. It is only necessary to complete the IDB to an instruction (IDB instruction). This can be done in the following way:

Table 1 IDB instructions

Chip	IDB	additional bytes	IDB instruction
C165	B5h	4Ah, B5h, B5h	EINIT
C166	55h	RR, MM, MM	XORB op1, op2
C167	A5h	5Ah, A5h, A5h	DISWDT

This workaround reduces the number of usable bytes for a preloader from 32bytes to 28bytes because 4bytes are necessary for the 'IDB instruction'.



Identification Byte (IDB) / Half Duplex Mode

Half Duplex Mode support

For the C166 family devices which support half duplex mode the described workaround is not necessary (not allowed), because the receiver of the ASC0 is disabled during transmission of the IDB. Therefore it is possible to use half duplex mode.

Chip	Step	IDB	Activation	Half Duplex support
80C166/83C166	since CB	55h	ALE+NMI#	No
88C166	since ES1-BA	55h	ALE+NMI#	No
SAx-C167	AC	A5h	P0L.3 + NMI# (see Errata Sheet)	No
SAx-C167	AD	A5h	P0L.4	No
SAx-C167	since BA	C5h	P0L.4	Yes
SAx-C167CW	all Steps	A5h	P0L.4	No
SAx-C167S-4RM	all Steps	C5h	P0L.4	Yes
SAx-C167SR-LM	all Steps	C5h	P0L.4	Yes
SAx-C167CR-LM	all Steps	C5h	P0L.4	Yes
SAx-C167CR- 16F	all Steps	C5h	P0L.4	Yes
SAx-C165	AA to BB	B5h	P0L.4	No
SAx-C165	since CA	B5h	P0L.4	Yes

Table 2 C166 Bootstrap Loader versions

http://www.infineon.com