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#	Version	Date	Item group	Detailed description	Note
1	1.0	26-Nov-2013	Wizard page 2 + 4	Relevant for TDA5240/35/25:  IFATT setting in Double / Single Down Conversion mode (DDC/SDC).  DDC requires external ceramic filter, SDC needs no CER filter, therefore DDC allows a wider IF attenuation (IFATT) range compared to SDC (see wizard page 4 or datasheet register RFC).  When changing from DDC to SDC (on wizard page 2) this different IFATT range needs to be kept in mind and invalid IFATT settings in SDC need to be corrected on wizard page 4 manually by the user.	
2	1.0		Wizard page 2	Relevant for TDA5240/35/25: Depending on:  1. the selected RF receive frequency 2. the conversion mode (DDC or SDC) and 3. the LO injection side (High side or Low side LO injection) there might be the possibility that spur frequencies get down converted into the wanted IF frequency band.  If this is the case, then there is the theoretical possibility that sensitivity can be degraded by a few dB for certain RF receive frequencies. In case of such possible degradation, this is shown as warning information on wizard page 2. The warning information typically tells that for a given down conversion mode (DDC or SDC) the LO injection side shall be changed. Such a change of the LO injection side needs to be done manually by the user on wizard page 2.	
3	1.0	26-Nov-2013	Wizard page 5 + 10	Relevant for TDA5240/35/25: When AFC is activated and AFC checkbox "Auto Configure Filter" is enabled (on wizard page 10), then the K1 and K2 factors should get calculated according to formula given in the tootip for "Settling Time". Unfortunately, when datarate gets changed on wizard page 5, then K1 and K2 values on wizard page 10 are NOT automatically updated by the tool, but K1 and K2 values need to be adapted manually by the user by one of the following methods:  a) Deactivate AFC (AFC StartConfiguration="OFF") and activate AFC again (AFC Start Configuration = "Direct ON") or b) Disable AFC checkbox "Auto Configure Filter" and enable checkbox again.	
4	1.0	26-Nov-2013	Wizard page 6	Relevant for TDA5240/35:  Data Rate Acceptance Limitation ("DRAL") shall <u>not</u> be used in "Run Mode Slave". See appropriate Appnote on our product homepage.	
5	1.0	26-Nov-2013	Wizard page 9	Relevant for TDA5240/35 in case FFB or UFFB gets used: In case AFC is activated, an AFC settling time of at least 1 bit needs to be kept in mind for wide PDF filter settings. In case of small PDF (like 93kHz or smaller) a settling time of at least 4 bit is required. This AFC settling time needs to be considered for the SYSRCTO register and WULOT register value. Further details on these calculations can be taken from our "SP_Timings" Appnote XLS sheet on our product homepage.	
6	1.0	26-Nov-2013	Registers	Relevant for TDA5235: In TDA5235 Explorer B12.6.51 the write register BPFCALCFG1 was mistakenly defined as read register and will get a wrong value (typically 0x00, which is wrong), when pressing the "Read Registers" button. BPFCALCFG1 register shall always contain the reset value 0x04. Please check your config file. In case BPFCALCFG1 register shows a value of 0x04 or in case BPFCALCFG1 register is missing in the generated config file (means reset value 0x04 will be taken), then everything is fine. In case BPFCALCFG1 register shows any other value, please correct your config file by putting the reset value 0x04.	
7	1.0	26-Nov-2013	Wizard page 9	Relevant for TDA5240/35 in case SelfPollingMode (SPM) gets used: In case SignalRecognition criterion is used, then a "SigRec on time in 1/16 Bits" value of 8 shall be chosen by default for all activated RF channels.	
8	1.0		Wizard page 9	Relevant for TDA5240/35 in case SelfPollingMode (SPM) gets used: Values for the <b>Self Polling Timing</b> on wizard page 9 (On time, Off time, WULOT, SYSRCTO, Totim_SYNC) can be calculated using our "SP_Timings" Appnote XLS sheet on our product homepage.	
9	1.0	26-Nov-2013	Wizard page 5+10	Relevant for TDA5240/35/25:  AFC shall <u>not</u> be used, when receiver is configured to ASK reception (AFC behaves unpredictable during ASK "ZERO"-chips, since there is no wanted signal available to tune to proper RX frequency).	