Driving decarbonization and digitalization. Together.



Staff Engineer - Digital Functional Verification

Job description

Are you an entrepreneurial thinker, creative problem solver and like to drive things forward? Your talent as a networker makes it easy for you to work with many different types of people? At Infineon we have the size, the competence and the faith in innovation to shape the power semiconductor industry. Seize the chance to come on board and become part of our success story!

In your new role you will:

- Define the verification concept for new IP/SoCs
- Be in charge of developing SV-UVM-based environments
- Develop and review verification specification
- Debug failing tests and analyse the root-cause
- Be in charge of **monitoring and reporting** the verification status
- Perform and evaluate verification regressions and ensure high-quality deliveries

Profile

You are best equipped for this task if you have:

- A degree in Electrical/Electronic Engineering, Computer Science or equivalent
- More than 5 years of experience within the semiconductor industry
- Knowledge of System Verilog and UVM verification methodology and complex module test benches
- Experience in using digital simulators like Cadence XCelium, knowledge in script languages (Perl, Python, etc.) and data management systems (Clearcase, GIT, etc.)
- Fluent English communication skills

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

At a glance

Location:

Job ID: **HRC0191497**

Start date: as soon as possible

Entry level: **3-5 years**Type: Full time
Contract: Permanent

Apply to this position online by following the URL and entering the Job ID in our job search. Alternatively, you can also scan the QR code with your smartphone:

Job ID: HRC0191497

www.infineon.com/jobs



