Driving decarbonization and digitalization. Together.



Senior Manager Flow, Methodology and Infrastructure (f/m/div)

Job description

Join us as a Team Lead and take charge of a dynamic team providing Physical Verification Solutions to Infineon's design and layout community. Envision and implement cutting-edge methodologies for Physical Verification, Parasitic Extraction, and ESD Verification, while serving as the liaison between internal development groups, project managers, and external EDA software providers. With a focus on professional team management, performance assessment, and resource pipeline balancing, you will lead your team to achieve quality, cost, scope, and timeline targets. Represent your team in cross-site methodology exchanges and collaborations, and be part of a globally renowned semiconductor industry leader. Are you ready to lead the way?

In your new role you will:

- Manage a team that provides Physical Verification Solutions to the whole design and layout community in Infineon
- Envision, develop and roll-out methodologies for Physical Verification, Parasitic Extraction and ESD Verification
- Take over ownership and steer projects with your energetic and charismatic leadershipstyle
- Be the interface to internal development groups, project managers and external EDA software providers
- Be responsible for full professional team management, i.e. resource pipeline balancing, team coordination, team development, performance assessment and hiring
- Be accountable for quality, cost, scope, and timeline targets that are to be achieved by your team
- Represent your team in cross site methodology exchange and collaborations

Profile

You contribute to a culture of an open and honest dialogue and feedback while feeling responsible for your own doing and decisions. You accept responsibility for decisions and for their positive or negative outcomes.

You are best equipped for this task if you have:

 A university degree in Electrical Engineering, Computer Science, or similar technical field

At a glance

Location: Munich (Germany)

Job ID: HRC0698681

Start date: Jun 01, 2024

Entry level: 5+ years
Type: Full time
Contract: Permanent

Apply to this position online by following the URL and entering the Job ID in our job search. Alternatively, you can also scan the QR code with your smartphone:

Job ID: HRC0698681

www.infineon.com/jobs



Contact

Daniel Lichtblau

Senior Specialist Recruiting



- At least 6 years of professional experience in the semiconductor industry in relevant R&D positions
- Energetic communication skills in combination with a positive mindset
- Experience in Product Development, Physical Verification or Analog Circuit Design
- Sound knowledge in your own technical area of expertise and a focus on management and coordination skills
- Remarkable presentation skills which enable you to master the alignment across internal and external contacts in a multicultural and international environment
- Highly motivated with ability to prioritize and perform under pressure
- Excellent English skills, german would be a plus

Benefits

• Munich: Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; On-site creche and kindergarden with 220 spots, open until 5:30pm; Holiday child care; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis & soccer court; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement; Performance bonus; Reduced price for public transport and very own S-Bahn station; Access for wheelchairs; Possibility to work remotely from abroad (EU)

Why Us

Driving decarbonization and digitalization. Together.

Infineon designs, develops, manufactures, and markets a broad range of semiconductors and semiconductor-based solutions, focusing on key markets in the automotive, industrial, and consumer sectors. Its products range from standard components to special components for digital, analog, and mixed-signal applications to customer-specific solutions together with the appropriate software.

The central R&D organization "Design Enabling and Services" (DES) provides the design environments (design-systems) to the different product development teams at Infineon. With leading edge design methods, complex building blocks and a wide range of product development services DES enables Infineon's advanced IC development. This means the complete value creation chain from abstract system models down to the fully verified product layout which ensures high quality manufacturing readiness.

We are on a journey to create the best Infineon for everyone.

This means we embrace diversity and inclusion and welcome everyone for who they are. At Infineon, we offer a working environment characterized by trust, openness, respect and tolerance and are committed to give all applicants and employees equal opportunities. We base our recruiting decisions on the applicant´s experience and skills.

We look forward to receiving your resume, even if you do not entirely meet all the requirements of the job posting.

Please let your recruiter know if they need to pay special attention to something in order to enable your participation in the interview process.

Click here for more information about Diversity & Inclusion at Infineon.

